

Hargun Suri
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EDUCATION:

University of Toronto, Toronto, Canada.

- Completed **Computer Engineering** in May '13.
- **Deans Honor List** 2009-2010

St Francis De Sales School, New Delhi, India

- Graduated with an average of **87%**.

Technical Skills:

- Proficient in **C, C++, Java, HTML, XML, PHP, SCHEME, PROLOG, VBA, Python** and **SQL**.
- Applications: **MS EXCEL, MS Visual Studios**

Management Skills:

- Successfully completed **Entrepreneurship Series** at **University of Toronto**.

Experience:

Co-Founder and Web Developer, Interngration.com.

(May'12 – Present)

- Conceptualized, designed and promoted Interngration.com: Interngration is a web-based application that is aimed at providing transparency between interns and companies. Students are able to view job reviews, interview question and salary information.
- Currently many companies have shown interest in this web application as it provides their HR department, unbiased reviews from students and an alternate platform to reach out to students.
- Developed the back-end of the web application using **PHP** and **SQL**. Also responsible for communicating with, and promoting the application to universities and companies.

Co-Founder and App. Developer, Eventster, Capstone Design Project - University of Toronto.

(Aug'12 – Present)

- Conceptualized and currently designing Eventster: Eventster is a conference management Android application geared towards the general public and MNCs. This app will allow conference coordinators to create an event and upload relevant information for attendees.
- Responsible for the back-end of the application using **SQL, PHP** and also the front-end designing using **Android SDK, JAVA** and **JSON**.

Component Engineer, Advanced Micro Devices, Canada.

(Jan '12 - Aug '12)

- Responsible for communicating with vendors to source for board components, this provided AMD with alternate vendors at competitive prices.
- Managed and updated AMD's internal components database – **E-MATRIX**.
- Designed an interface using **VBA** to increase output efficiency, while performing component comparisons.

Reliability and Compliance Engineer, Advanced Micro Devices, Canada.

(May '11 - Aug '12)

- Performed 3-metre electromagnetic and electrostatic discharge testing on graphical processing units. Also, debugged and provided solutions for failing boards too conform with the European and US radiation limits.
- Performed simulations during design phase, thus targeting issues before board production.
- Responsible for constant communication with clients in Europe and US for electromagnetic radiations support.
- Updated the lab test software, this improved lab efficiency by 12% and reduced testing errors.

Personal:

- Winner of the **City of Toronto's Excellence Award '13** in entrepreneurship.
- Finalist for **Hacker UofT** incubator program, because of work done on Interngration.com.
- Travelled and lived across **US, Canada** and **India**.