

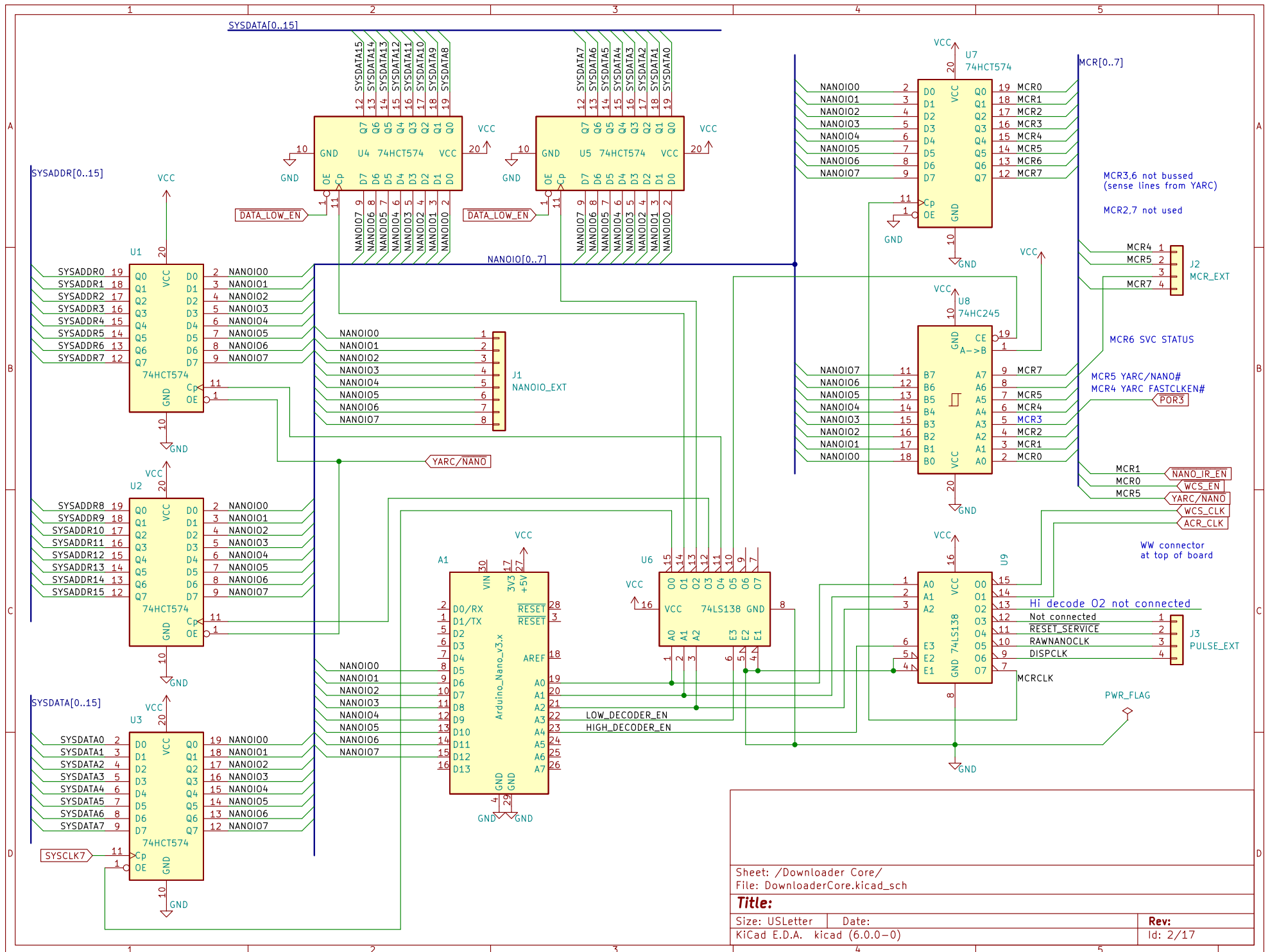
Floorplan

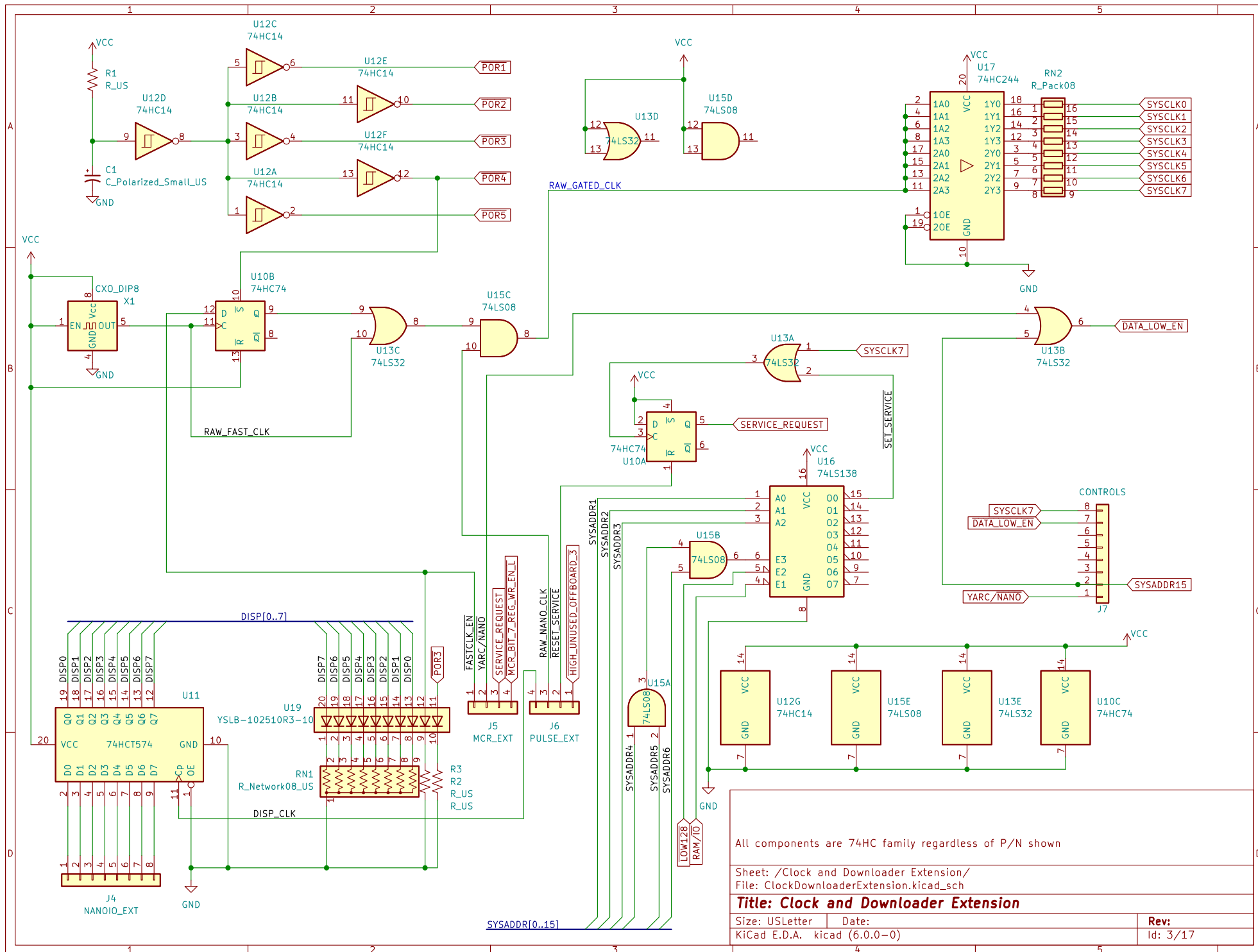
Sheet: /
File: YARC.kicad_sch

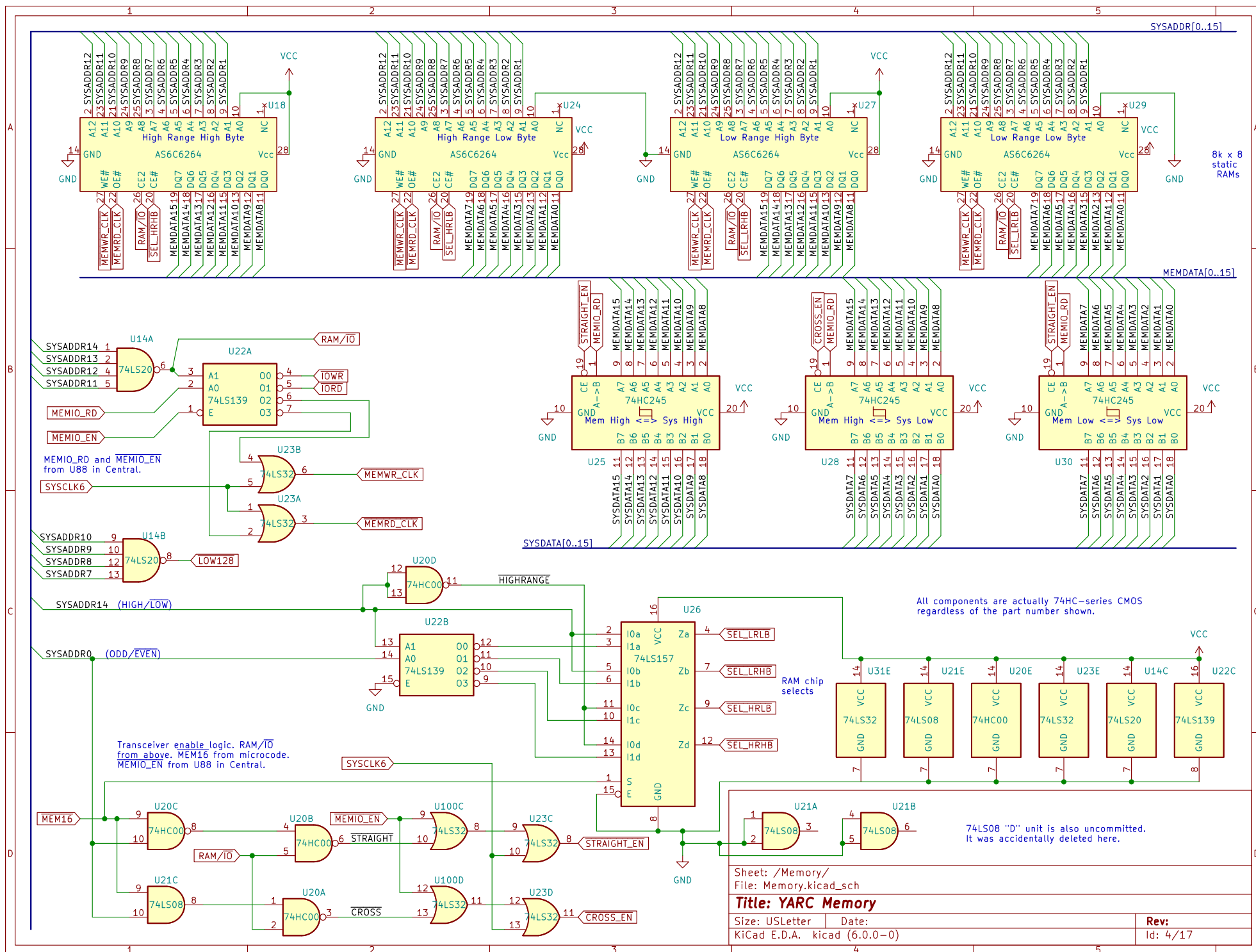
Title: YARC

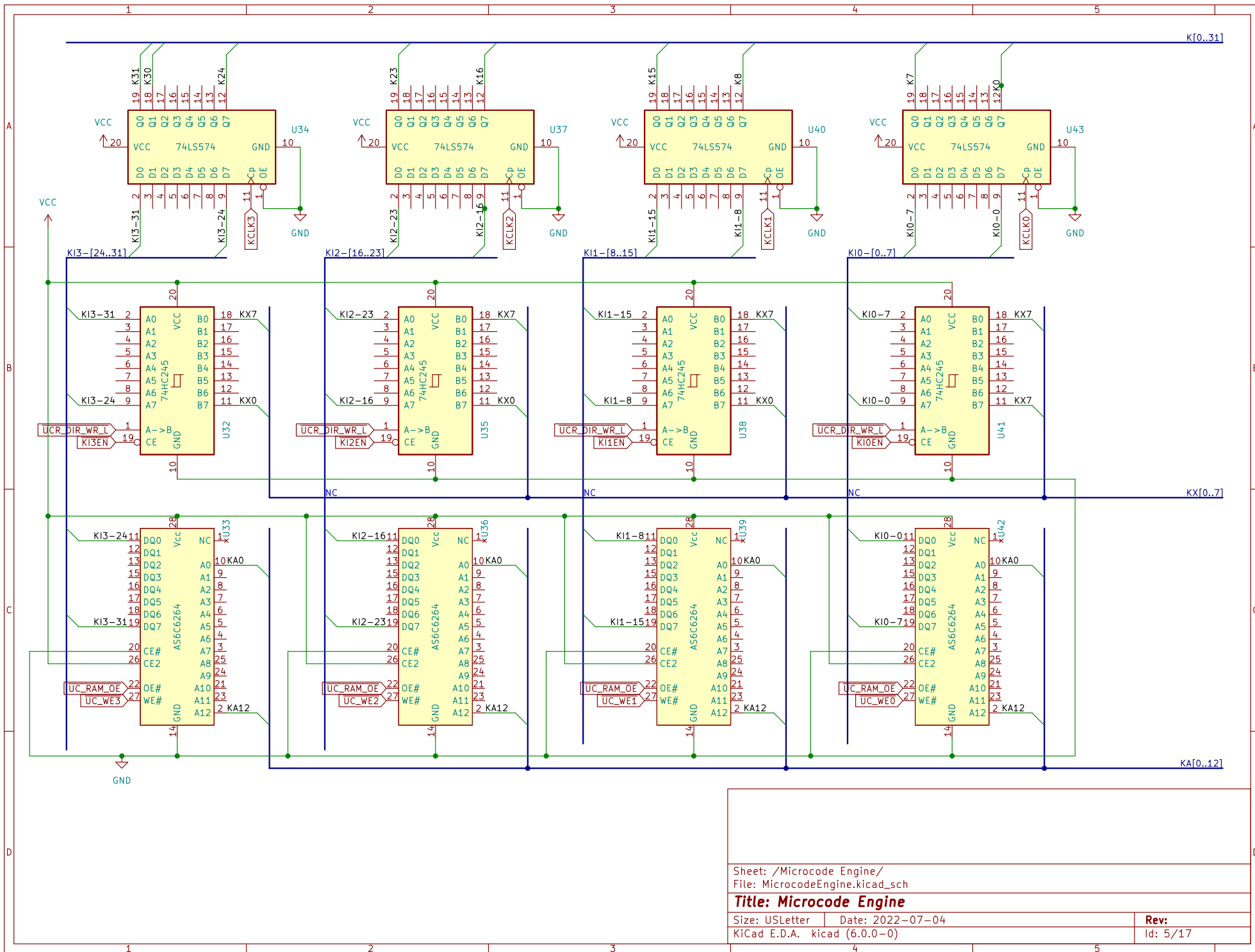
Size: USLetter Date: 2022-01-12
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 1/17







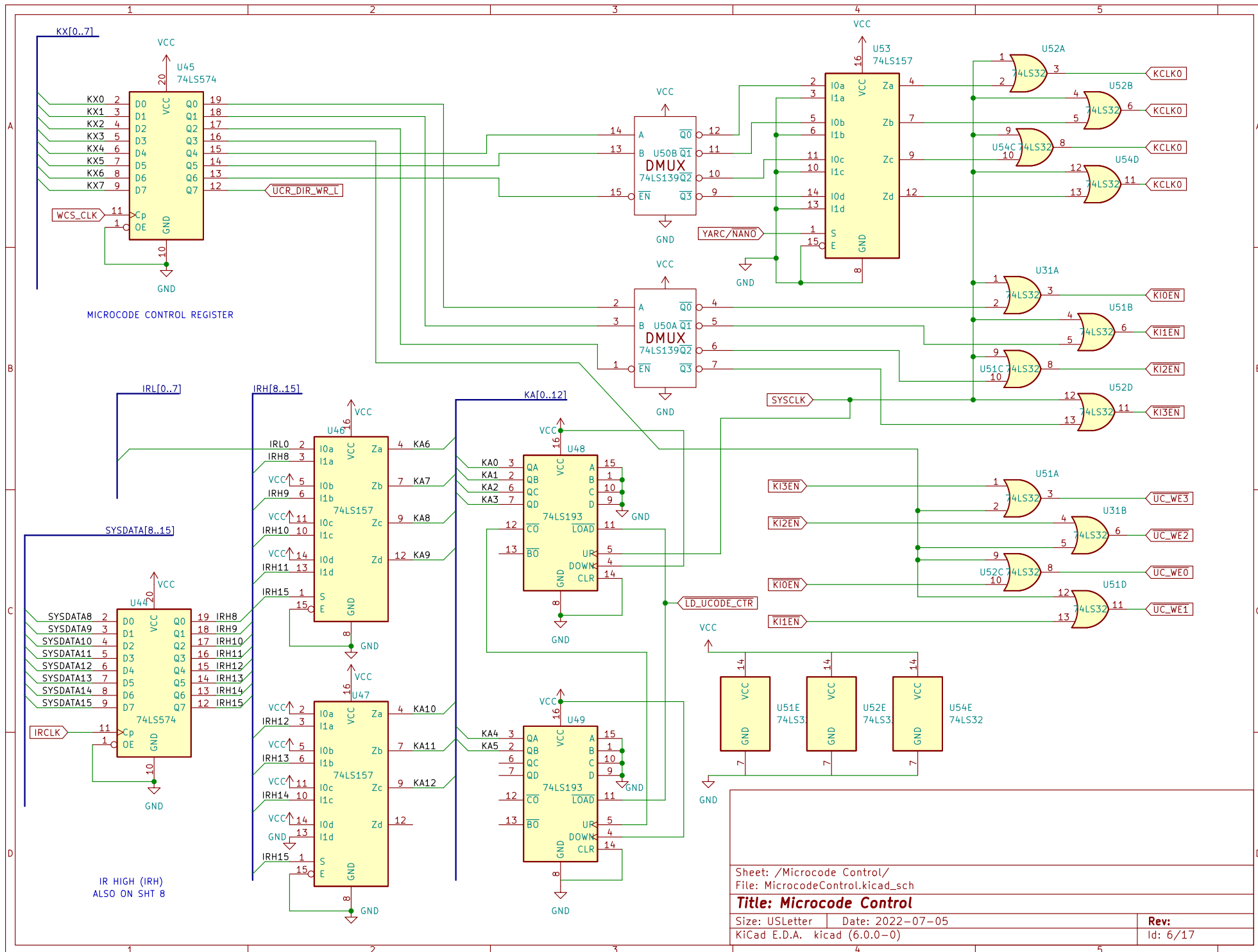


Sheet: /Microcode Engine/
File: MicrocodeEngine.kicad_sch

Title: Microcode Engine

Size: USLetter | Date: 2022-07-04
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 5/17

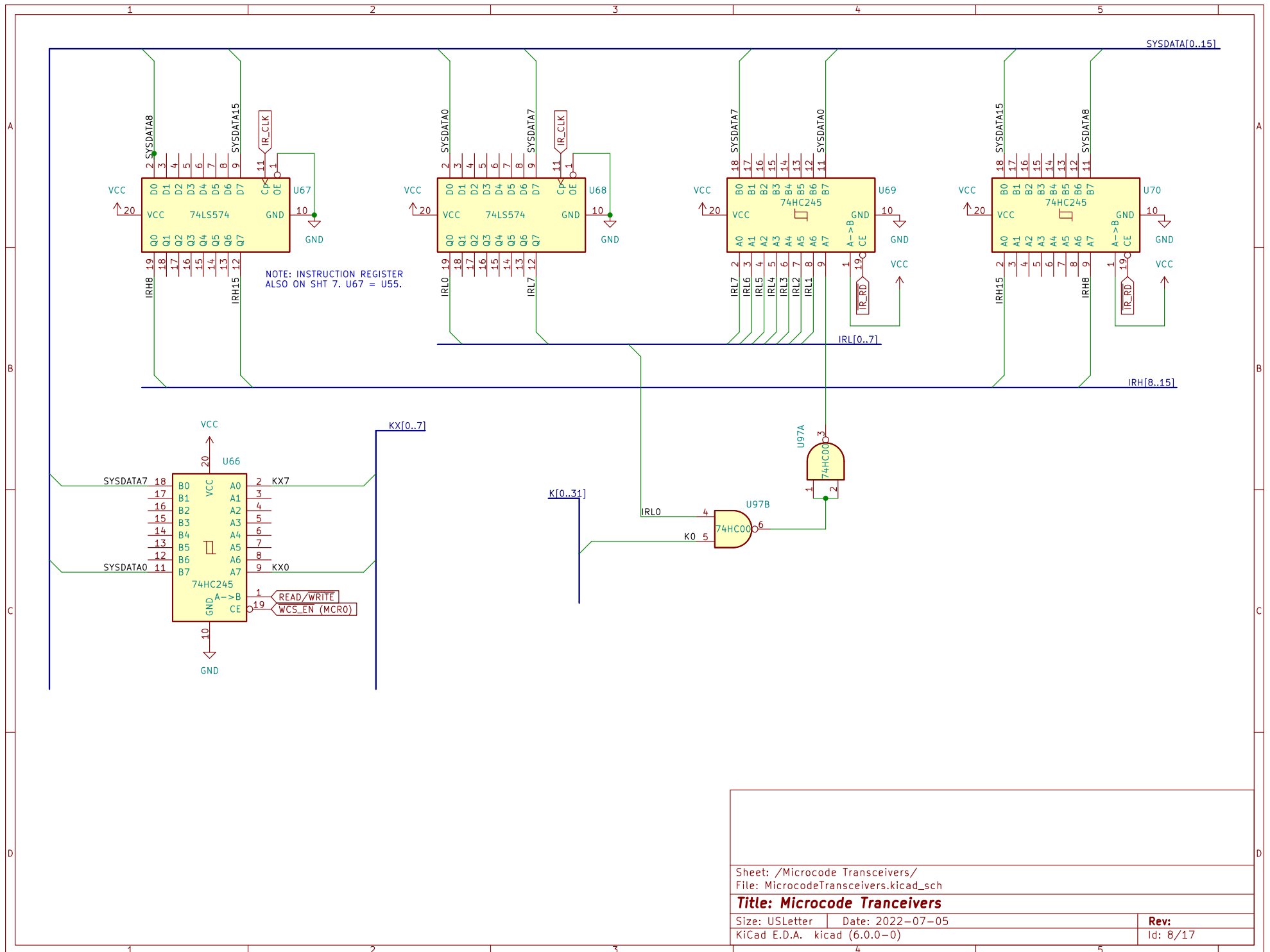


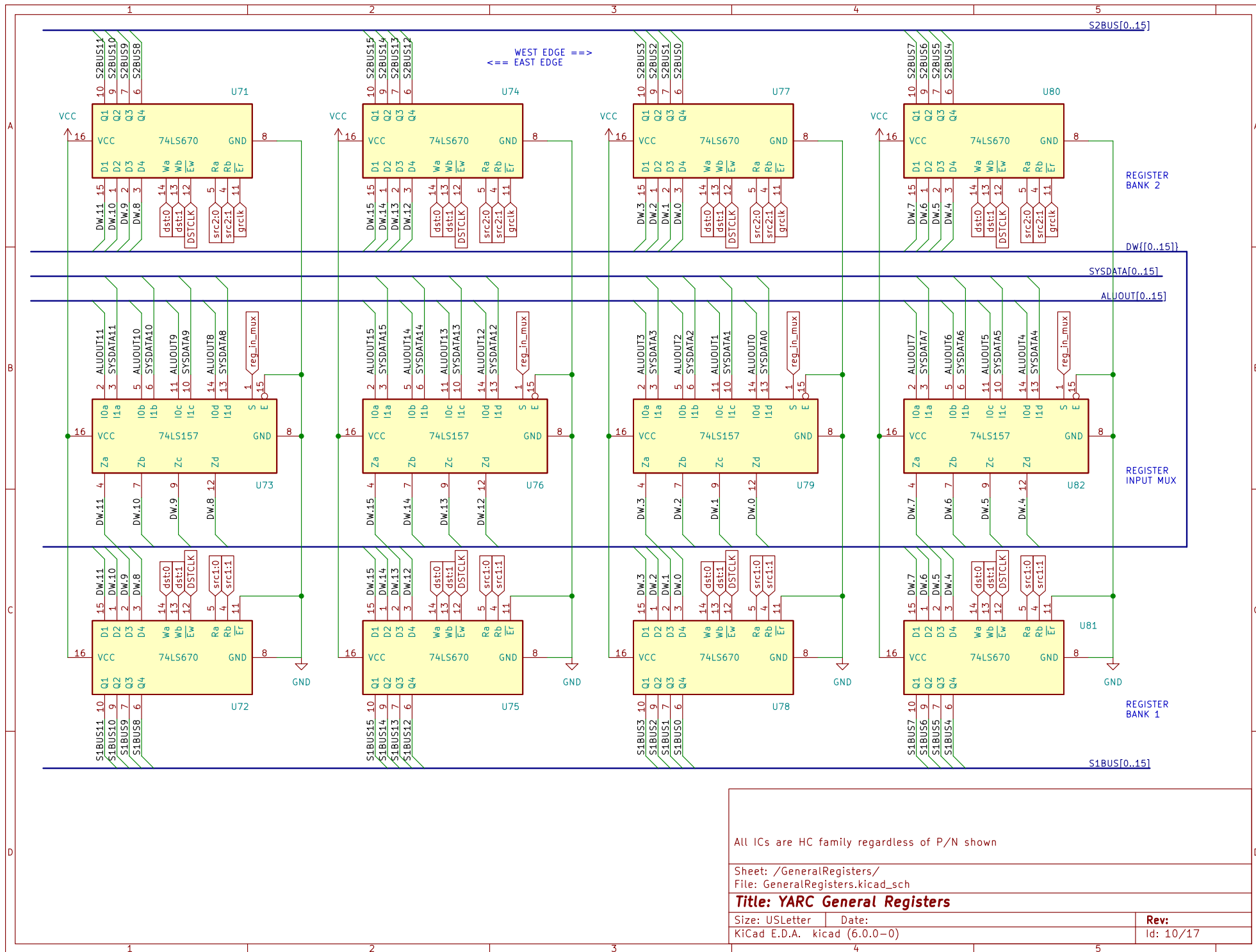
Sheet: /Microcode Control/
File: MicrocodeControl.kicad_sch

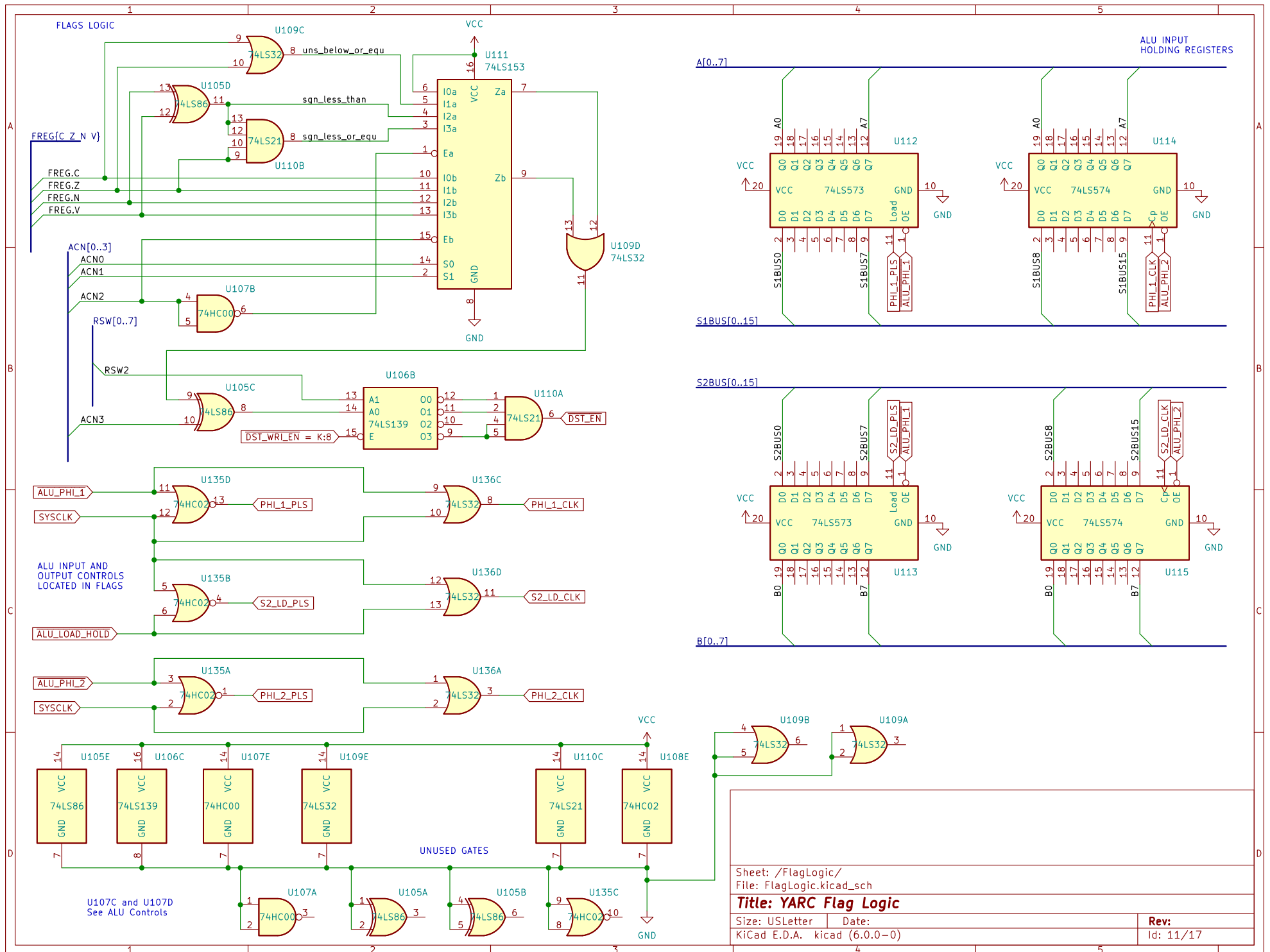
Title: Microcode Control

Size: USLetter Date: 2022-07-05
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 6/17





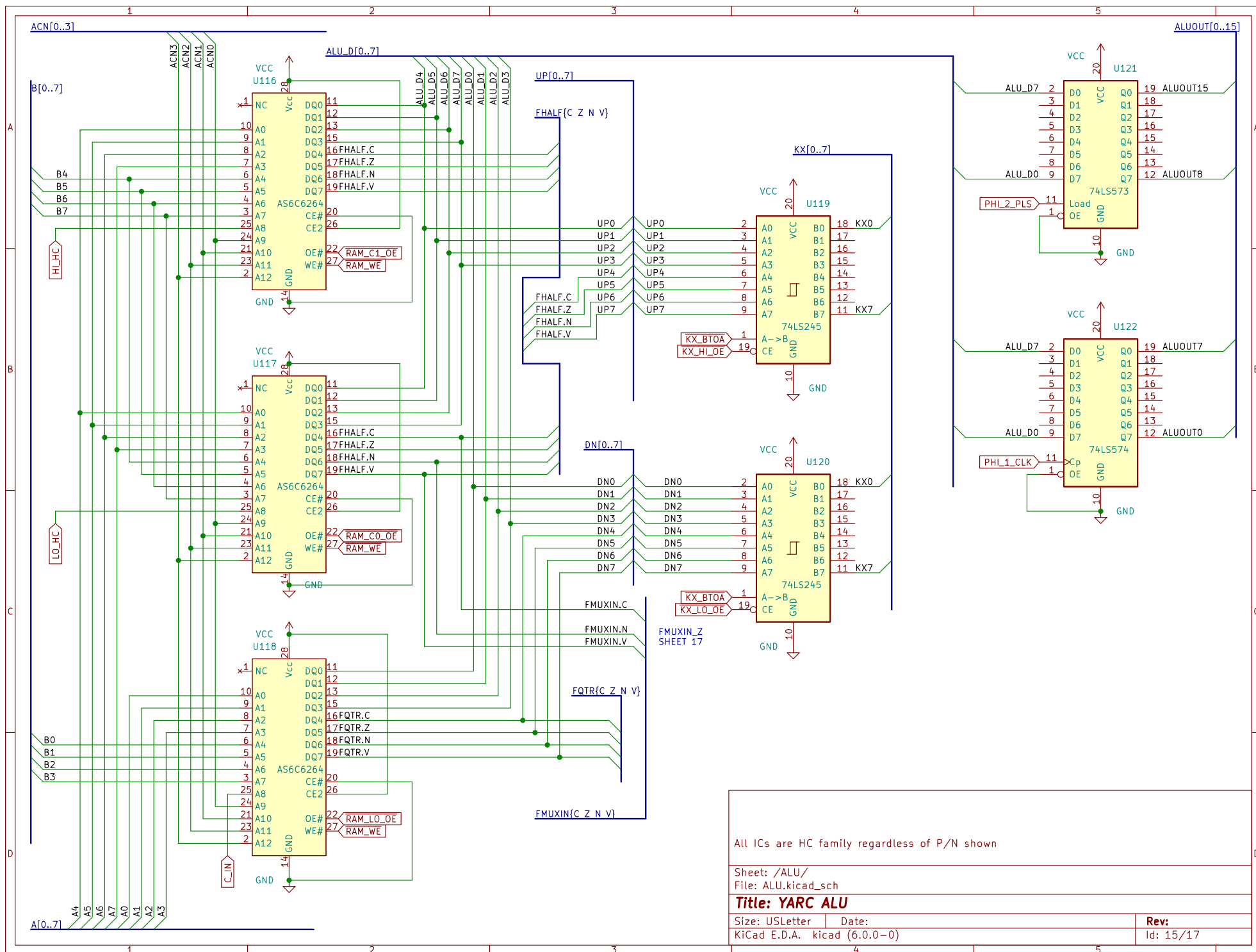


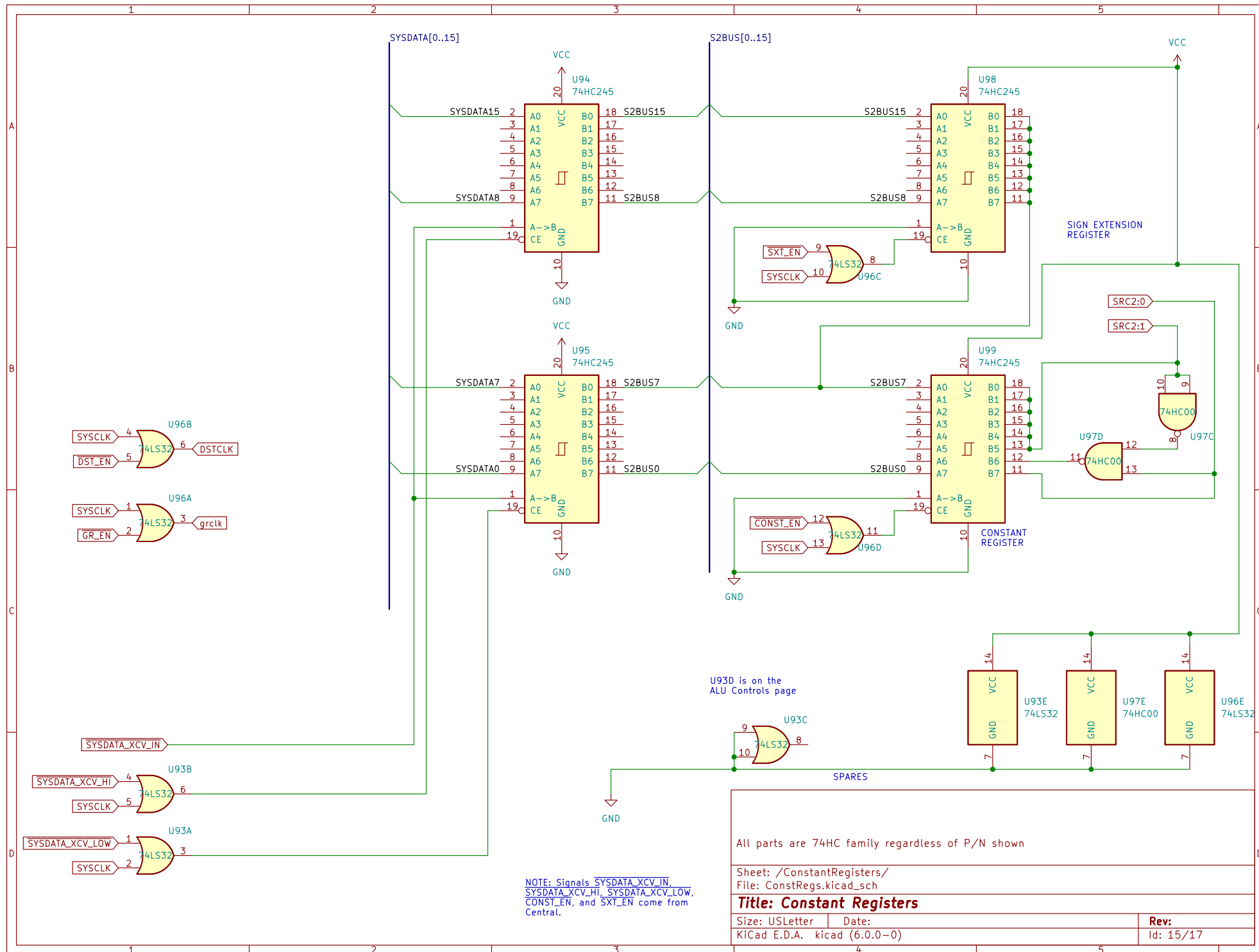
Sheet: /FlagLogic/
File: FlagLogic.kicad_sch

Title: YARC Flag Logic

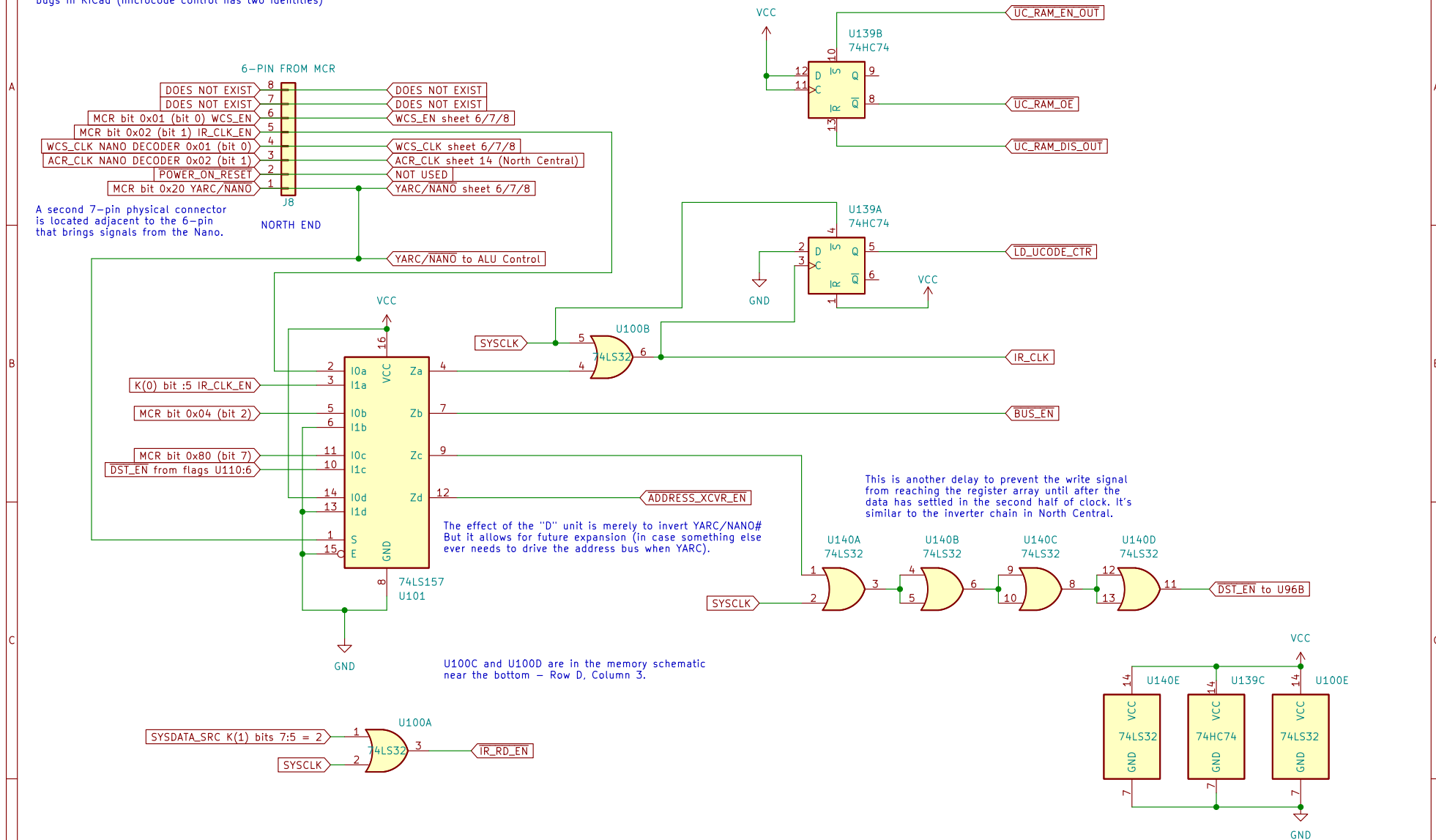
Size: USLetter Date:
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 11/17





We cannot distinguish sheets 6, 7, or 8 because of bugs in KiCad (microcode control has two identities)



This sheet summarizes the use of the YARC kludge space, which is located north of the main system memory. Some logic from the kludge space appears on other sheets.

Sheet: /KludgeSpace/

File: KludgeSpace.kicad_sch

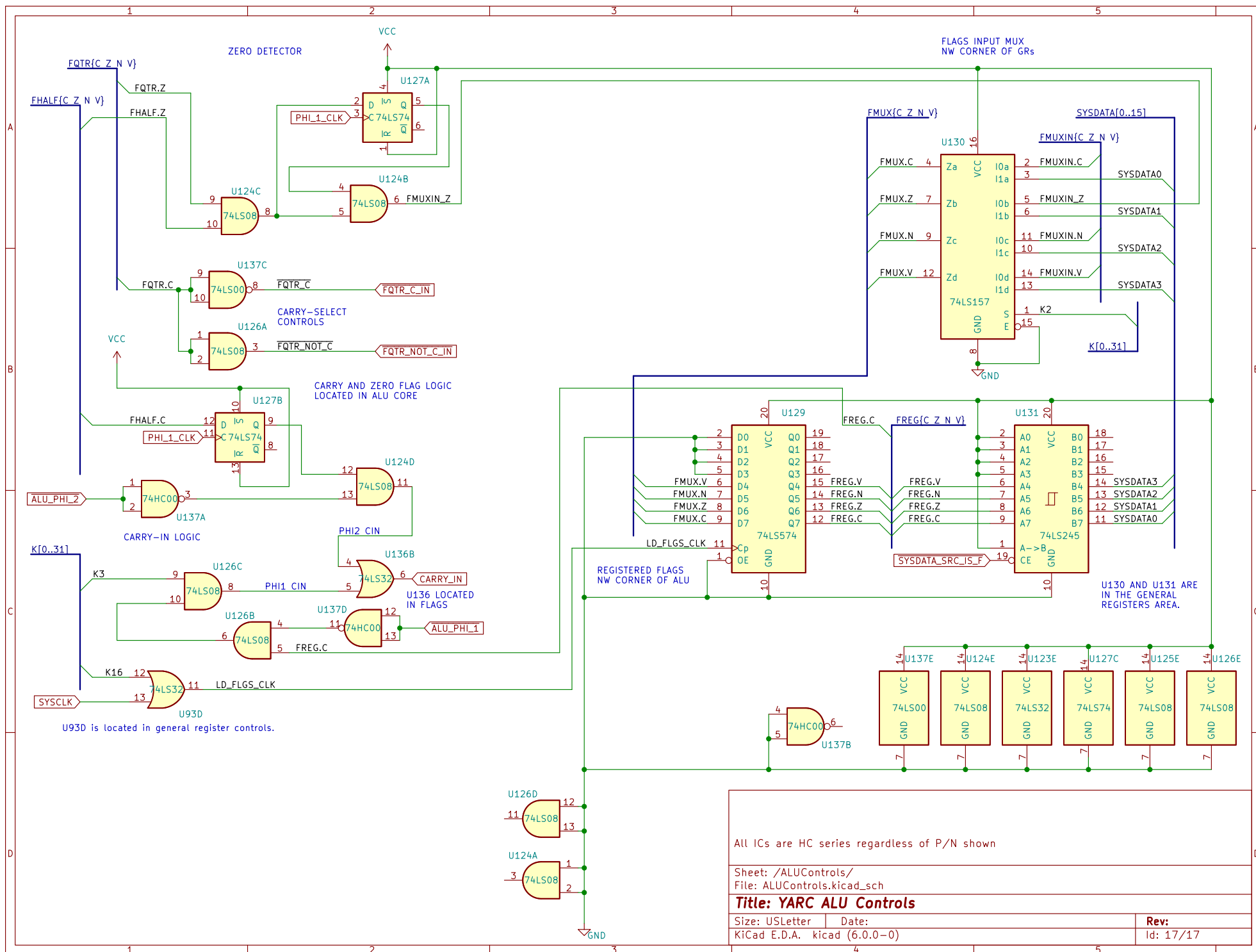
Title: YARC Kludge Space

Size: USLetter	Date:
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Size: 32Kb	Date:
KiCad E.D.A. kicad (6.0.0-0)	

Rev:

Id: 16/17



All ICs are HC series regardless of P/N shown

Sheet: /ALUControls/
File: ALUControls.kicad_sch

Title: YARC ALU Controls

Size: USLetter Date:
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 17/17