



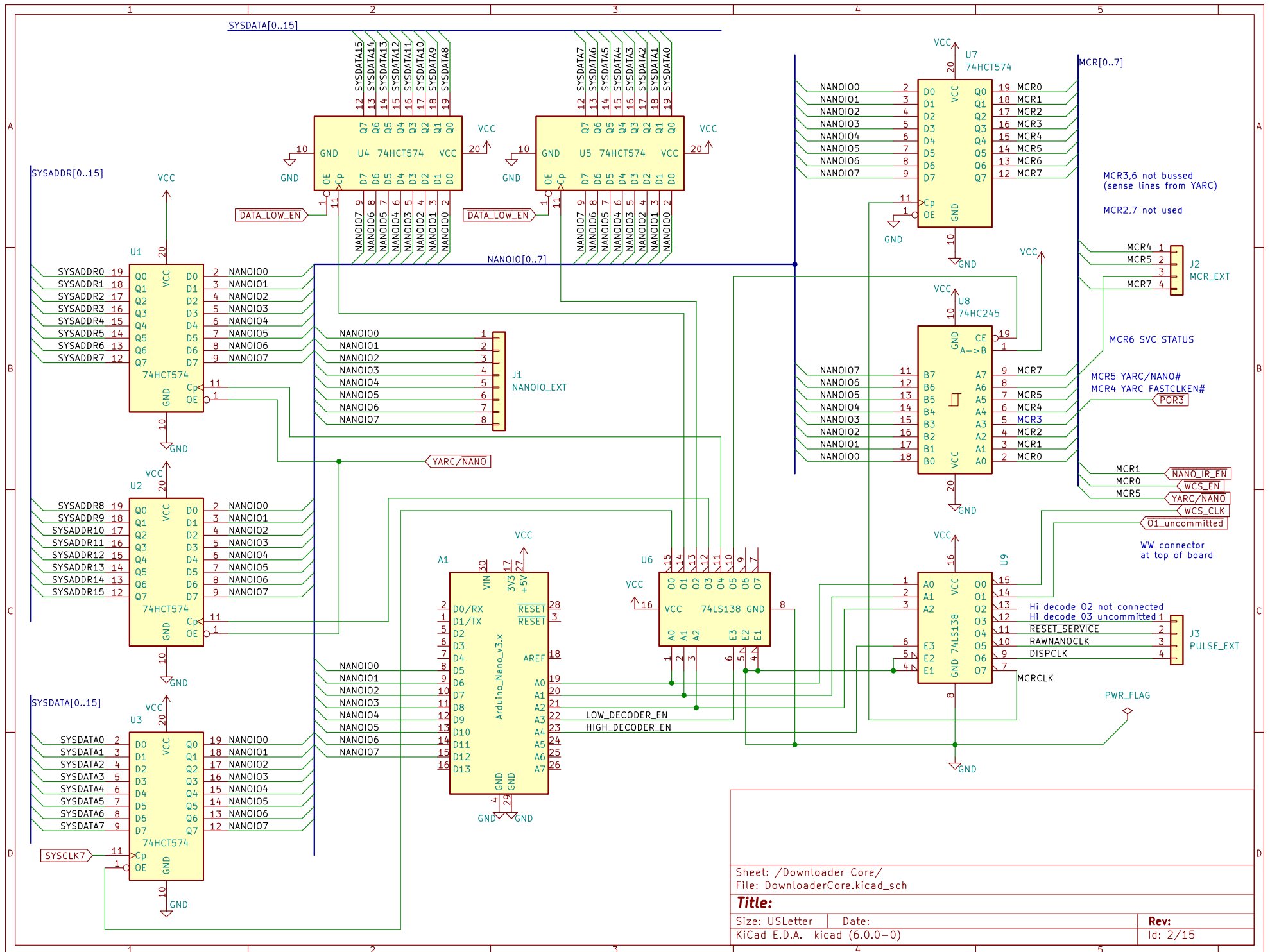
Floorplan

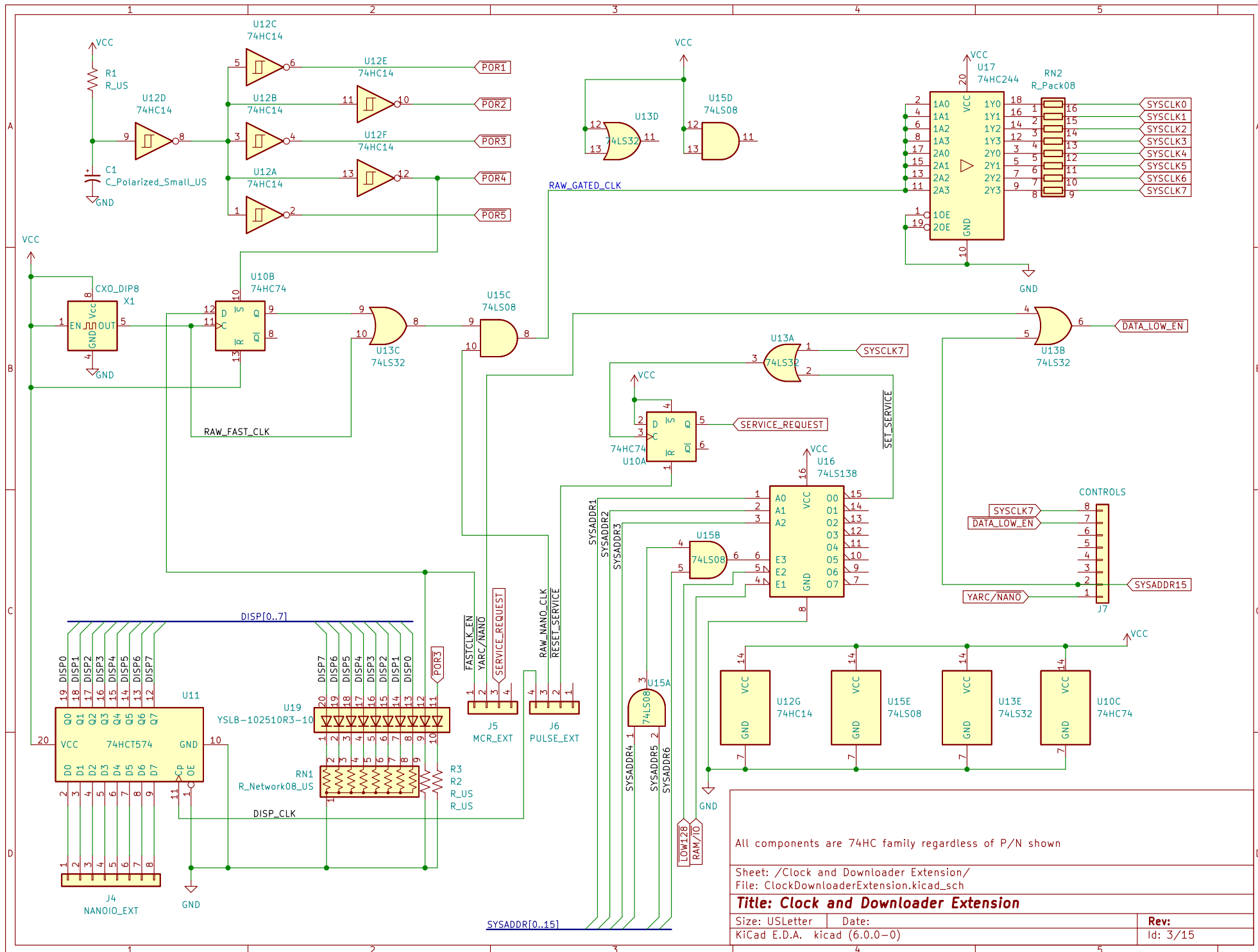
Sheet: /
File: YARC.kicad_sch

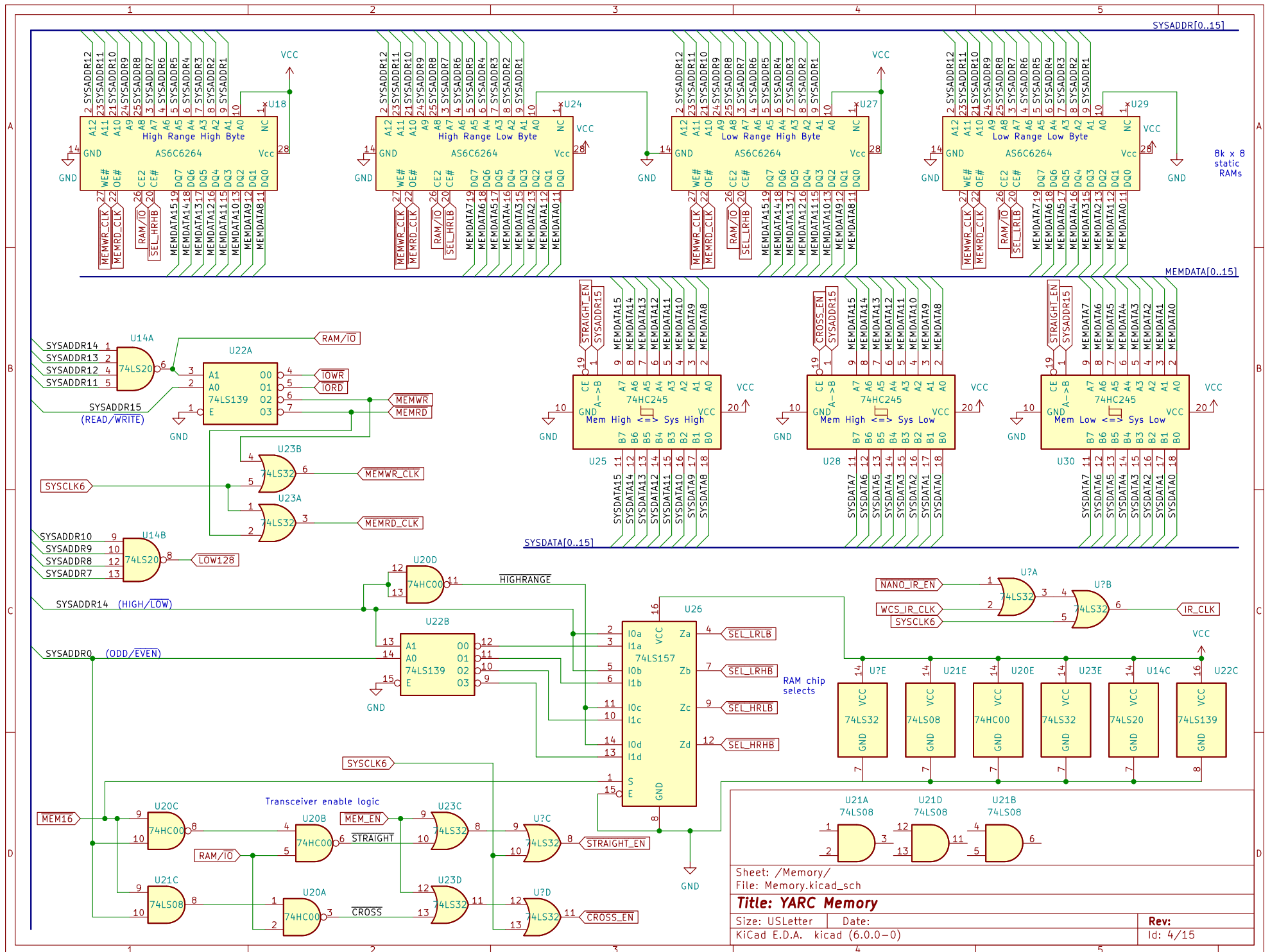
Title: YARC

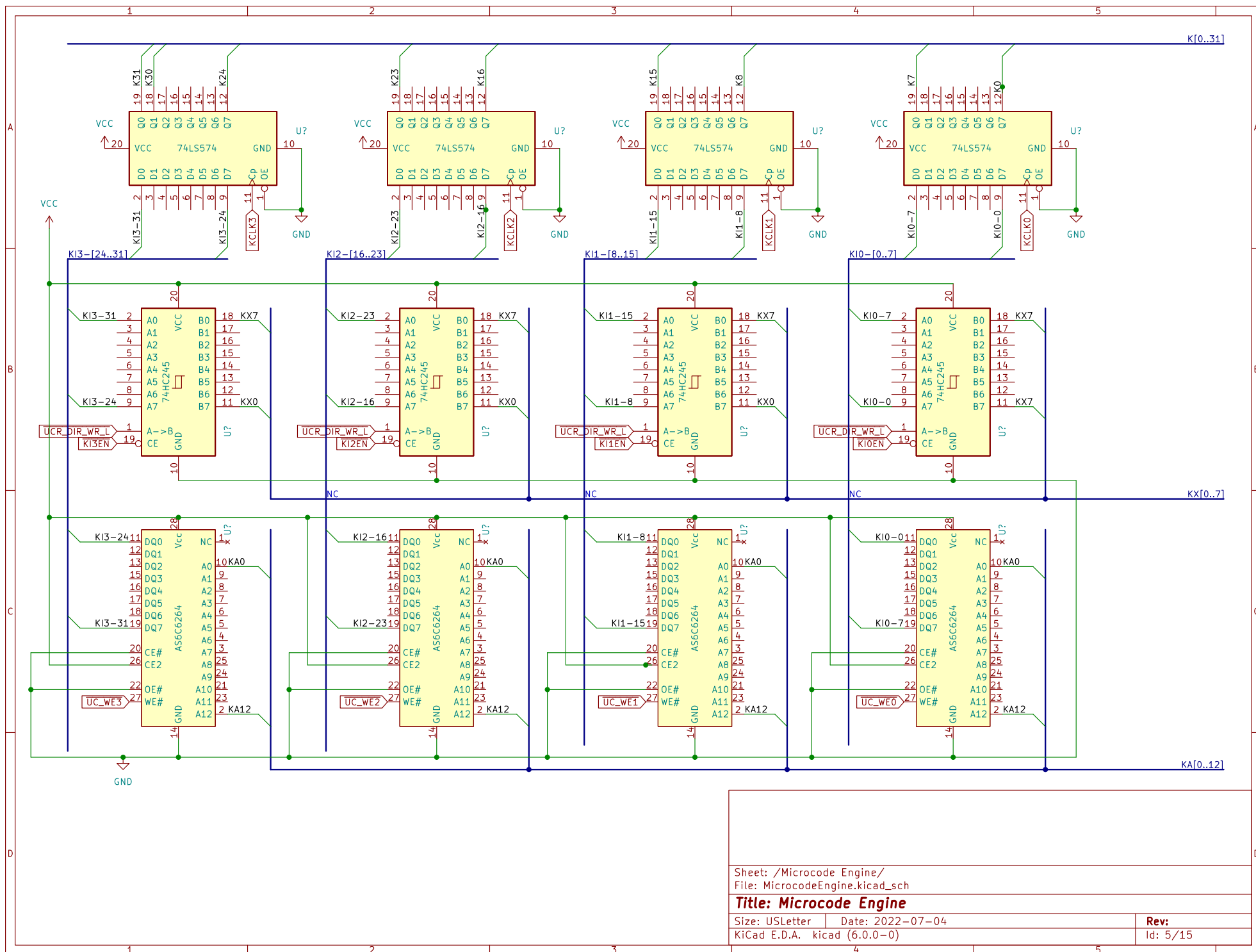
Size: USLetter Date: 2022-01-12
KiCad E.D.A. kicad (6.0.0-0)

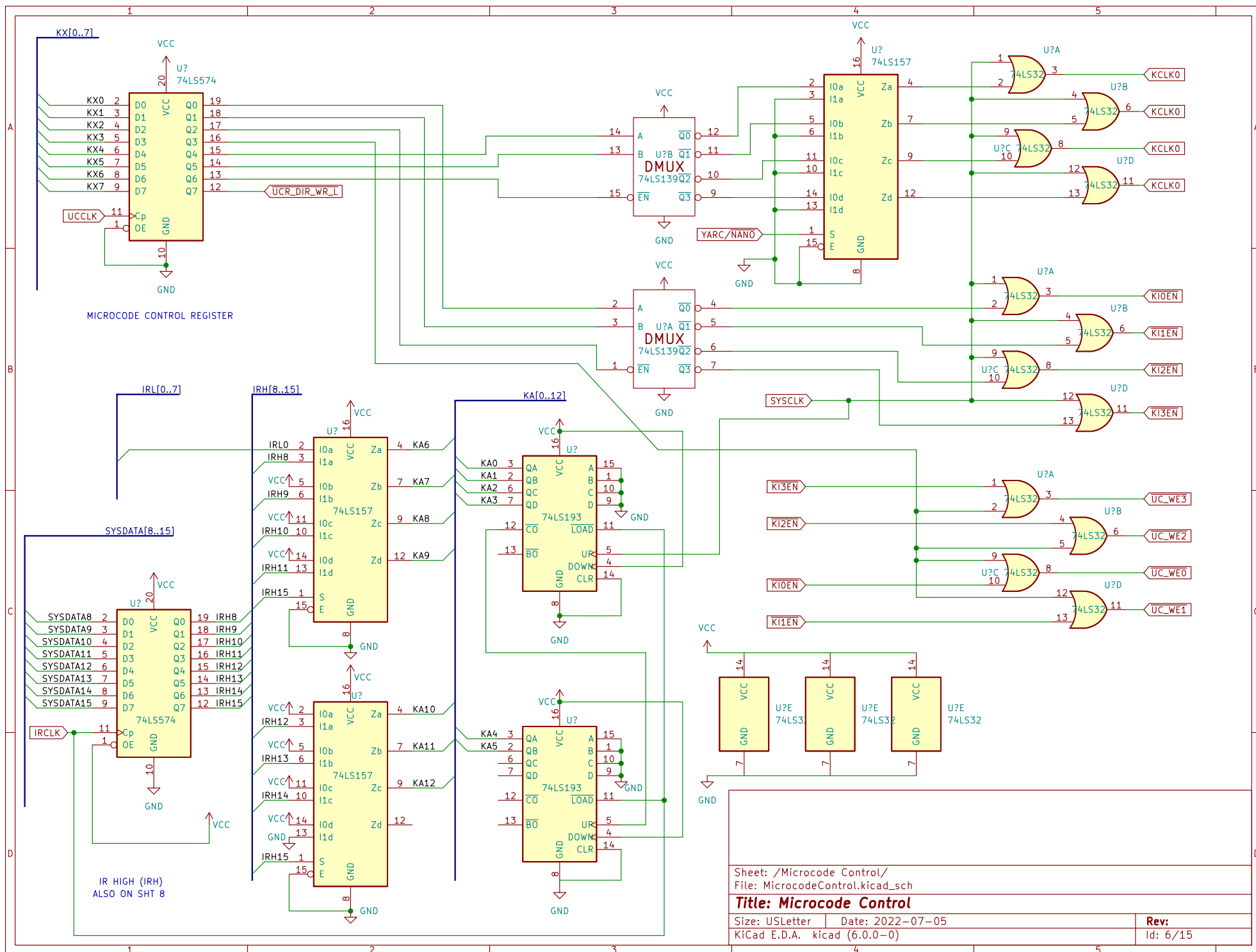
Rev:
Id: 1/15









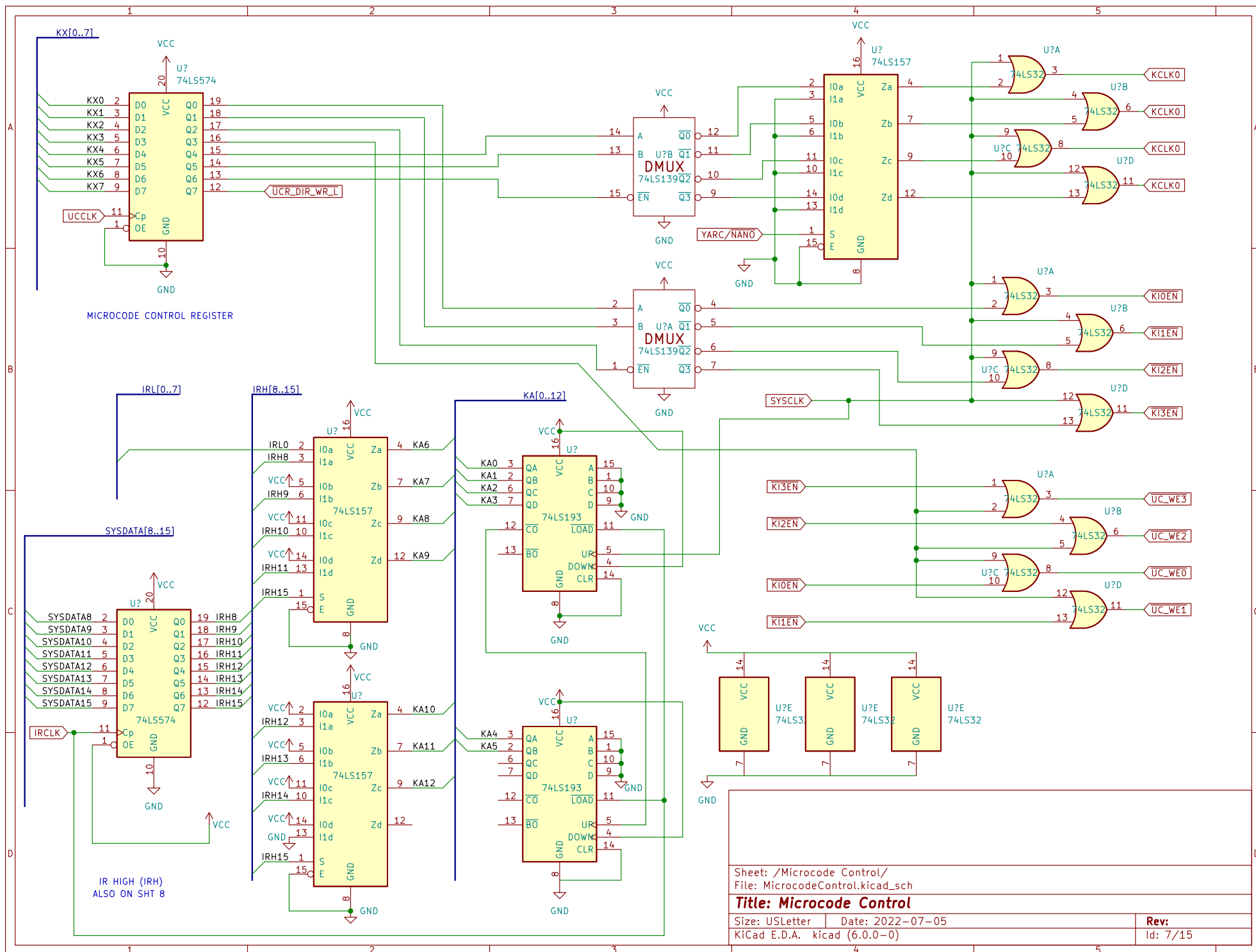


Sheet: /Microcode Control/
File: MicrocodeControl.kicad_sch

Title: Microcode Control

Size: USLetter Date: 2022-07-05
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 6/15

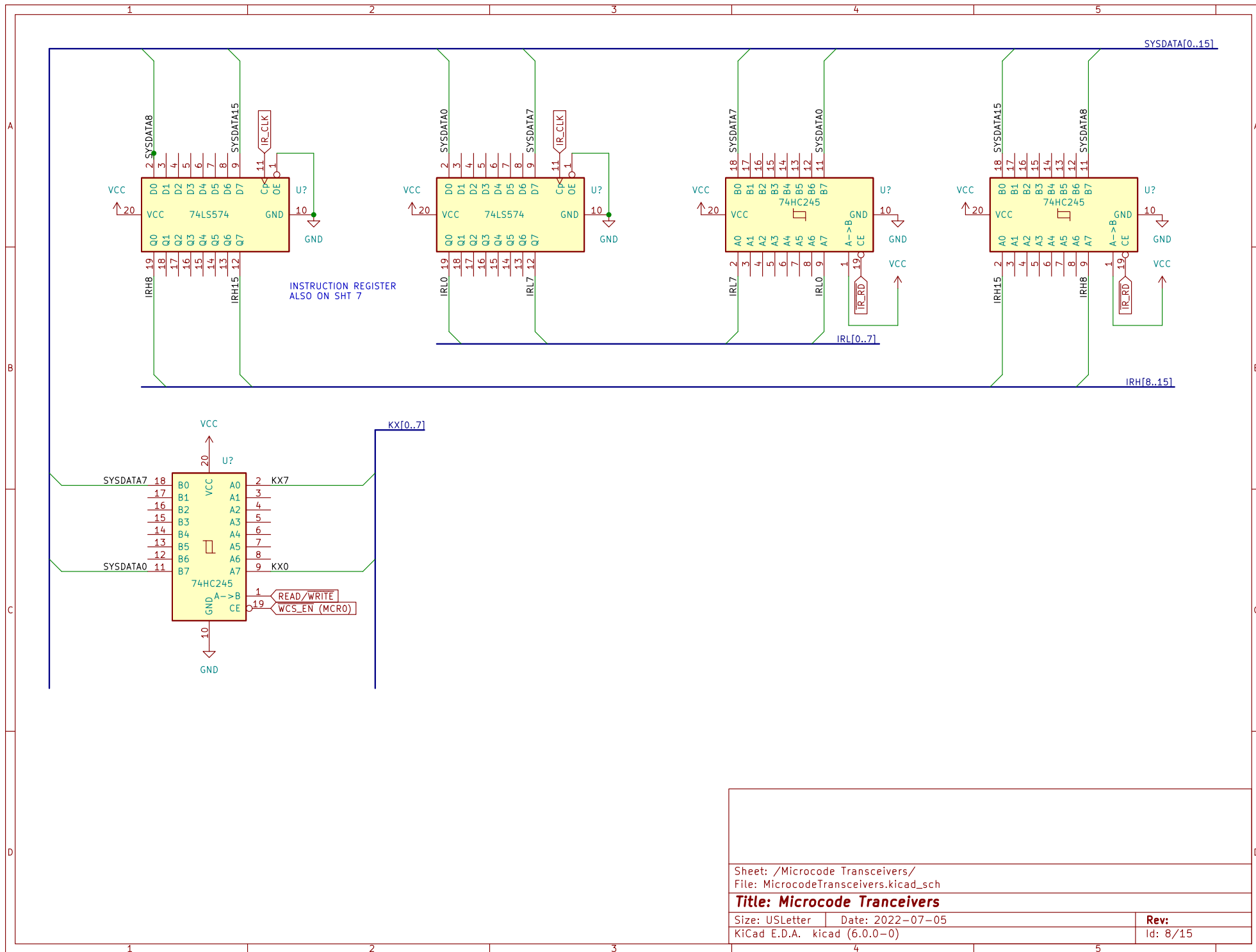


Sheet: /Microcode Control/
File: MicrocodeControl.kicad_sch

Title: Microcode Control

Size: USLetter Date: 2022-07-05
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 7/15



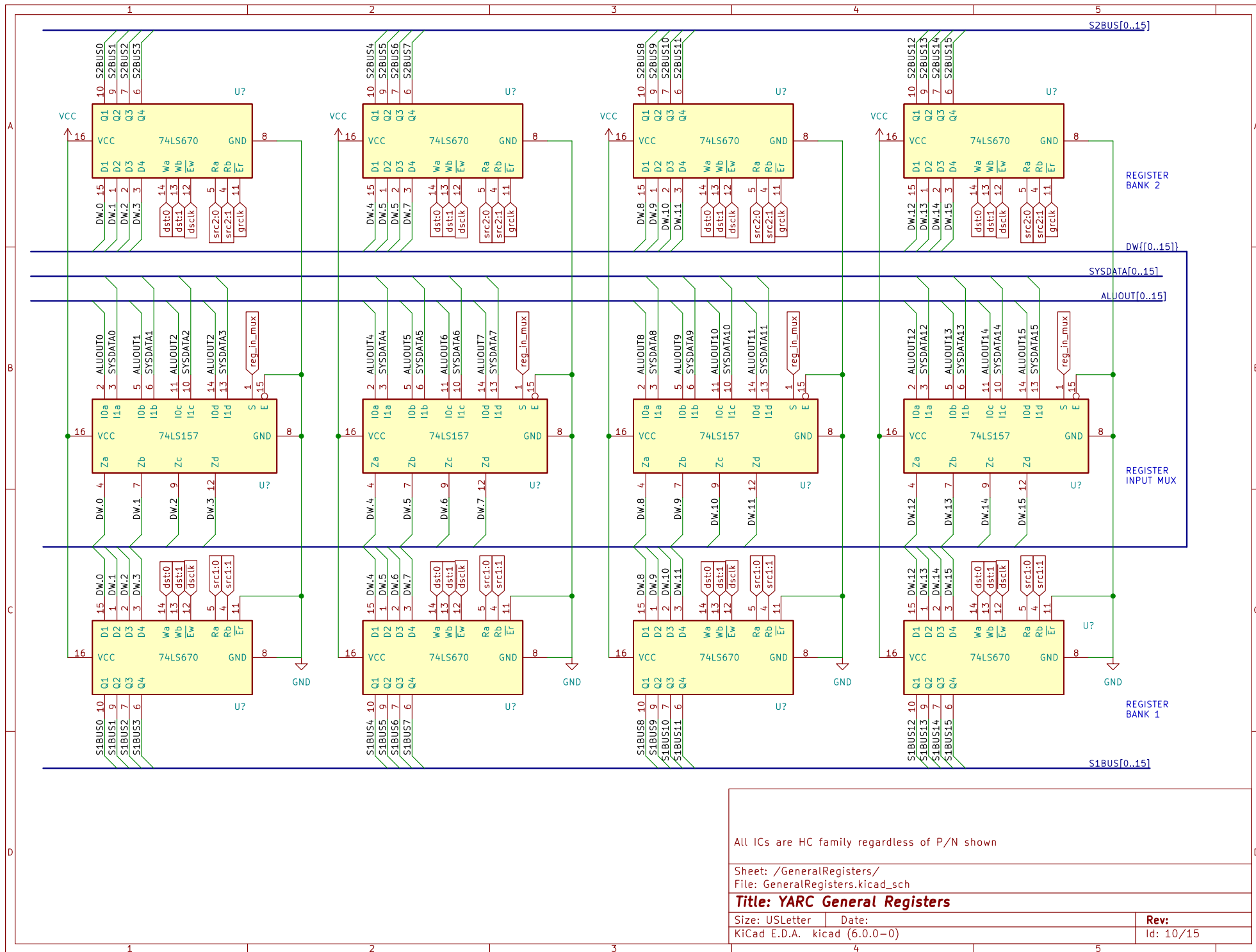
1					2					3					4					5					
A																									A
B																									B
C																									C
D																									D
1					2					3					4					5					

Sheet: /Stacks/
File: Stacks.kicad_sch

Title: **YARC Stacks**

Size: USLetter | Date: | Rev: |
KiCad E.D.A. kicad (6.0.0-0) | Id: 9/15

Sheet: /Stacks/		
File: Stacks.kicad_sch		
Title: YARC Stacks		
Size: USLetter	Date:	Rev:
KiCad E.D.A. kicad (6.0.0-0)		Id: 9/15



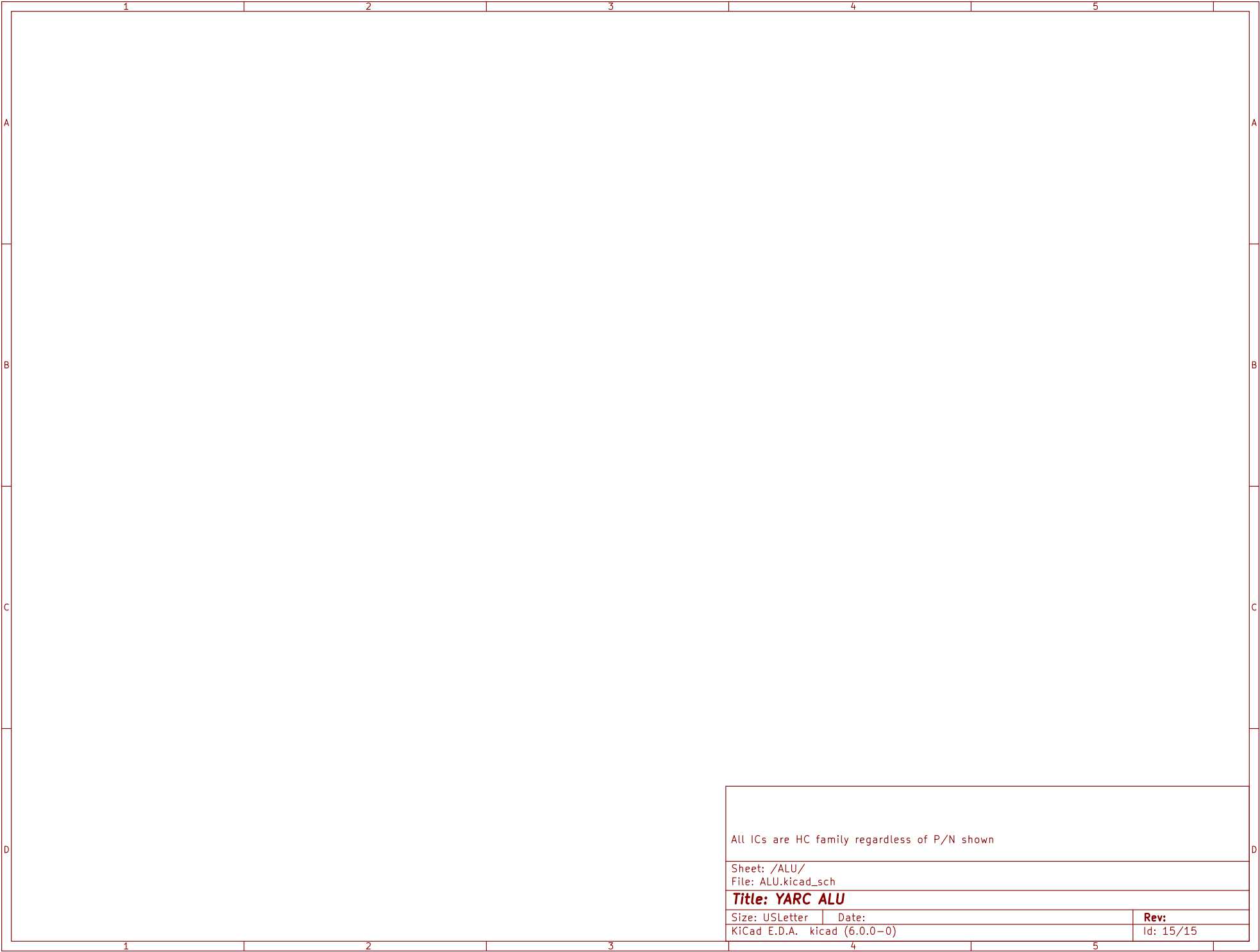
A blank sheet of graph paper with a grid pattern. The grid consists of horizontal and vertical lines forming squares. The top edge is labeled with numbers 1 through 5, and the right edge is labeled with letters A through D. In the bottom right corner, there is a rectangular box containing text.

Sheet: /FlagLogic/ File: FlagLogic.kicad_sch	
Title: YARC Flag Logic	
Size: USLetter	Date:
KiCad E.D.A. kicad (6.0.0-0)	Rev: Id: 11/15

	1	2	3	4	5	
A						A
B						B
C						C
D						D
	1	2	3	4	5	

All ICs are HC family regardless of P/N shown

Sheet: /North Central/ File: NorthCentral.kicad_sch			Title: North Central distribution logic		
Size: USLetter	Date:		Rev:		
KiCad E.D.A. kicad (6.0.0-0)				Id: 14/15	



All ICs are HC family regardless of P/N shown

Sheet: /ALU/
File: ALU.kicad_sch

Title: YARC ALU

Size: USLetter Date:
KiCad E.D.A. kicad (6.0.0-0)

Rev:
Id: 15/15

