

**EX NO: 7      PAGING TECHNIQUE OF MEMORY MANAGEMENT**

**DATE:**

**AIM:**

To write a c program to implement Paging technique for memory management.

**ALGORITHM:**

STEP 1: Start

STEP 2: Declare page number, page table, frame number and process size.

STEP 3: Read the process size, total number of pages

STEP 4: Read the relative address

STEP 5: Calculate the physical address

STEP 6: Display the address

STEP 7: Stop

**CODE:**

```
#include<stdio.h>
#include<conio.h>
int l[16] ,p[10];
int n, page;
void binary(int n);
void phyadd(int n);
void logadd(int key)
{
    for(int i=0;i<16;i++)
    {
        if(key==l[i])
        {
            n=i;
            printf("index %d\n",n);
        }
    }
    phyadd(n);
}
```

```
}  
void phyadd(int n)  
{  
    int off,pageno,temp,add;  
    off=n%page;  
    printf("off %d\n",off);  
    pageno=n/page;  
    printf("pageno %d\n",pageno);  
    for(int i=0;i<page;i++)  
    {  
        if(pageno==i)  
        {  
            temp=p[i];  
            printf("temp %d",temp);  
        }  
    }  
    add=(temp*page)+off;  
    printf("\n%d",add);  
}  
int main()  
{  
    int key;  
    printf("enter value of logical address\n");  
    for(int i=0;i<16;i++) {  
        printf(" logical address %d: ",i);  
        scanf("%d",&l[i]);  
    }  
    printf("\nenter pagesize\n");  
    scanf("%d",&page);  
    for(int i=0;i<page;i++) {
```

```

printf("size of page %d: ",i);
scanf("%d",&p[i]);
}

printf("\nenter value to find physical address\n");
scanf("%d",&key);

logadd(key);

getch();
}

```

**OUTPUT:**

```

enter value of logical address
logical address 0: 11
logical address 1: 12
logical address 2: 13
logical address 3: 15
logical address 4: 17
logical address 5: 24
logical address 6: 18
logical address 7: 19
logical address 8: 20
logical address 9: 14
logical address 10: 45
logical address 11: 67
logical address 12: 89
logical address 13: 34
logical address 14: 56
logical address 15: 7

enter pagesize
3
size of page 0: 11
size of page 1: 45
size of page 2: 56

enter value to find physical address
100
off 0
pageno 0
temp 11
33

```

<b>Observation (20)</b>	
<b>Record(5)</b>	
<b>Total (25)</b>	
<b>Intial</b>	

**RESULT:**

Thus the c program for implementing Paging technique for memory management are executed successfully and the outputs are verified