# Gokul Nadathur

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## EDUCATION

**UW MADISON**
MS in Computer Science
Fall 2000, Madison, WI

**IIT-BHU**
B.Tech in Computer Science
May 1999, Varanasi, India

## SKILLS

**PROGRAMMING**
C++ • C • Python
Familiar:
bash and related tools

## AWARDS

Netapp innovation award
2006, 2010 and 2011
Awarded to top 2% of Engineering

## RECENT PUBLICATION

FAST 2016 WIP
Adaptive Write-Back Destaging for Server-Side Caches

## LINKS

Github:// gnadathur

## EXPERIENCE

**PERNIXDATA** | Technical Director
June 2014 – Current | San Jose, CA

- Lead engineer responsible for defining the system architecture of a high performance all flash storage array.
- Substantially contributed to the design of a low latency data pipeline spanning HW and SW leveraging various interfaces to flash media such as SAS, NVMe and using RDMA technologies such as infiniband.
- Developed storage performance analytics algorithms to provide insight to performance anomalies which added to the overall IP of the product.
- Designed and implemented performance improvements in the FVP software stack used to accelerate IOs to virtual machines in ESX. Some of the work was published as WIP in a reputed conference.

**NETAPP** | Principal Engineer
May 2005 – May 2014 | Sunnyvale, CA

- Held diverse roles as performance analyst and developer.
- Contributed to design, architecture and implementation of a highly parallel network, protocol and clustering stack that improved throughput of various workloads from 50% to 2X. The improvements had a substantial impact on Netapp's clustered data ONTAP business.
- First engineer of the Storevault business unit which delivered a NAS filer targeted at the SMB market.
- Drove definition of major product features such as the data management model and back up that contributed to multiple patents.
- Designed and implemented large portions of the SW stack such as NVRAM driver, file system enhancements, data management and platform related SW.

**PILLAR DATA SYSTEMS** | Senior Software Engineer
Mar 2003 – May 2005 | San Jose, CA

- Early engineer of the file system team.
- Developed major components of the file system such as the core locking model, read ahead algorithm, write caching, buffer cache policies.

## SELECTED PATENTS

Connection distribution within a connectional parallelism architecture.
Gokul Nadathur, Anumita Biswas

Adjustment of threads for execution based on over-utilization of a domain in a multi-processor system by sub-dividing parallizable group of threads to sub-domains.
Gokul Nadathur, Manpreet Singh, Grace Ho

System and method for differentiated cross-licensing for services across heterogeneous systems using transient keys.
Gokul Nadathur, Samiullah Mohammed, Aseem Vaid

Removable portable data backup for a network storage system.
Gokul Nadathur, Aseem Vaid