

[Your Name]
[Your Address]
[City, State, ZIP]
[Email Address]
[Date]

United States Patent and Trademark Office (USPTO)
P.O. Box 1450
Alexandria, VA 22313-1450

Dear USPTO Team,

I am writing to submit a provisional patent application for my invention, titled "Energy-as-Computation," which centers around an innovative system of analog-binary hybrid gates and energy-based computation methods. This concept addresses the limitations of traditional digital computing by leveraging energy modulation for both computation and data transfer, potentially reducing power consumption and expanding the scope of computational applications beyond binary systems.

Key features of this invention include:

- The design and function of energy-efficient logic gates that operate on analog principles while retaining binary compatibility.

- The integration of continuous voltage spectrums to encode a broader range of data values, offering a scalable framework for both traditional and novel computational tasks.

- A foundational shift towards energy as both a source of power and a computational element.

I have attached all the necessary documentation and technical schematics for this application. Please feel free to reach out to me at [Your Email/Phone Number] if further clarification is needed.

Thank you for your attention, and I look forward to the progress of this application.

Sincerely,
[Your Full Name]