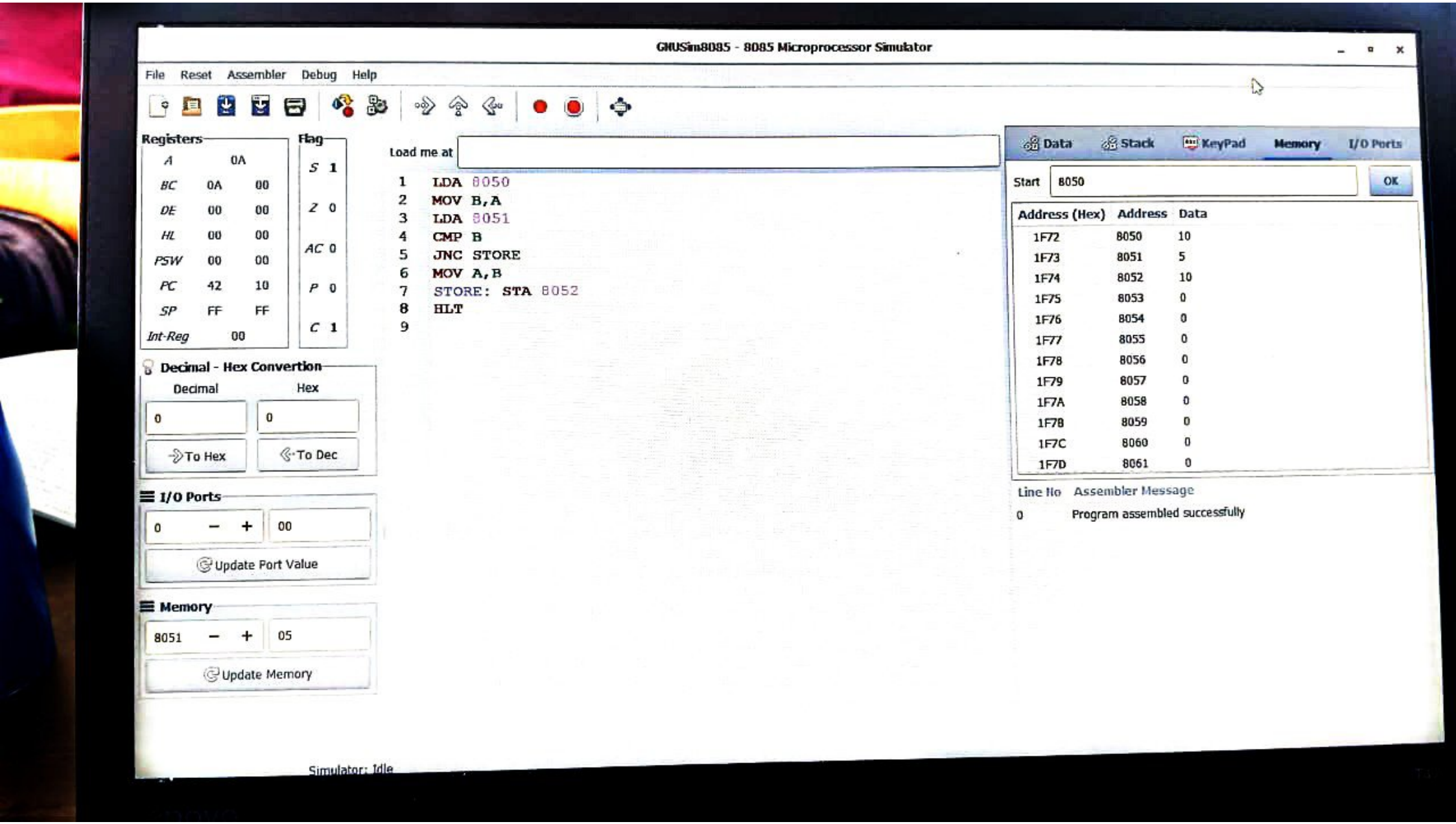
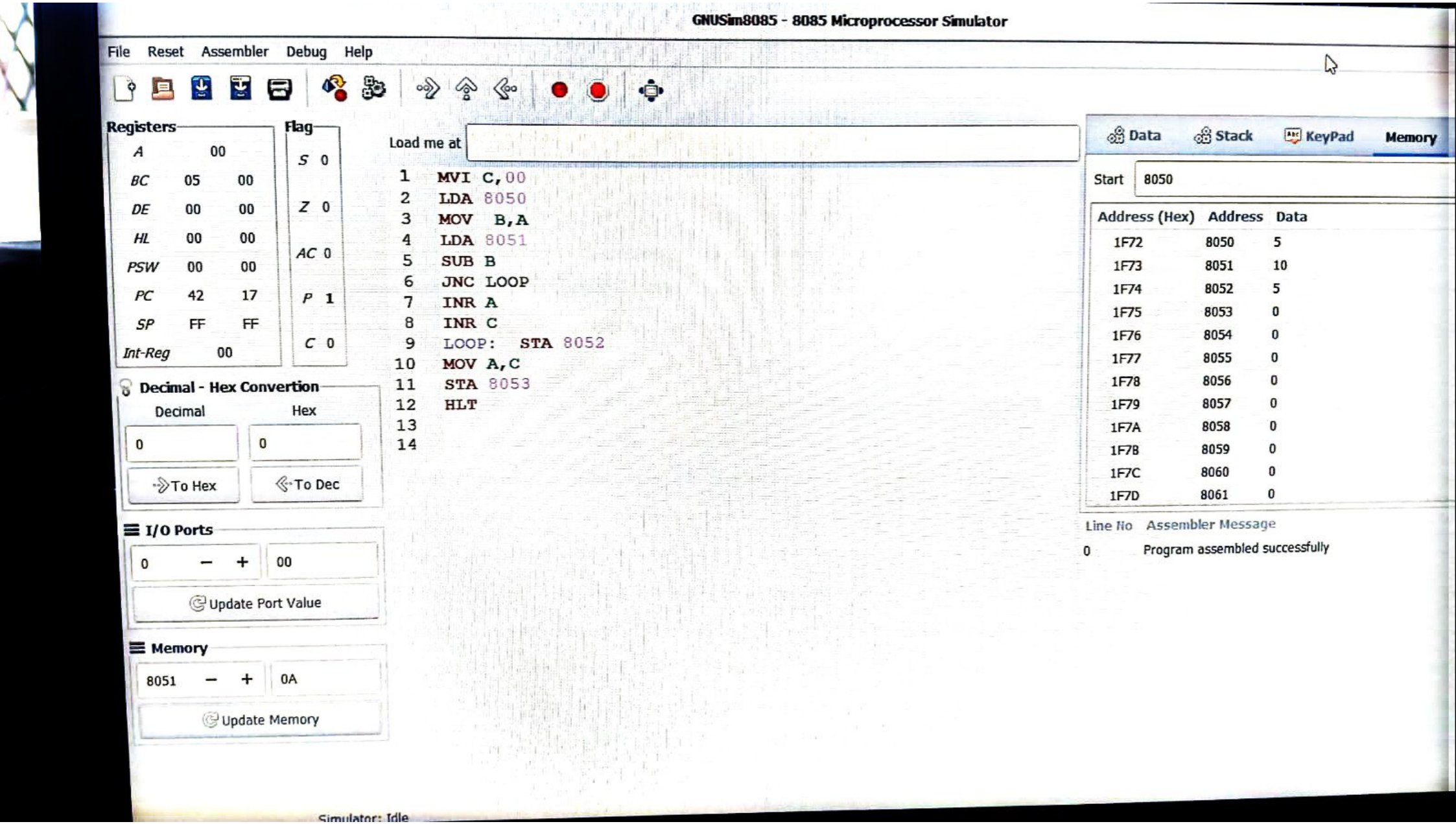

GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 0A
BC 0A 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 10
SP FF FF
Int-Reg 00
Flag
S 1
Z 0
AC 0
P 0
C 1
Load me at
1 LDA 8050
2 MOV B,A
3 LDA 8051
4 CMP B
5 JNC STORE
6 MOV A,B
7 STORE: STA 8052
8 HLT
9
Data Stack KeyPad Memory I/O Ports
Start 8050 OK
Address (Hex) Address Data
1F72 8050 10
1F73 8051 5
1F74 8052 10
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
Decimal - Hex Convertion
Decimal Hex
0 0
To Hex To Dec
I/O Ports
0 00
Update Port Value
Memory
8051 05
Update Memory
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle

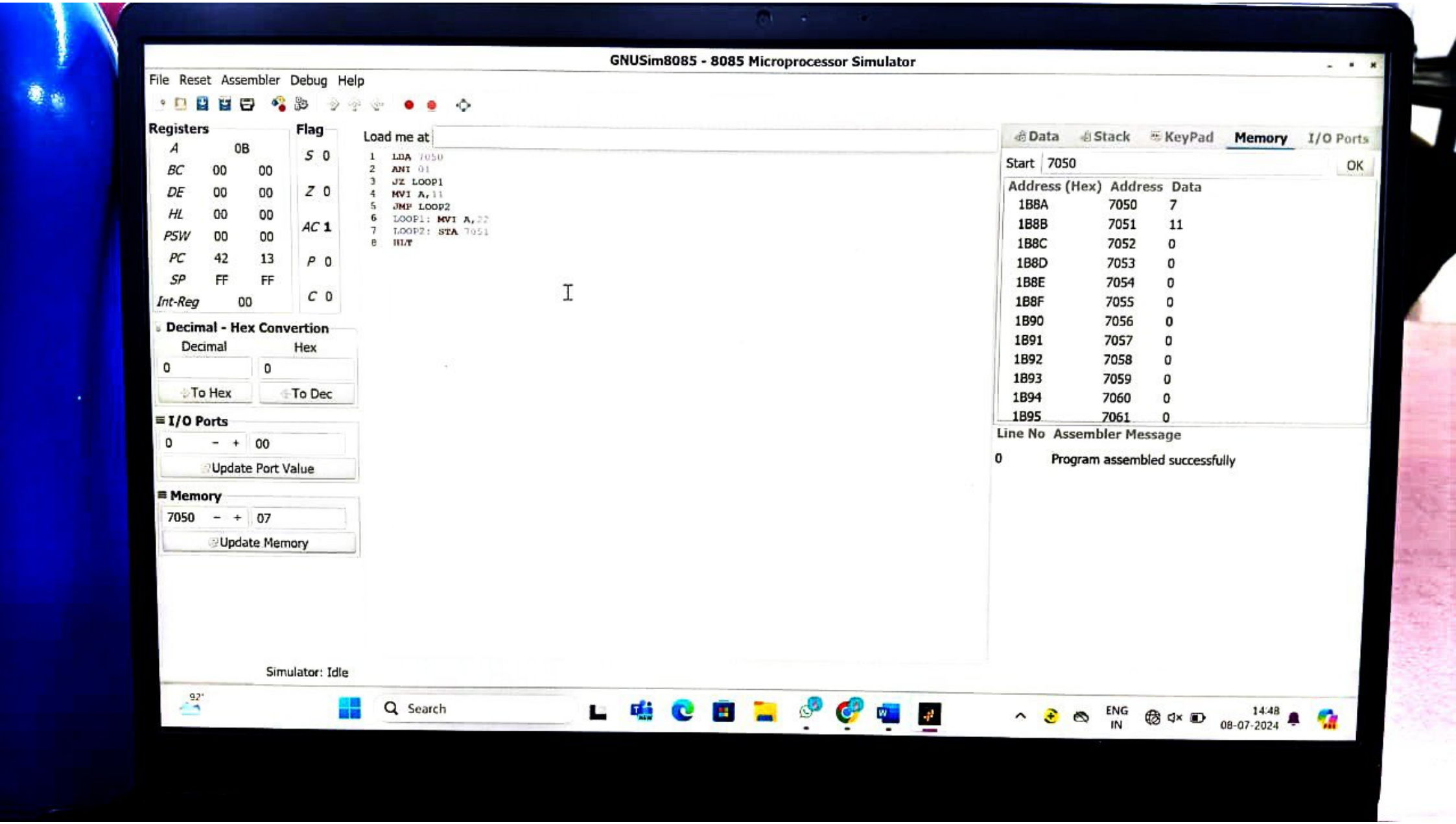
Data Stack KeyPad **Memory**

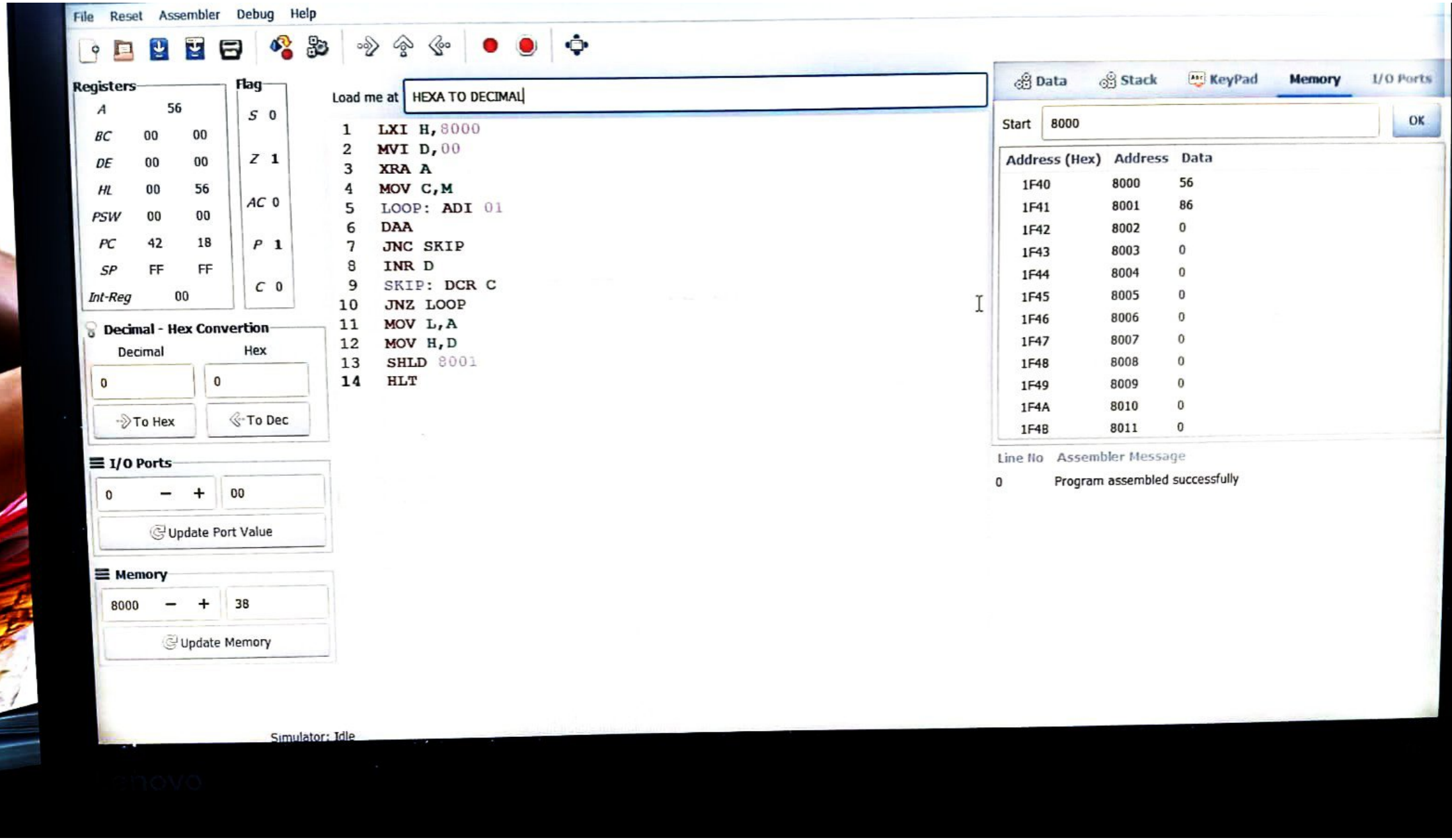
Start 8000

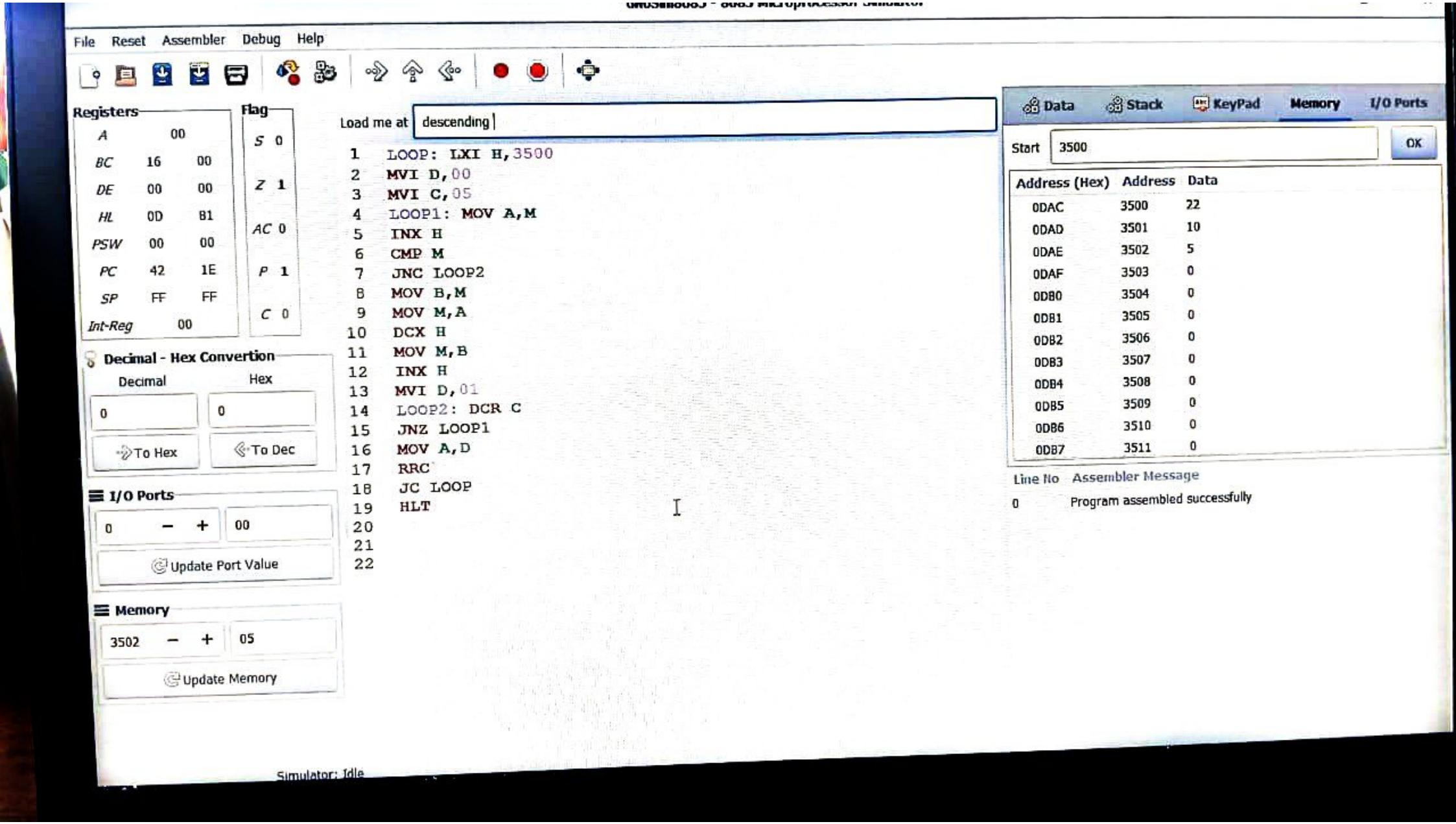
Address (Hex)	Address	Data
1F40	8000	42
1F41	8001	66
1F42	8002	0
1F43	8003	0
1F44	8004	0
1F45	8005	0
1F46	8006	0
1F47	8007	0
1F48	8008	0
1F49	8009	0
1F4A	8010	0
1F4B	8011	0
1F4C	8012	0
1F4D	8013	0

Line No	Assembler Message
0	Program assembled successfully


GNUSim8085 - 8085 Microprocessor Simulator
File
Reset
Assembler
Debug
Help
Registers
A 00
BC 05 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 17
SP FF FF
Int-Reg 00
Flag
S 0
Z 0
AC 0
P 1
C 0
Load me at
1 MVI C,00
2 LDA 8050
3 MOV B,A
4 LDA 8051
5 SUB B
6 JNC LOOP
7 INR A
8 INR C
9 LOOP: STA 8052
10 MOV A,C
11 STA 8053
12 HLT
13
14
Data
Stack
KeyPad
Memory
Start 8050
Address (Hex) Address Data
1F72 8050 5
1F73 8051 10
1F74 8052 5
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 − + 00
Update Port Value
Memory
8051 − + 0A
Update Memory
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle


GNUSim8085 - 8085 Microprocessor Simulator
File Reset Assembler Debug Help
Registers
A 0B
BC 00 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 13
SP FF FF
Int-Reg 00
Flag
S 0
Z 0
AC 1
P 0
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 - + 00
Update Port Value
Memory
7050 - + 07
Update Memory
Load me at
1 LDA 7050
2 ANI 01
3 JZ LOOP1
4 MVI A,11
5 JMP LOOP2
6 LOOP1: MVI A,22
7 LOOP2: STA 7051
8 HLT
Data
Stack
KeyPad
Memory
I/O Ports
Start 7050
OK
Address (Hex) Address Data
1B8A 7050 7
1B8B 7051 11
1B8C 7052 0
1B8D 7053 0
1B8E 7054 0
1B8F 7055 0
1B90 7056 0
1B91 7057 0
1B92 7058 0
1B93 7059 0
1B94 7060 0
1B95 7061 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle
Search
ENG
IN
14:48
08-07-2024


File Reset Assembler Debug Help
Registers
A 56
BC 00 00
DE 00 00
HL 00 56
PSW 00 00
PC 42 18
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 − + 00
Update Port Value
Memory
8000 − + 38
Update Memory
Load me at HEXA TO DECIMAL
1 LXI H,8000
2 MVI D,00
3 XRA A
4 MOV C,M
5 LOOP: ADI 01
6 DAA
7 JNC SKIP
8 INR D
9 SKIP: DCR C
10 JNZ LOOP
11 MOV L,A
12 MOV H,D
13 SHLD 8001
14 HLT
Data Stack KeyPad Memory I/O Ports
Start 8000
OK
Address (Hex) Address Data
1F40 8000 56
1F41 8001 86
1F42 8002 0
1F43 8003 0
1F44 8004 0
1F45 8005 0
1F46 8006 0
1F47 8007 0
1F48 8008 0
1F49 8009 0
1F4A 8010 0
1F4B 8011 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle


File Reset Assembler Debug Help
Registers
A 00
BC 16 00
DE 00 00
HL 0D B1
PSW 00 00
PC 42 1E
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 − + 00
Update Port Value
Memory
3502 − + 05
Update Memory
Load me at descending
1 LOOP: LXI H,3500
2 MVI D,00
3 MVI C,05
4 LOOP1: MOV A,M
5 INX H
6 CMP M
7 JNC LOOP2
8 MOV B,M
9 MOV M,A
10 DCX H
11 MOV M,B
12 INX H
13 MVI D,01
14 LOOP2: DCR C
15 JNZ LOOP1
16 MOV A,D
17 RRC
18 JC LOOP
19 HLT
20
21
22
Data Stack KeyPad Memory I/O Ports
Start 3500 OK
Address (Hex) Address Data
0DAC 3500 22
0DAD 3501 10
0DAE 3502 5
0DAF 3503 0
0DB0 3504 0
0DB1 3505 0
0DB2 3506 0
0DB3 3507 0
0DB4 3508 0
0DB5 3509 0
0DB6 3510 0
0DB7 3511 0
Line No Assembler Message
0 Program assembled successfully
Simulator: Idle

GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

A	0A	
BC	0A	00
DE	00	00
HL	00	00
PSW	00	00
PC	42	10
SP	FF	FF
Int-Reg	00	

Flag

S 0
Z 0
AC 0
P 0
C 0

Load me at

```
1  LDA 8050
2  MOV B,A
3  LDA 8051
4  CMP B
5  JNC STORE
6  STORE: MOV A,B
7  STA 8052
8  HLT
```

Data Stack KeyPad **Memory**

Start 8050

Address (Hex)	Address	Data
1F72	8050	10
1F73	8051	21
1F74	8052	10
1F75	8053	0
1F76	8054	0
1F77	8055	0
1F78	8056	0
1F79	8057	0
1F7A	8058	0
1F7B	8059	0
1F7C	8060	0
1F7D	8061	0

Decimal - Hex Convertion

Decimal: 0 — To Hex
Hex: 0 — To Dec

I/O Ports

0 − + 00
Update Port Value

Memory

8050 − + 10
Update Memory

Line No	Assembler Message
0	Program assembled successfully

Simulator: Idle

Search

ENG IN

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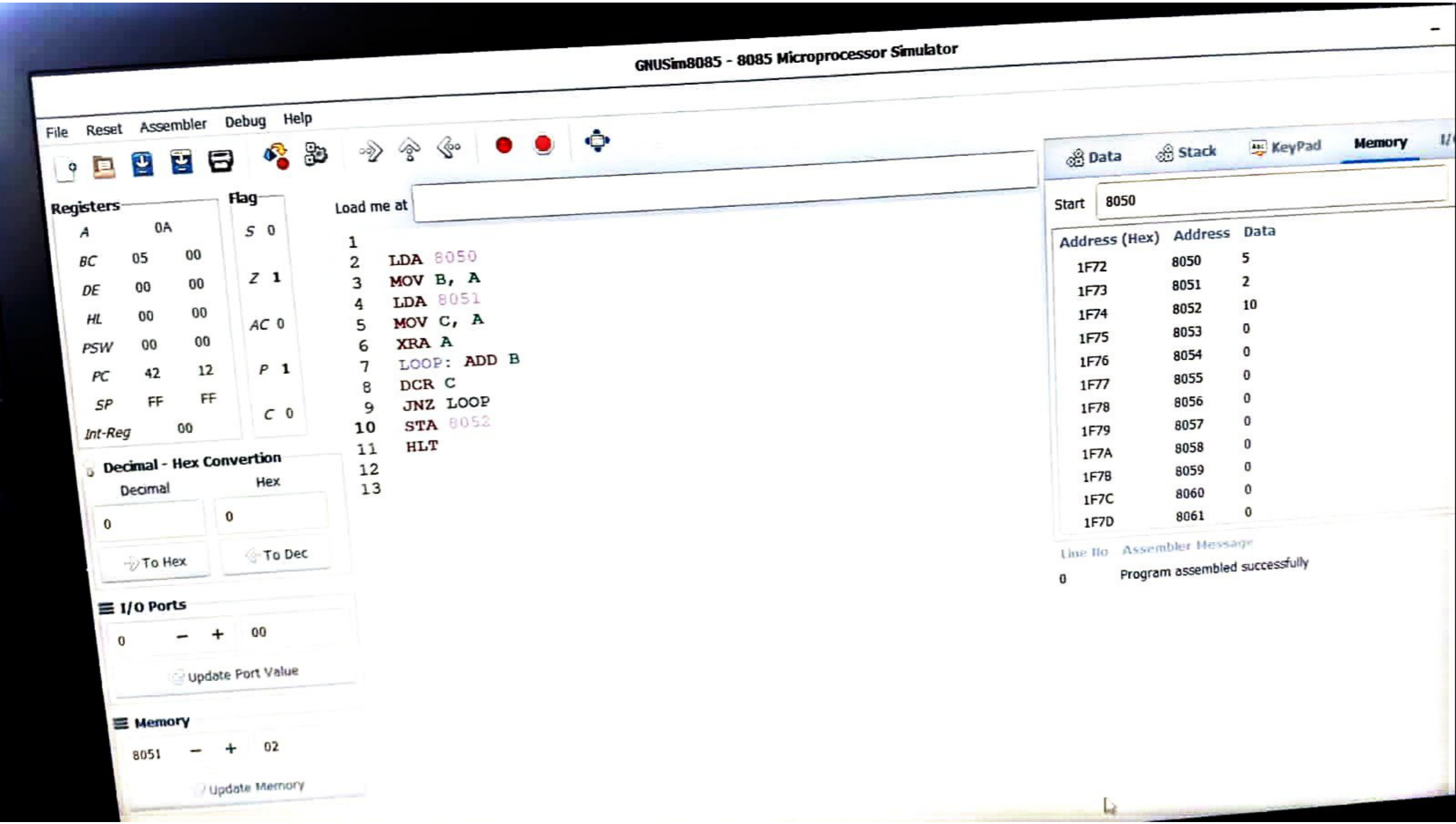
GNUSim8085 - 8085 Microprocessor Simulator
File
Reset
Assembler
Debug
Help
Registers
A 0A
BC 05 00
DE 00 00
HL 00 00
PSW 00 00
PC 42 12
SP FF FF
Int-Reg 00
Flag
S 0
Z 1
AC 0
P 1
C 0
Decimal - Hex Convertion
Decimal
Hex
0
0
To Hex
To Dec
I/O Ports
0 − + 00
Update Port Value
Memory
8051 − + 02
Update Memory
Load me at
1
2 LDA 8050
3 MOV B, A
4 LDA 8051
5 MOV C, A
6 XRA A
7 LOOP: ADD B
8 DCR C
9 JNZ LOOP
10 STA 8052
11 HLT
12
13
Data
Stack
KeyPad
Memory
Start 8050
Address (Hex) Address Data
1F72 8050 5
1F73 8051 2
1F74 8052 10
1F75 8053 0
1F76 8054 0
1F77 8055 0
1F78 8056 0
1F79 8057 0
1F7A 8058 0
1F7B 8059 0
1F7C 8060 0
1F7D 8061 0
Line No Assembler Message
0 Program assembled successfully