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VIVA questions for digital electronics lab - edaboard.com

The following are a few commonly asked viva questions for Digital Electronics experiments: What is Boolean Algebra? Define a logi-gate. Why is it called so? What is positive logic and negative logic? Define a truth table. Define an analog signal and a digital signal. List out the IC's for various logic operations like NAND, NOR, XOR, OR, AND etc.

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING ...

DIGITAL ELECTRONICS LAB EXPERIMENT NO: 1 Aim : - Introduction to Digital Electronics Lab-Nomenclature of Digital Ics, Specifications, Study of the Data Sheet, Concept of V cc and Ground, Verification of the Truth Tables of Logic Gates using TTL Ics.

DIGITAL ELECTRONICS LAB - Bhagwant University

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Digital Electronics Questions and Answers - Aptitude

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Lab Manual: Digital Electronics Lab (EE-224-F) DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Page 2 STUDENTS GUIDELINES There is 1Hr 40 Minutes allocated to a laboratory session in Digital Electronics. It is a necessary part of the course at which attendance is compulsory.

Digital Electronics Lab - BRCM College of Engineering ...

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DIGITAL ELECTRONICS LAB VIVA QUESTIONS. VIVA QUESTIONS. Q.1 What do you mean by Logic Gates? Q.2 What are the applications of Logic Gates? Q.3 What is Truth Table? Q.4 Why we use basic logic gates? Q.5 Write down the truth table of all logic gates? VIVA QUESTIONS.

Engineers: DIGITAL ELECTRONICS LAB VIVA QUESTIONS

And in the digital electronic, the logic high is denoted by the presence of a voltage potential. Sushanth KJ, Asst. Pr of. Dept. of ECE, BIT, Mangalur u Page 10 VLSI Lab Viva questions and answ er s 81. Mention what are the different gates where Boolean logic are applicable?

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