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Digital Integrated Circuits By Rabaey

Integrated circuits can be classified into analog, digital and mixed signal, consisting of both analog and digital signaling on the same IC.. Digital integrated circuits can contain anywhere from one to billions of logic gates, flip-flops, multiplexers, and other circuits in a few square millimeters. The small size of these circuits allows high speed, low power dissipation, and reduced ...

Integrated circuit - Wikipedia

Power optimization is the use of electronic design automation tools to optimize (reduce) the power consumption of a digital design, such as that of an integrated circuit, while preserving the functionality.

Power optimization (EDA) - Wikipedia

Low voltage CMOS Schmitt Trigger in 0.18 μ m technology www.iosrjen.org 10 | Page Interconnects have become the most crucial especially, in deep and very deep submicron designs, where it can

Low voltage CMOS Schmitt Trigger in 0.18 μ m technology

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1.. IntroductionRecent advances in micro-electro-mechanical systems (MEMS) technology, wireless communications, and digital electronics have enabled the development of low-cost, low-power, multifunctional sensor nodes that are small in size and communicate untethered in short distances.

Wireless sensor networks: a survey - ScienceDirect

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