**Digital Design and Computer Organization Lab**

**WEEK 1**

**25 Aug 2021**

|  |  |  |  |
| --- | --- | --- | --- |
| Vanshika Goel | **PES1UG20CS484** | H Section | Roll No: 40 |

*(Performed on own laptop due to technical issues in lab)*

**Q1)** AND and OR gates

a.v file

Text

Description automatically generated

atestbench.v file

**Text

Description automatically generated**

Output:

Graphical user interface

Description automatically generated

Graphical user interface

Description automatically generated

**Q2)** XOR and NAND gates

b.v file

Text

Description automatically generated

btestbench.v file

Text

Description automatically generated

Output:

Graphical user interface, text

Description automatically generated

Graphical user interface, application

Description automatically generated