

MEG GOFORTH-HARMON

Software Engineer ♦ DOD Clearable ♦ Steamboat Springs, CO
gofortma@rose-hulman.edu ♦ [LinkedIn - Meg Goforth-Harmon](#) ♦ +1(720) 467-0588

EDUCATION

Bachelor of Science in Computer Science, Rose-Hulman Institute of Technology

Expected 2025

Minors: Cybersecurity, Political Science

Relevant Coursework: Introduction to Systems Programming, Computer Architecture I-II, Introduction to Database Systems, International Relations, Data Structures and Algorithm Analysis, Software Design, Advanced Database Systems, Software Requirements Engineering, Practical Security I-III

SKILLS

| | |
|--------------------------|--|
| Programming | C, C++, C/, Java, HTML, Javascript, Verilog, SQL, RISC-V, Python, YAML, Bash |
| Tools/Software | Virtual Machines, AWS, GIT, Quartus Prime, Atlassian Suite, Bamboo, Kubernetes |
| Database Systems | MySQL, Redis, MongoDB, Microsoft SQL Studio, Firebase, Cassandra Apache, Neo4j |
| Operating Systems | Windows, MacOS, Ubuntu, CentOS, Rocky, RHEL8, RHEL7, SUSE |

EXPERIENCE

Software Development Engineer I - Internship

June 2023 - August 2023

Kratos Defense & Security Solutions

Colorado Springs, CO

- Enabled and improved interoperability testing for network systems using Python, YAML, and Bamboo.
- Installed and maintained operating systems and BIOS on networking hardware.
- Wrote in-depth documentation for software and hardware processes.
- Worked in Full-Stack environments for hardware and software codesign and integration.

Software Engineering Intern

June 2022 - January 2023

Direct Supply

Milwaukee, WI/Remote

- Managed operational systems workflow and SQL databases and worked with EDI and ecommerce workflow automation through XML, Visual Basic, and C/.
- Utilized engineering scrum team management to streamline project execution.

PROJECTS

CPU Development Designed and implemented a 16-bit Load/Store CPU with a team that included a 3-stage multicycle system, 16-bit registers, and a 16-bit instruction set. Utilized Verilog to create a CPU that could run a simple assembly language program that could take user input and output in order to run assembly commands.

FPGA Board Game Design Utilized Quartus Prime FPGA Design and an Altera DE2 Development Board to develop a multiplayer game of Pong through using combinational and sequential digital logic to implement a long game using a state machine.

Portfolio Website <https://meg-goforth-harmon-portfolio.web.app>

ACTIVITIES AND LEADERSHIP

HACU CIA Bootcamp Career Exploration Bootcamp

March 2022

Alpha Omicron Pi - Delta Xi Chapter, Vice President of Membership Experience

December 2022 - Present

CSSE Department Mentor

August 2022 - Present

CSSE Department Instructor for Practical Security Courses

August 2023 - Present

Library Front Desk Worker

August 2023 - Present

RHITM Acapella Club, President

February 2023 - Present

Study Abroad British Politics and Government Study Abroad to UK

May 2022 - June 2022