

# Lab 1: Schematic Drawing Exercises

Matthew Friedman 861151348  
Souradeep Bhattacharya 861105938  
EE128 Section: 021

October 03, 2017

## **Abstract**

The objective of this lab was to familiarize ourselves with OrCad Capture CIS. To do so we built and modified 2 circuit schematics.

## **Experiment System Specification**

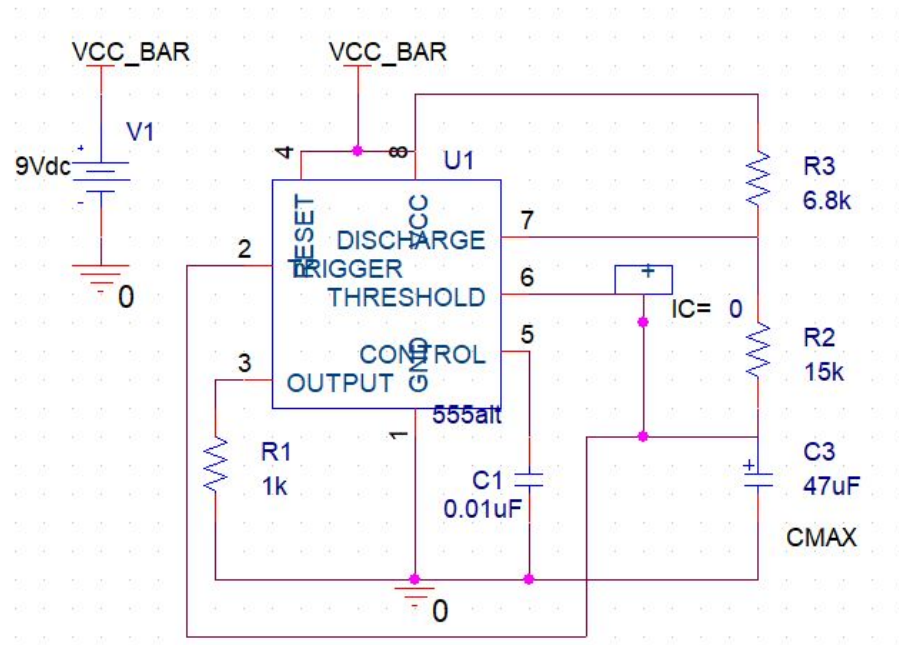
### **Part 1**

Draft a timer using the 555 timing IC, resistors, and capacitors.

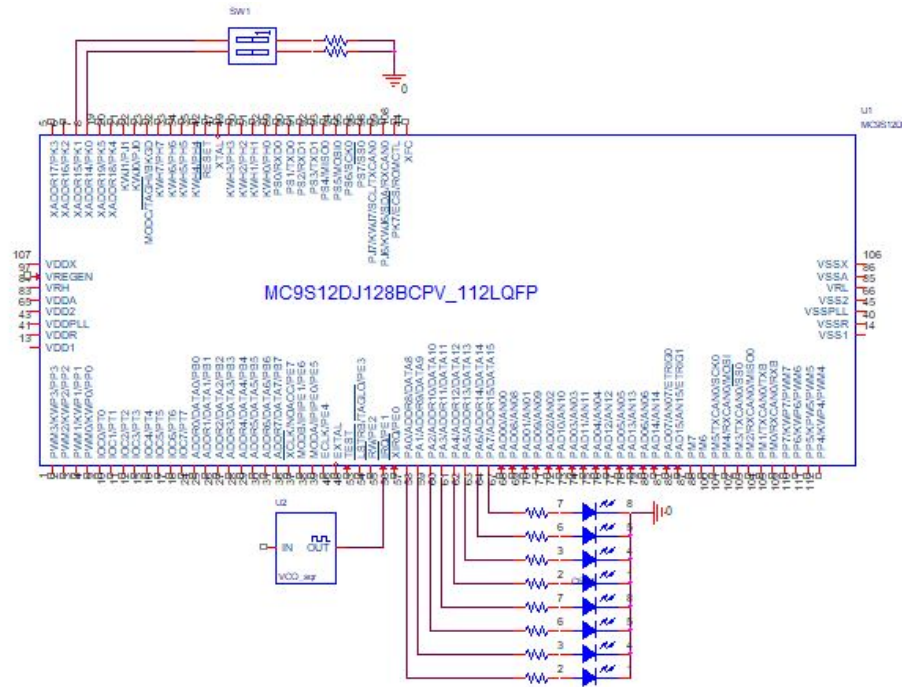
### **Part 2**

Setup an interrupt circuit for the MC9S12DJ128BCPV micro controller IC, and add LEDs to port B.

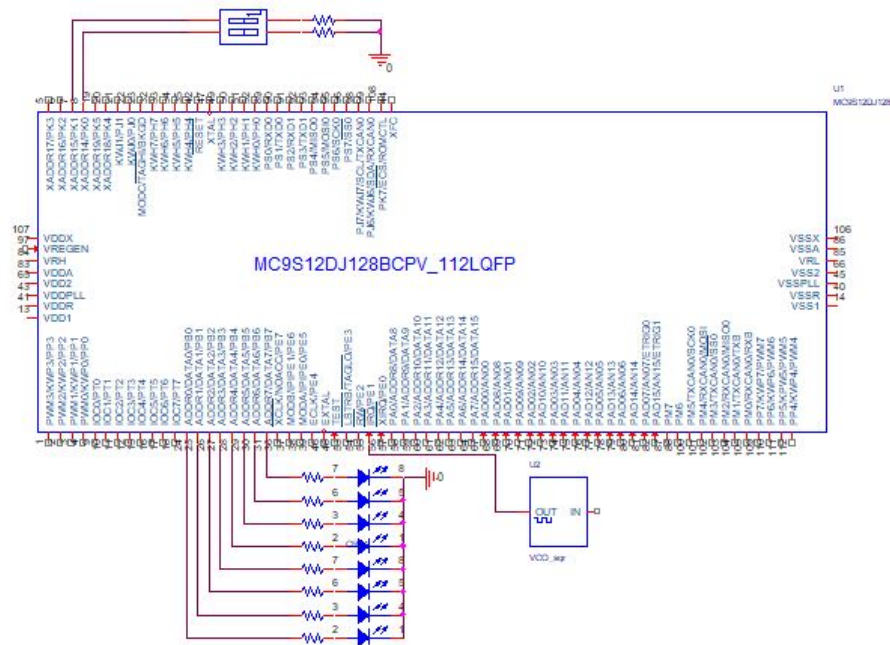
## Part 1



## Part 2-A



## Part 2-B



## Technical Problems

We had to install a secondary library that contained parts required for the lab. This was done by downloading the libraries from the iLearn site and adding them to the folder where the rest of the libraries were.

## Conclusion

In conclusion, we successfully refreshed our skills with OrCad Capture CIS by drafting and modifying 2 circuit schematics.