

CORE

Var Mem

Queue Management

PTR

τ_e

Instruction
Memory

PC

v1

v2

⋮

FSM

L

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□/◇

∧/∨

U

I1

I2

⋮

I1

I2

⋮

SCQ

I1

I2

⋮

RAW Sensor Signals
(binary)

ATOMICs

AP[1]

⋯

AP[N]

Filters



LOAD



NEGATE



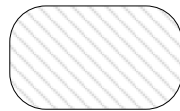
AND/OR



GLOBAL/FUTURE



UNTIL



RAM