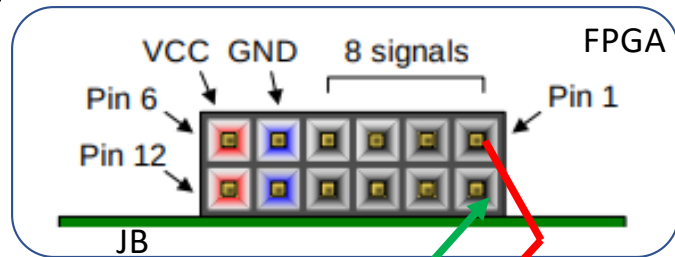


PC



Send Data to FPGA

Receive Data from FPGA