

GOKULAKRISHNAN.T

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Software Analyst @ Aspire Systems

OBJECTIVE:

To pursue a challenging career in a progressive environment where innovation, creativity and learning are encouraged and skills get enhanced to their maximum potential and to contribute to overall growth of the organization.

EDUCATIONAL QUALIFICATION:

COURSE	INSTITUTION	SCORE	REMARKS	DURATION
SSLC (State Board)	Don Bosco Matriculation School, Chennai.	95.8%	School First	2012
HSC- CS (State Board)	Don Bosco Higher Secondary School, Chennai.	90.9%	School Third	2014
B.Tech- ECE (Deemed University)	Sastra University, Tanjore.	85.3%	Overall department first in 6 th semester & College first in 10 subjects.	2014-18

*CS – Computer Science

*ECE – Electronics and Communication Engineering

TECHNICAL SKILLS:

- Programming languages : C#, C, C++
 - Database Language : SQL
 - Web Design : HTML, JQuery, Javascript, Ajax
 - Network Design : 4NEC2
 - Animation Software: Macromedia Flash
 - Statistical Analysis : MATLAB
 - Circuit Design : Arduino, VHDL, LabVIEW
 - Simulation Softwares: Circuitmaker, VMLAB, Multisim, Keil Microvision, Quartus, QUCS, ModelSim
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WORK EXPERIENCE: (July-2018 to Present)

Software Analyst at Aspire Systems, Chennai.

- Designed and Developed Library Management System using SQL Server and .Net Framework.
- Worked in Project Release management and Development for Fast-Tracker project and Asset Register project using VSTS and TFS.
- Adhered to high-quality development and coding standards while delivering project solutions on-time to customers.
- Hands on experience in SDLC process, CQRS Microservices architecture pattern, WEB and Windows services.

PAPER PUBLISHED:

- Published a paper titled “**Electronic Pesticide**” in the proceedings of the National Conference on Advances in Process Engineering(CAPE-2017), organized by Sastra University.
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INTERNSHIP DETAILS :

- 2 Month Summer Research Fellowship on “Active Antenna Design for low frequency Galactic noise observation” at Indian Institute of Astrophysics, Gauribidanur : May-July 2017.
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PROJECT WORK:

- Small Cell Design with LTE Standard using FPGA for research in 5G technologies.
 - Implemented Radar Gun using ultrasonic detector for aid of blind people.
 - Constructed Quadrifilar helix antenna to predict and analyze weather.
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CO-CURRICULAR ACHIEVEMENTS :

- First place in Circuit design competition and second place in Electronic quiz organized during THETA 2017 in SRC, Kumbakonam.
 - First place in Paper Presentation in National Conference on Advances in Process Engineering (CAPE-2017) organized in Sastra University.
 - First place in Electric Quiz and Second place in Overall Dept. Quiz organized during DAKSH 2016 in Sastra University.
 - Second place in General Quiz organized in 2016 at Sastra University.
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AREAS OF INTEREST :

- Software Design and Development
 - Microsoft .Net Services
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EXTRA CURRICULAR ACTIVITIES:

- Active member of National Service Scheme(NSS) from 2015 in Sastra University and participated in various camps.
 - Good Badminton, Table Tennis, Cricket & Chess player.
 - Participated in school dramas, literary competitions and in Office cultural events.
 - Article Writer in Chemunique magazine and in Quora.
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PERSONAL DETAILS :

Date of Birth : 4th September 1996
Address : 23/12, 15th West Cross Street, MKB Nagar, Chennai-39.
Contact : 9884934996
Languages Known : English(R/W/S), Tamil(R/W/S), Hindi(R/W)
