

G.JASWANTH

Summary

A highly motivated and detail-oriented graduate with a strong foundation in Information Technology and VLSI design. Proficient in programming languages such as C and Python, with a deep understanding of digital circuit design, FPGA development, and hardware description languages (HDL) like Verilog and VHDL.

Profile

- **Phn no** : 9391047213
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- **Github** : <https://www.github.com/in/gollapallijaswanth>
- **Codechef** : https://www.codechef.com/users/jaswanth_445
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- **Address** : AP, East Godavari , Kaleru

Education

Board of secondary education	2019 – 2020
<ul style="list-style-type: none">• Mandapeta public school , Mandapeta• CGPA: 10/10	
Board of intermediate education , MPC	2020 – 2022
<ul style="list-style-type: none">• MPS junior college , Mandapeta• CGPA: 6.0/10.0	
Bachelor of engineering , ECE	2022 – 2026
<ul style="list-style-type: none">• Sasi institute of technology and engineering , Tadepalligudem• CGPA: 7.4	

Internships

VLSI (short term Internship) , Skilldzure

- As an intern,i gain exposure to the entire VLSI design process, including system integration, circuit design, layout, and verification.
- In this virtual short term internship worked on the designing, testing, and fabricating of the integrated circuit.

Networking Virtual internship , Eduskills

- A virtual internship offers a valuable opportunity to expand your professional network by connecting with industry professionals and building relationships globally, even from a distance.

Data Engineering Virtual Internship , Eduskills

- A Data Engineering Virtual Internship offers a valuable opportunity to gain hands-on experience in building and managing data pipelines, even from a remote location.

Skills

Programming languages

- C , JAVA , PYTHON

Technical skills

- MATLAB , Xilinx vivado , Operating skills

Languages

- Telugu , Hindi , English