



EE2002

Analog Electronics

Part 1

Assoc. Prof. Liter **SIEK**

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Syllabus with 4AUs

Part 1 Liter SIEK (S2-B2c-106)

(1) Op-Amps

(2) Diodes

Part 2 ÁZPÒÞÕÄ~ æ ã ÁÜGÈÖGĚ Î)

(3) Single-Stage Amplifiers (BJT)

(4) Single-Stage Amplifiers (MOSFET) Á

Part 3 ÁÜQÁRĚ } ÁÜGÈÖGĚGD

(5) Frequency Response (Bode Plot)

(6) Frequency Response (SCTC & OCTC)

ÜÖSÄŕ! (Subj. Co-ord) (S2-B2&1€6)Á

Text book: Microelectronic Circuit Design
By Richard C. JAEGER & Travis N. BLALOCK
2nd Edition or 3rd Edition and beyond

Part 1 Liter Siek (S2-B2c-106)
Lectures from Week 1 to 4

Part 2 Zheng YuanJin (S2.2-B2-46)
Lectures from Week 5 to 8

Part 3 Sit Ji-Jon (S2.2-B2-02)
Lectures from Week 9 to 12

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Assoc Prof Liter SIEK EE2002 Introduction

Analog Electronics

Background req'd

- Ohm's Law
- Thevenin's Equivalence
- Superposition Theorem
- KCL
- KVL
- Resistor/potential divider
- Laplace
- A good understanding of EE2001



Georg Simon Ohm 1789-1854

$$CA ([15 + 5 \times 2 + 5 \times 3 + 10]\% = 50\%)$$

- Quiz on Wk 6 (betw. 2.30pm-4.00pm Sat. 19 Feb. 2022)
 - To be conducted on Saturday(venue tbd).
 - On **Op-Amps** and **Diodes** ONLY
 - 35 minutes 10 MCQs
 - 15% contribution to the final
- 2 assignments each 5% contribution to the final (total:10%)
- 3 Lab Experiments each 5% contribution to the final (total:15%)
- Class Participation (total:10%)

EXAM

(27th April 2022 Wednesday 9.00AM 2.5hrs)

- 4 Questions equally weighted
- No choice
- 50% contribution to final
- Questions from Part 1(1Q), Part 2(2Qs) and Part 3(1Q).