CS2700 Homework 3

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Question 1.

7.1 List three broad classifications of external, or peripheral, devices. Communication and human and machine readable.

Question 2.

7.2 What is the International Reference Alphabet? A 7-bit text code to represent text characters.

Question 3.

7.3 What are the major functions of I/O module? Control, timing, processor and device communication, data buffering, and error detection.

Question 4.

7.4 List and briefly define three techniques for performing I/O.

- (a) Programmed I/O: data exchanged between the processor and the I/O module. A program is given control of an I/O device to get its status or read, write, and transfer data.
- (b) Interrupt-driven I/O: processor issues the I/O command then waits for the I/O module to interrupt the processor when its ready to exchange data with the processor.

(c) Direct Memory Access: Gives I/O direct access to what ever it requests through a mock processor.

Question 5.

7.5 What is the difference between memory-mapped I/O and isolated I/O? Memory mapped I/O has a single address space for memory location and I/O drives. Isolated I/O has a seperate address space from main memory.

Question 6.

7.6 When a device interrupt occurs, how does the processor determine which device issued the interrupt?

It will poll each I/O module to see which module sent the interrupt/

Question 7.

7.7 When a DMA module takes control of a bus, and while it remains in control of the bus, what does the processor do?

If the processor needs the bus at the time, it will be forced to suspend operation until the bus is given back up.

Question 8.

8.1 What is an operating system?

A program that controls other programs and interfaces between the programs and the computer's hardware.

Question 9.

8.2 List and briefly define the key services provided by an OS.

(a) Program Creation: Providing tools to write programs for the OS

- (b) Program Execution: Handling loading instructions and data into memory and initializing I/O devices
- (c) Access to I/O Devices: Controlling read and write access and knowing how to handle I/O devices
- (d) System Access: Only allowing users access to what they have writes to.
- (e) Error Detection: Handling errors in the least catastrophic ways possible and letting the user know about it
- (f) Accounting: Giving the ability to monitor hardware and system performance.

Question 10.

8.3 List and briefly define the major types of OS Scheduling.

Long-term scheduling: Determining which programs are given to the system to process.

Meduim-term scheduling: Determining whether to add a process to main memory and if so how much of it.

Short-term scheduling: Determining which job should be executed next.

I/O scheduling: Deterimining which I/O request to handle.

Question 11.

8.4 What is the difference between a process and a program.

A program is usually user affected. A process is a program that will run on its own and interacts with the system.

Question 12.

8.5 What is the purpose of swapping?

To keep memory free and accessible for processes that need it more than others.

Question 13.

8.6 If a process may be dynamically assigned to different location in main memory, what is implication for the addressing mechanism?

To keep track of the physical and logical addresses of the process, the latter for swapping out processes.

Question 14.

8.7 Is it necessary for all of the pages of a process to be in main memory while the process is executing?

Only pages that contains the data the process needs to execute is necessary.

Question 15.

 $8.8~\mathrm{Must}$ the pagers of a process in main memory be contiguous No

Question 16.

8.9 Is it necessary for the pages of a process in main memory to be in sequential order?

No

Question 17.

8.10 It is a cache for page table entries to make memory access more efficient and to have to pull from physical memory less.