GOPIKA C RAJENDRAN
M.Tech in VLSI Design

Graduate equipped with a Bachelor of Technology in Electronics and Communication Engineering with a current Master of Technology in VLSI Design eager to join a reputable organization to begin a fulfilling, lifelong career. Extensive knowledge of digital integrated circuit design, analog integrated circuit design. Additional knowledge of Verilog HDL; expert in the implementation and design of digital and analog circuits using Cadence and Synopsys.

CONTACT

Phone
+91 7034124005

Email
gopikarajendran4005@gmail.com

Address
Kerala, India

www.linkedin.com/in/gopika-r-404454171

https://github.com/gopika-rajendran

SKILLS

- Verilog HDL
- Python
- EDA Tools: Synopsys, Cadence Virtuoso, Intel Quartus Prime, ModelSim
- Digital electronics
- Analog electronics

COURSES

- Python, GUVI - ONLINE (NOVEMBER 2020, 4 WEEKS)

- Geospatial Applications For Disaster Risk Management, ISRO - ONLINE (OCTOBER 2020)

- Deployment Of Application Models In MATLAB And Simulink To STM32 MCUs, NIELIT Calicut- ONLINE(15 th -17 th Sept. 2020)

LANGUAGES

- English
- Hindi
- Malayalam

EDUCATION

2022-Present
Vellore Institute of Technology, Vellore, India
M.Tech in VLSI Design
CGPA : 7.69

2016 - 2020
Jawaharlal College of Engineering and Technology, Kerala, India
B.Tech in Electronics and Communication Engineering
CGPA : 8.64

2014 - 2016
Carmel CMI School, Kerala, India
12th - CBSE
Percentage : 89.04 %

2012- 2014
Deva Matha Central School, Bangalore, India
10th - CBSE
CGPA : 9.0

PROJECTS

- **Design and Implementation of Asynchronous FIFO**
  In this project, I have implemented asynchronous FIFO using INTEL FPGA and simulated using Quartus Prime. The output is verified using FPGA Board.

- **Human Fall Detection & Location Tracking**
  In this project, I have implemented safety unit for elders with smart sensors and electronics that detect fall, locate the fall location and provide safety measures to elders.

- **Smart Trolley**
  In this project, I implemented a smart shopping cart with RFID technology. This technology is used for boosting security performance and speed while purchasing as well as the time saved at billing counters.

INTERNSHIPS

KELTRON REC INDUSTRIAL TRAINING PROGRAM

June 2017 - July 2017
- Electronic Product Development,
- Hardware and firmware development stages.