GOPIKA C RAJENDRAN

Thrissur, Kerala · gopikarajendran4005@gmail.com · 7034124005 · https://github.com/gopika-rajendran/RESUME ·

SUMMARY

Graduate equipped with a Bachelor of Technology in Electronics and Communication Engineering with a current Master of Technology in VLSI Design eager to join a reputable organization to begin a fulfilling, lifelong career. Extensive knowledge of digital integrated circuit design, analog integrated circuit design. Additional knowledge of Verilog HDL; expert in the implementation and design of digital and analog circuits using Cadence and Synopsys.

EDUCATION

Vellore Institute of Technology   Vellore, Tamil Nadu
M.Tech VLSI Design *GPA: NA*   Sep 2022 - Present

Jawaharlal College of Engineering & Technology   Palakkad, Kerala
B.Tech Electronics & Communication Engineering *GPA: 8.64*   Aug 2016 - Nov 2020

SKILLS

Language:	Verilog HDL, Python, C++
EDA Tools:	Cadence, Virtuoso, ModelSim, Quartus, Synopsys
Subject Knowledge:	Analog Electronics, Digital IC Design

INTERNSHIP

Keltron REC Industrial Training Program   Thrissur, Kerala

June 2017 –July 2017

- Area of expertise - Electronic Product Development, hardware and firmware development stages.

Livewire   Thrissur, Kerala
                                                                                                    July 2018

- Area of expertise - Internet of Things.

PROJECTS

- **Design and Implementation of Asynchronous FIFO**
https://github.com/gopika-rajendran/Design-and-implementation-of-asynchronous-FIFO-using-INTEL-FPGA
In this project, I have implemented asynchronous FIFO using INTEL FPGA and simulated using Quartus Prime. The output is verified using FPGA Board.
- **Human Fall Detection & Location Tracking**
https://github.com/gopikarajendran/HUMAN-FALL-DETECTION-AND-LOCATION-TRACKING-SYSTEM
In this project, I have implemented safety unit for elders with smart sensors and electronics that detect fall, locate the fall location and provide safety measures to elders.
- **Smart Trolley**   https://github.com/gopika-rajendran/Smart-Trolley

In this project, I implemented a smart shopping cart with RFID technology. This technology is used for boosting security performance and speed while purchasing as well as the time saved at billing counters.

COURSES

- **Python, GUVI** - ONLINE (NOVEMBER 2020, 4 WEEKS)

- **Geospatial Applications For Disaster Risk Management, ISRO** - ONLINE (OCTOBER 2020)

- **Deployment Of Application Models In Matlab And Simulink To STM32 MCUs, NIELIT Calicut**- ONLINE(15 th -17 th Sept,2020.)