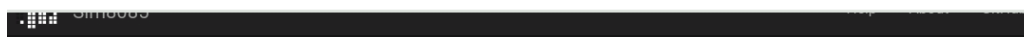




sim8085.com



2



P	<input type="checkbox"/>
C	<input type="checkbox"/>
AC	<input type="checkbox"/>

Your code has been compiled and loaded to memory location 0x0800. Now you need to execute it to see the results.



Load at 0x0800

main.asm

```

1 LXI H,1100
2 MOV B, M
3 MVI C, 00
4 INX H
5 MOV A, M
6 LOOP: CMP B
7 JC skip
8 INR C
9 JMP LOOP
10 skip: STA 1102
11 MOV A, C
12 STA 1103
13 HLT
14

```

the 65536  
ble to 8085.  
edit them.

Next

## Memory View

0x 1100

	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
110	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
111	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
112	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
113	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
114	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
115	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
116	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
117	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
118	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
119	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
11F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x 1100

## Assembler Output

1	21 4c 04	LXI H,1100
2	46	MOV B, M
3	0E 00	MVI C, 00
4	23	INX H
5	7E	MOV A, M
6	B8	LOOP: CMP B
7	DA 10 08	JC skip
8	0C	INR C
9	C3 08 08	JMP LOOP
10	32 4e 04	skip: STA 1102
11	79	MOV A, C
12	32 4f 04	STA 1103
13	76	HLT
14		

