



L	<input type="checkbox"/>
S	<input type="checkbox"/>
P	<input type="checkbox"/>
C	<input type="checkbox"/>
AC	<input type="checkbox"/>



Load at 0x0800

main.asm

```
1 LDA 4051
2 MOV B, A
3 LDA 4052
4 MOV C, A
5 MVI A, 00H
6 LOOP: ADD B
7 DCR C
8 JNZ LOOP
9 STA 4053
10 HLT
```

Warning: All the 65536 available to 8085. To edit them.

Next

Memory View

Ox	4500																
		0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
500	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
501	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
502	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
503	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
504	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
505	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
506	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
507	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
508	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
509	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50A	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50B	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50C	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50D	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50E	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00
50F	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00

Start Address at: 0x 4400

Assembler Output

1	3A d3 0f	LDA 4051
2	47	MOV B, A
3	3A d4 0f	LDA 4052
4	4F	MOV C, A
5	3E 00	MVI A, 00H
6	80	LOOP: ADD B
7	0D	DCR C
8	C2 0a 08	JNZ LOOP
9	32 d5 0f	STA 4053
10	76	HLT