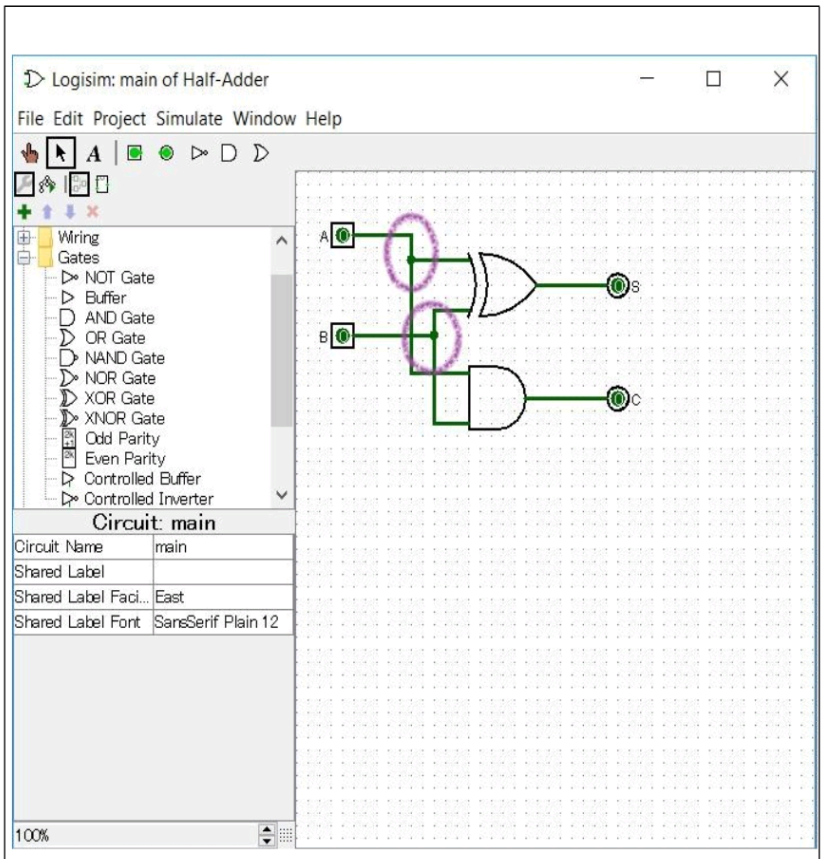

RESULT:-

Hence the designing of the 2-bit half adder using logisim simulator has been implemented successfully.

32-BIT FULL ADDER USING LOGISIM SIMULATOR

EXP.NO: 12

AIM:

To design and implement 32-bit full adder by using logisim simulator.

TRUTH TABLE:-

Row	Inputs			Outputs		Comment
	x	y	c_{in}	c_{out}	s	
0	0	0	0	0	0	$0+0+0=00_2$
1	0	0	1	0	1	$0+0+1=01_2$