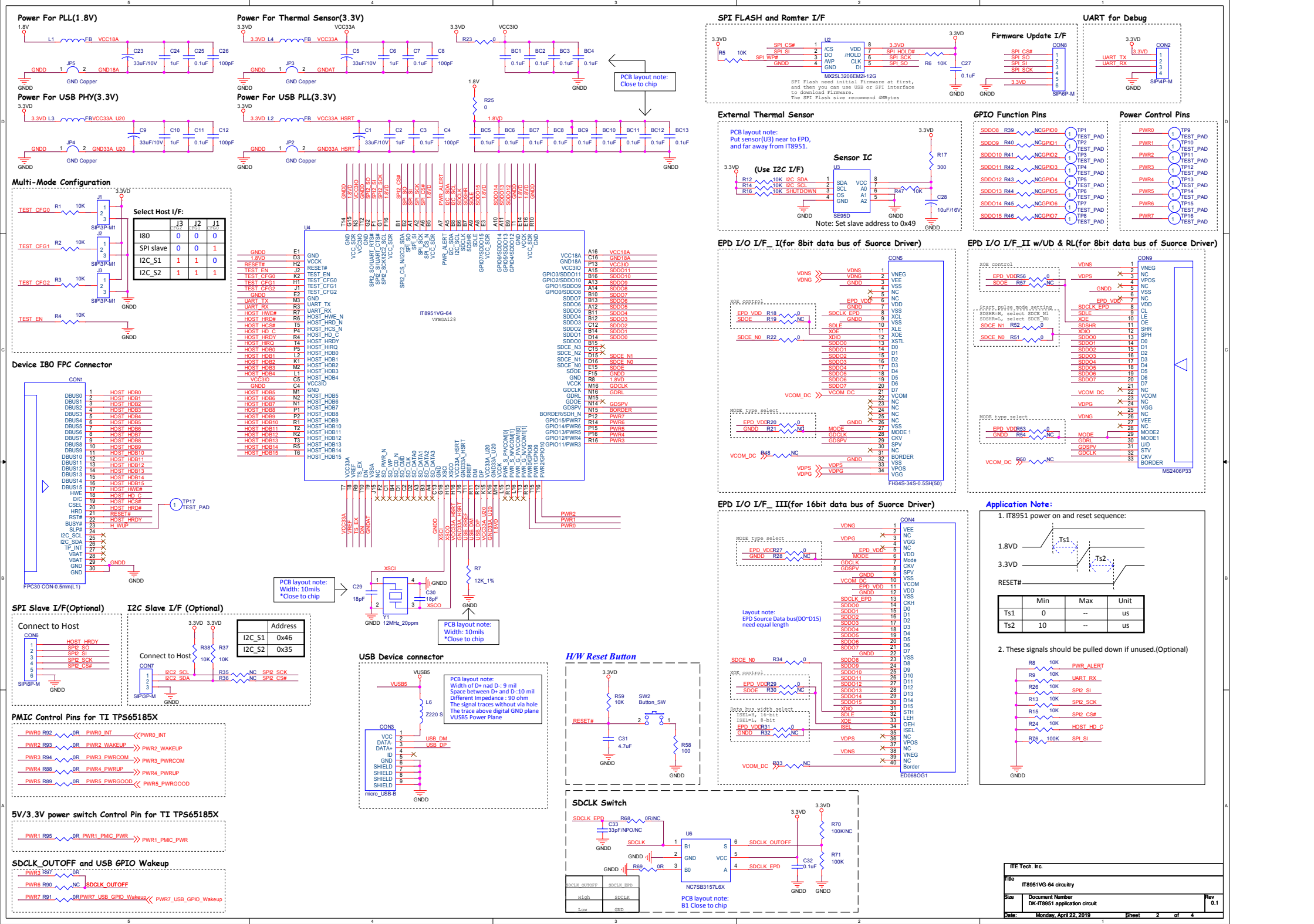
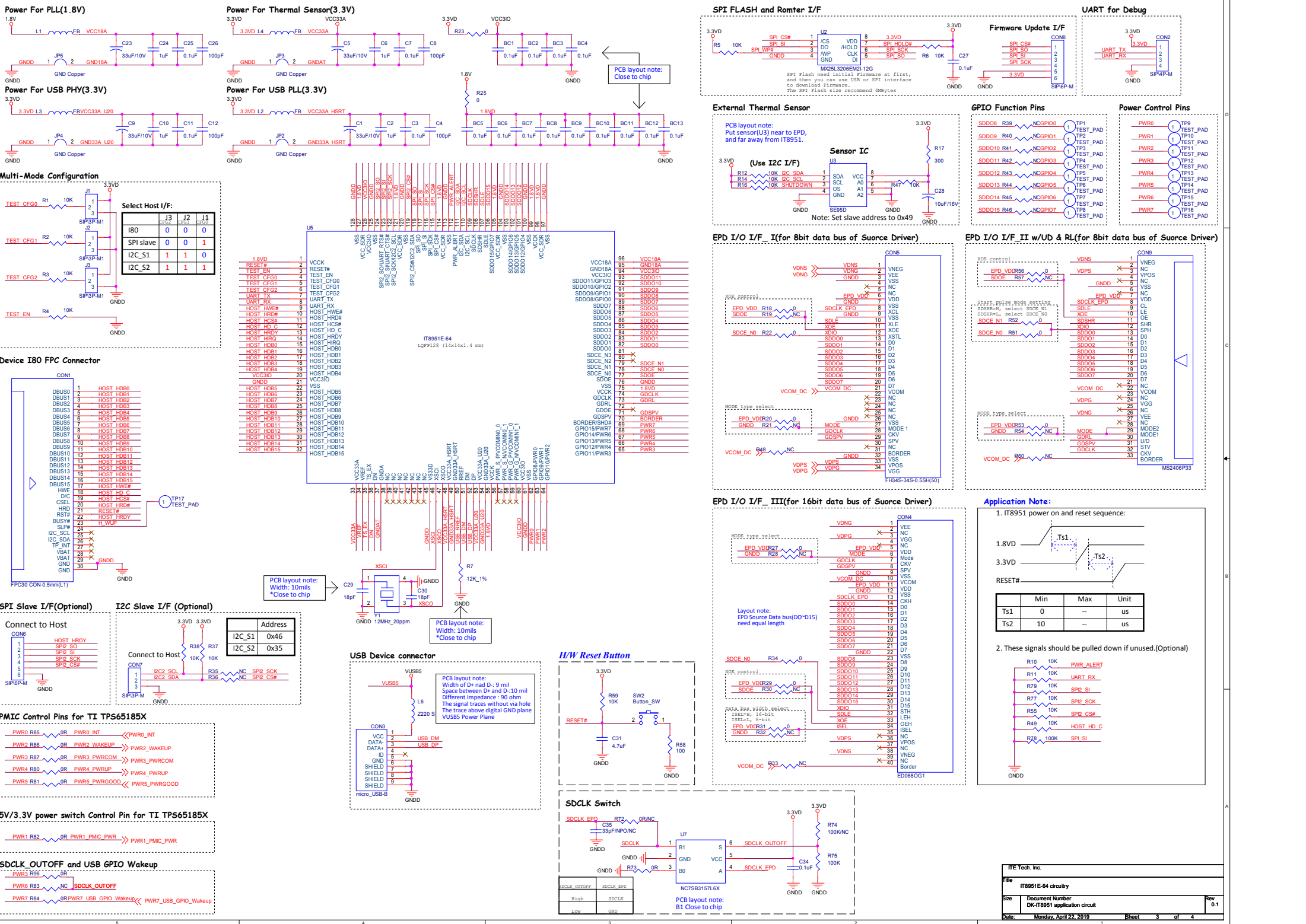


**** This is preliminary schematic release. All schematics are subject to change without notice.**

ITE Tech. Inc.		
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Note list :

Please check every note before your design.

PCB Layout Note
Application Note

Host Design note

When you using SPI2 Interface other Host pin should be NC if unused. (Optional)

HOST_HWE#
HOST_HRD
HOST_HCS
HOST_HD_C
DB0~DB16

GPIO Design note

GPIO should be NC if unused. (Optional)

GPIO0~GPIO7
GPIO8~GPIO15

SDCE Dseign note

When IT8951 control the SHR, the SDCE must be selected

SHR = 0 ; SDCE_N0 = Output

SHR = 1 ; SDCE_N1 = Output

EPD Source Data bus (D0~D15)

EPD Source Data bus (D0~D15) need equal length

SPI Flash

SPI Flash need initial Firmware at first,
and then you can use USB or SPI interface
to download Firmware.

The SPI Flash size recommend 4MBytes

Title			
Layout note			
Size	Document Number		Rev
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