

1

2

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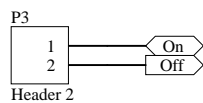
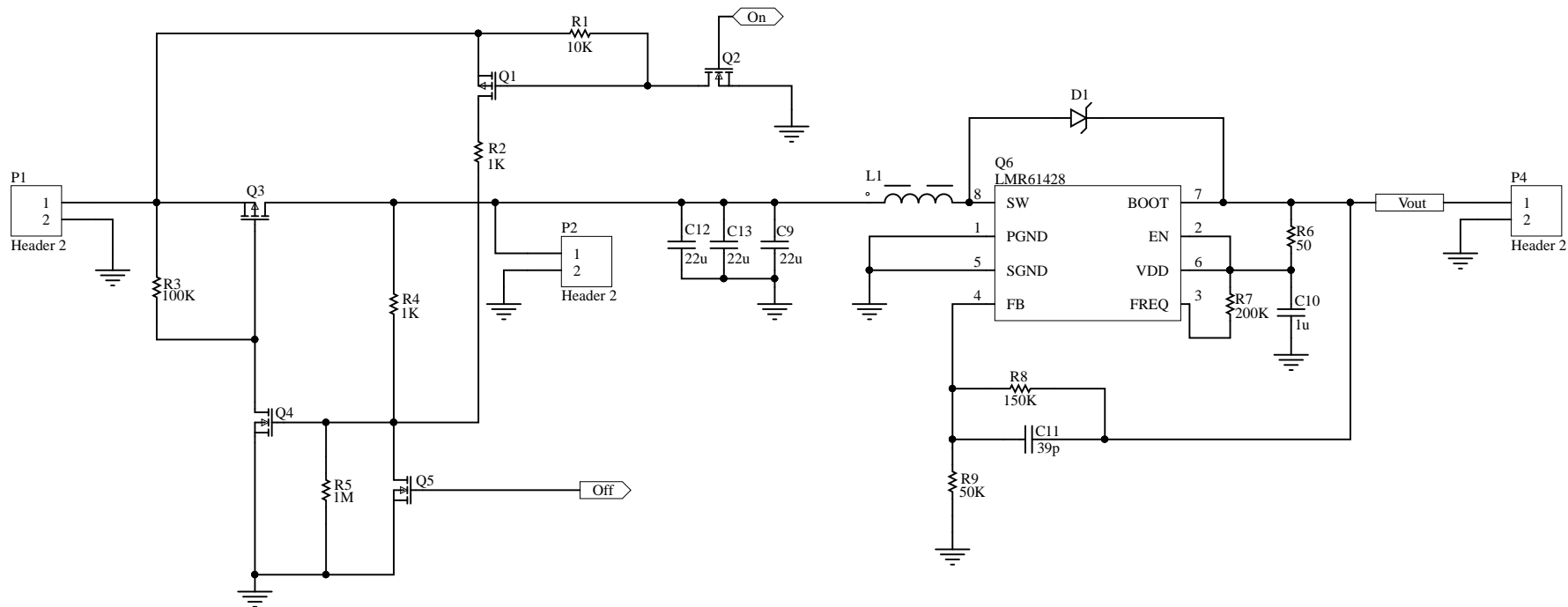
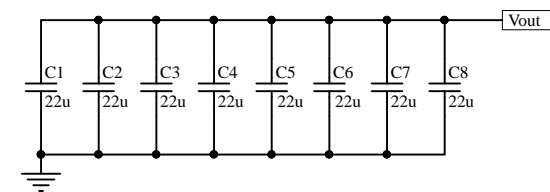
4


A

B

C

D



Title			<div>Altium Limited L3, 12a Rodborough Rd Frenchs Forest NSW Australia 2086</div> <div></div>
Size: A4	Number:	Revision:	
Date: 19.12.2019	Time: 23:19:03	Sheet of	
File: C:\HSR\SA\SA\Hardware\Layout\Powerlatch\PowerLatch\PowerLatch.SchDoc			

Comment	Description	Designator	Footprint	LibRef	Quantity
Cap	Capacitor	C1, C2, C3, C4, C5, C	CAPC3225X28N	Cap	8
Cap	Capacitor	C9, C10, C11, C12, C	C1210_M	Cap	5
Schottky		D1	DIOM4336X25N	Schottky	1
Inductor		L1	INDP120120X100N	Inductor	1
Header 2	Header, 2-Pin	P1, P2, P3, P4	HDR1X2	Header 2	4
MOSFET P-Channel		Q1, Q3	SOT95P245X110-3N	MOSFET P-Channel	2
MOSFET N-Channel		Q2, Q4, Q5	SOT95P245X110-3N	MOSFET N-Channel	3
LMR61428	Step-Up Voltage Regu	Q6	TSOP65P490X110-8N	LMR61428	1
Res1	Resistor	R1, R2, R3, R4, R5, R	RESC1608X06N	Res1	9