

Εθνικό Μετσόβιο Πολυτεχνείο Σχολή Ηλεκτρολόγων Μηχανικών και Μηχανικών Υπολογιστών Τομέας Τεχνολογίας Πληροφορικής και Υπολογιστών Εργαστήριο Μικροϋπολογιστών Χειμερινό Εξάμηνο 2021-2022

4η Εργαστηριακή Άσκηση Ηλεκτρονική κλειδαριά με αισθητήρα CO

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1. Assembly

Σημειώσεις

Οι ρουτινές εξυπηρέτησης των διακοπών του ADC και του timer1 υλοποιήθηκαν έτσι ώστε να μεταβάλλουν και να αποθηκεύουν μόνο την κατάσταση των LEDs αερίου, χωρίς να δίνουν έξοδο στα LEDs. Αυτή η έξοδος γίνεται στην κύρια ρουτίνα.

Κατά τον συναγερμό (>70ppm) τα LEDs αερίου πρέπει να αναβοσβήνουν. Αυτό υλοποιήθηκε με την βοήθεια του T flag του SREG, του οποίου η τιμή γίνεται $0x00 \rightarrow 0x01$ ή $0x01 \rightarrow 0x00$ κάθε 100ms. Κατά την πληκτρολόγηση λάθους κωδικού, η χρονοκαθυστέρηση έχει χωριστεί ως εξής:

$$4\times(0.1\times5+0.1\times5)$$

Έτσι κάθε 100ms που έχουμε καινούργια έξοδο από τον ADC, η τιμή αυτή ανανεώνεται στα LEDs.

Κώδικας

```
.DSEG
    _tmp_:.byte 2
2
    .DEF temp = r16
                             ;used for cpc instruction
    .DEF clear_lcd = r17 ; indicates if the message "CLEAR" is on the LCD (clear_lcd == 0x01)
    \rightarrow or not (clear_lcd == 0x00)
    .DEF gas_led = r18 ;stores the states of the gas_leds (PBO-PB6). We divided 1024(maximum
6
    \rightarrow number the ADC can read) by 8 = 128. That means 128 gas levels. With the first
    \rightarrow (<128) being all gas_leds off.
    .DEF gas_detected = r19 ; indicates if the message "GAS DETECTED" is on the LCD
    \rightarrow (gas detected == 0x01) or not (gas detected == 0x00)
    .CSEG
9
    .include "m16def.inc"
10
    ;Define RESET, TIMER1 overflow and ADC routines
11
    .org 0x00
    rjmp main
13
    .org 0x10
14
    rjmp ISR_TIMER1_OVF
15
    .org 0x1C
```

```
rjmp ISR_ADC
17
18
    main:
19
             ldi r24, low(RAMEND)
                                           ; Initialize stack pointer
20
             out SPL, r24
21
             ldi r24, high(RAMEND)
                                       ; RAMEND is defined in m16def.inc
22
             out SPH, r24
23
24
             clr temp
25
             clr clear_lcd
26
             clr gas_led
27
             clr gas_detected
28
             clr r28
29
             clr r20
30
             clr r21
31
             ; T flag of SREG is used for flicking the gas leds
32
33
             ldi r24, (1 << PC7)||(1 << PC6)||(1 << PC5)||(1 << PC4)
                                                                           ; Initialize 4 MSB of
35
             → PORTC as outputs
             out DDRC, r24
36
             ser r24
38
             out DDRB, r24
                                                     ; Initialize LEDs
39
             out DDRD, r24
                                                     ; Initialize PORTD(LCD) as output
40
41
             rcall ADC init
                                                      ; Initialize ADC
42
             rcall TCNT1_init
                                               ; Initialize timer1
43
44
             clr r24
45
             rcall lcd_init_sim
                                                  ;Clear LCD
46
47
             sei
                                                                     ;Enable global interrupts
48
49
             first_button:
50
                      rcall scan_keypad_rising_edge_sim
51
             mov r22, r24
             or r22, r25
53
                      cpi r22, 0
54
                      ;The cpu spends most of the time in this code block therefore we output
55
                      \rightarrow any changes to the gas LEDs states performed by the ADC interrupt.
                      out PORTB, gas_led
56
57
                      breq first_button
58
         first_ascii:
59
                                                         ; Convert to ASCII for comparison
                      rcall keypad_to_ascii_sim
60
                     mov r20, r24
                                                    ;first button
61
62
             second button:
63
                     rcall scan_keypad_rising_edge_sim
             mov r22, r24
65
             or r22, r25
             cpi r22, 0
67
```

```
; The cpu might spend some time in this code block therefore we output
68
                       \rightarrow any changes to the gas LEDs states performed by the ADC interrupt.
                      out PORTB, gas_led
69
70
                      breq second_button
71
         second_ascii:
72
                      rcall keypad_to_ascii_sim
                                                           ; Convert to ASCII for comparison
73
                      mov r21, r24
                                                     :second button
74
75
                             ; Check if the input code is correct. For our team, that's "71"
              check:
76
77
                      cpi r20, '7'
                      brne wrong
78
              cpi r21, |'1|'
79
              brne wrong
80
                       ; Past this point we have a correct password
81
              correct:
                       cli
                                                                      ;Stop global interrupts. We
83
                       → don't want any of the gas detection features during this part
84
              rcall lcd_init_sim
                                          ;Clear LCD
86
              ldi r24, W
              rcall lcd_data_sim
88
              ldi r24, 'E'
89
              rcall lcd_data_sim
90
              ldi r24, |'L'|
91
              rcall lcd_data_sim
92
              ldi r24, 'C'
93
              rcall lcd_data_sim
94
              ldi r24, '0'
95
              rcall lcd_data_sim
96
              ldi r24, 'M'
97
              rcall lcd_data_sim
              ldi r24, 'E'
99
              rcall lcd_data_sim
101
              rcall scan_keypad_rising_edge_sim ; for successful remote operation
102
103
                      ldi r24, 0x80
                                                      ;Light up
104
105
                      out PORTB, r24
                                                       ; the MSB of leds PB
106
107
                      ldi r24, low(4000)
108
                      ldi r25, high(4000)
109
                                                         ; Wait for 4 secs
                      rcall wait_msec
110
111
                      clr r24
                                                                  ; Turn off
112
                                                        ; the MSB of leds PB
                      out PORTB, r24
113
                      rcall lcd init sim
                                                   ;Clear "WELCOME"
114
                      clr gas_detected
                                                 ;LCD is empty therefore this should be 0x00
115
                      clr clear lcd
                                                       ;LCD is empty therefore this should be 0x00
116
                                                                      ; Enable global interrupts
                      sei
117
                       \hookrightarrow again
                      jmp first_button
118
```

```
119
              wrong:
120
              rcall scan_keypad_rising_edge_sim
                                                    ; for successful remote operation
121
122
                       ldi r24,4
                                                           ; Number of loops
123
                                                         ;4 loops of 0.5 sec off 0.5 on
         wrong_loop:
124
                                                          ; Save number of loops
                       push r24
125
                       ; TURN ON
126
                       ldi r24, 0x80
127
                                                         ;Get state of gas leds
                       or r24, gas_led
128
                       out PORTB, r24
                                                        ;Output to leds, both MSB and gas
129
                       ldi r24, low(100)
130
                       ldi r25, high(100)
131
                       rcall wait_msec
                                                         ; Wait 0.1 sec
132
133
                       ldi r24, 0x80
134
                       or r24, gas_led
                                                         ;Get state of gas leds
135
                       out PORTB, r24
                                                        ;Output to leds, both MSB and gas
136
                       ldi r24, low(100)
137
                       ldi r25, high(100)
                       rcall wait_msec
                                                         :Wait 0.1 sec
139
140
                       ldi r24, 0x80
141
                       or r24, gas_led
                                                         ;Get state of gas leds
142
                       out PORTB, r24
                                                        ; Output to leds, both MSB and gas
143
                       ldi r24, low(100)
144
                       ldi r25, high(100)
145
                       rcall wait_msec
                                                         ; Wait 0.1 sec
146
147
                       ldi r24, 0x80
148
                                                         ;Get state of gas leds
                       or r24, gas_led
149
                       out PORTB, r24
                                                        ;Output to leds, both MSB and gas
150
                       ldi r24, low(100)
151
                       ldi r25, high(100)
152
                                                         ; Wait 0.1 sec
                       rcall wait_msec
153
154
                       ldi r24, 0x80
155
                       or r24, gas_led
                                                         ;Get state of gas leds
156
                       out PORTB, r24
                                                        ;Output to leds, both MSB and gas
157
                       ldi r24, low(100)
158
                       ldi r25, high(100)
159
                                                         :Wait 0.1 sec
                       rcall wait_msec
160
                       ; TURN OFF
161
                       mov r24, gas_led
162
                       out PORTB, r24
163
                       ldi r24, low(100)
164
                       ldi r25, high(100)
165
                       rcall wait_msec
                                                         ; Wait 0.1 sec
166
167
                       mov r24, gas_led
168
                       out PORTB, r24
169
                       ldi r24, low(100)
170
                       ldi r25, high(100)
171
                                                         ;Wait 0.1 sec
                       rcall wait_msec
172
```

```
173
                        mov r24, gas_led
174
                        out PORTB, r24
175
                        ldi r24, low(100)
176
                        ldi r25, high(100)
177
                        rcall wait_msec
                                                           ; Wait 0.1 sec
178
179
                        mov r24, gas_led
180
                        out PORTB, r24
181
                        ldi r24, low(100)
182
                        ldi r25, high(100)
183
                                                           ;Wait 0.1 sec
                        rcall wait_msec
184
185
                        mov r24, gas_led
186
                        out PORTB, r24
187
                        ldi r24, low(100)
                        ldi r25, high(100)
189
                        rcall wait_msec
                                                           ;Wait 0.1 sec
190
191
                        mov r24, gas_led
                        out PORTB, r24
193
194
                        pop r24
195
                        subi r24, 1
                                                                ;Subtract one loop
196
                        brne wrong_loop
197
198
199
               jmp first_button
200
201
               wait_msec:
202
                        push r24
203
204
                        push r25
                        ldi r24, low(1000)
205
                        ldi r25, high(1000)
206
                        rcall wait_usec
207
                        pop r25
208
                        pop r24
209
                        sbiw r24, 1
210
                        brne wait_msec
^{211}
212
                        ret
213
214
               wait_usec:
215
                        sbiw r24, 1 ;2 cycles
216
                        nop
217
                        nop
218
                        nop
219
                        nop
220
                        brne wait_usec ;1 cycle the majority of the time
221
222
223
               ;ADC initialization routine
224
               ADC_init:
225
                       ldi r24, (1 << REFSO)
                                                         ; Vcc = 5V
226
```

```
out ADMUX, r24
227
228
                       ldi r24, (1 << ADEN) | (1 << ADIE) | (1 << ADPS2) | (1 << ADPS1) | (1 <<
                                           ;Enable ADC, Enable ADC interrupts, prescaler = 128
                       out ADCSRA, r24
230
                       ret
231
              ;TIMER1 initialization routine
232
              TCNT1_init:
233
                       ldi r24, (1 << TOIE1)
                                                     ;Enable interrupts for timer1
234
                       out TIMSK, r24
235
236
                       ldi r24, (1 << CS12) || (0 << CS11) || (1 << CS10)
                                                                                    ;Set prescaler
237
                       \rightarrow at 1024.
                       out TCCR1B, r24
238
239
                       ;Clock at 8MHz
                       ;8MHz / 1024(prescale) = 7812.5Hz
241
                       ; Interrupt every 100ms | interrupt every 0.1 * 7812.5 = 781.25 cycles
                       ; Overflow happens at 65536 cycles. Therefore start counting from 65536 -
243
                       \rightarrow 781.25 = 64755 = 0xFCF3
                       ldi r24, 0xFC
244
                       out TCNT1H, r24
245
                       ldi r24, 0xF3
246
                       out TCNT1L, r24
247
248
                       ret
249
              ;TIMER1 overflow ISR
250
              ISR_TIMER1_OVF:
251
                       push r24
252
                       ; Here we implement the gas LEDs flickering if the alarm is on. The T
253
                       \hookrightarrow flag of SREG is used for determining whether the gas LEDs should be
                       \rightarrow on/off(T flag = 0x00/0x01) for the next 100ms.
                       cpi r28, 0x01
                                             ;r28 is indicative of whether the alarm (over
                       \rightarrow 70ppm) is on or off (0x01, 0x00)
                                                ; If the alarm is off, set T and continue with ISR
                       brne continue_1
                       ; If the alarm is on then
256
                                                ; If T was clear(qas_leds off) then they should be
                       brtc continue_1
                       → ON for the next 100ms
                                   ; If T was set (gas_leds on) then they should be OFF for the
                       clt
                       \rightarrow next 100ms.
                       jmp continue_2
259
260
              continue_1:
261
                       set
262
              continue 2:
263
                                             ;Of course with have to save the state of SREG after
                       in r24, SREG
264
                       \rightarrow we modify T.
                      push r24
265
                       ldi r24, (1 << CS12) | (0 << CS11) | (1 << CS10)
266
                       out TCCR1B, r24
268
                       ldi r24, OxFC
                                                               ; Initialize counter at 64755
269
                       out TCNT1H, r24
270
                       ldi r24, 0xF3
```

```
out TCNT1L, r24
272
273
                       in r24, ADCSRA
274
                       ori r24, (1 << ADSC)
                                                     :Start AD Conversion
275
                       out ADCSRA, r24
276
277
                       pop r24
                                                                          ;restore SREG and r24
278
                       out SREG, r24
279
                       pop r24
280
281
                       reti
282
283
              ISR ADC:
284
                      push r25
285
                       push r24
286
                       in r24, SREG
                       push r24
288
                       in r24, ADCL
                                                              ;r24,r25 store the output of the ADC
290
                       in r25, ADCH
292
                       cpi r24, 0x80
                                                               ; adc < 128
293
                       cpc r25, r1
294
                       brcs long_jump_under_128;Too far for relative jump
295
296
                       cpi r24, OxCD
297
                                                                      ; adc < 205 (70ppm)
                       cpc r25, r1
298
                       brcs long_jump_under_205
299
                       ;This code is over 70ppm
300
                       ldi r28, 0x01
                                                               ;Alarm is on
301
302
                                                           ; If T is set then just calculate the
                       brts continue_adc
303
                       \rightarrow new gas_led state.
                       clr gas_led
                                                                      ; Else, a cleared T indicates
304
                       → that the gas_leds should be off
                       jmp ISR_ADC_EXIT
                                                          ;So exit the ISR
305
              long_jump_under_128:
                       jmp under_128
307
              long_jump_under_205:
308
                       jmp under_205
309
              continue_adc:
310
                                                       ; If "GAS DETECTED" message is on LCD, no
                       cpi gas_detected, 0x01
311
                       → need to show it again
                       breq dont_show_LCD
312
                       ; If not on LCD then show it
313
                       rcall lcd_init_sim
                                                            ; Clear LCD
314
315
                       ldi r24, [G]
316
                       rcall lcd data sim
317
                       ldi r24, 'A'
318
                       rcall lcd_data_sim
319
                       ldi r24, [S]
320
                       rcall lcd_data_sim
321
                       ldi r24, [ ]
322
```

```
rcall lcd_data_sim
323
                       ldi r24, [D]
324
                       rcall lcd_data_sim
325
                       ldi r24, 'E'
326
                       rcall lcd_data_sim
327
                       ldi r24, TT
328
                       rcall lcd_data_sim
329
                       ldi r24, E
330
                       rcall lcd_data_sim
331
                       ldi r24, C'
332
                       rcall lcd_data_sim
333
                       ldi r24, TT
334
                       rcall lcd data sim
335
                       ldi r24, [E]
336
                       rcall lcd data sim
337
                       ldi r24, [D]
                       rcall lcd_data_sim
339
340
                       ldi gas_detected, 0x01
                                                         ; "GAS DETECTED" on LCD, dont come back
341
                       \rightarrow here unless its off!
                       clr clear_lcd
                                                                ;"CLEAR" is not on LCD.
342
              dont_show_LCD:
343
344
                       cp r24, r1
                                                                      ; adc < 256
345
                       ldi temp, 0x01
346
347
                       cpc r25, temp
                       brcs under_256
348
349
                       cpi r24, 0x80
                                                                ; adc < 384
350
                       ldi temp, 0x01
351
                       cpc r25, temp
352
                       brcs under_384
353
354
                       cp r24, r1
                                                                      ; adc < 512
355
                       ldi temp, 0x02
                       cpc r25, temp
357
                       brcs under_512
359
                       cpi r24, 0x01
                                                                ; adc < 640
360
                       ldi temp, 0x02
361
                       cpc r25, temp
362
                       brcs under_640
363
364
                       cp r24, r1
                                                                      ; adc < 768
365
                       ldi temp, 0x03
366
                       cpc r25, temp
367
                       brcs under_768
368
369
                       cpi r24, 0x01
                                                                ; adc < 896
370
                       ldi temp, 0x03
371
                       cpc r25, temp
372
                       brcs under_896
373
374
                       ldi gas_led, 0x7F
375
```

```
jmp ISR_ADC_EXIT
376
              under_128:
377
                       clr r28
378
                       cpi clear_lcd, 0x01
                                                               ; If "CLEAR" message is on LCD, no
379
                        \hookrightarrow need to show it again
                       breq dont_show_clear_128
380
381
                       rcall lcd_init_sim
                                                              ; Clear LCD
382
383
                       ldi r24, C
384
                       rcall lcd_data_sim
385
                       ldi r24, [L]
386
                       rcall lcd data sim
387
                       ldi r24, [E]
388
                       rcall lcd data sim
389
                       ldi r24, [A]
                       rcall lcd_data_sim
391
                       ldi r24, [R]
                       rcall lcd_data_sim
393
                       ldi clear_lcd, 0x01
                                                               ; "CLEAR" on LCD, dont come back here
395
                        \hookrightarrow unless its off!
396
              dont_show_clear_128:
397
                                                            ;"GAS DETECTED" is not on LCD.
                       clr gas_detected
398
                       clr gas_led
399
                       jmp ISR_ADC_EXIT
400
401
              under_205:
402
                       clr r28
403
                       cpi clear_lcd, 0x01
404
                       breq dont_show_clear_205
405
406
                       rcall lcd_init_sim
                                                              ; Clear LCD
407
408
                       ldi r24, [C]
409
                       rcall lcd_data_sim
410
                       ldi r24, [L]
411
                       rcall lcd_data_sim
412
                       ldi r24, 'E'
413
                       rcall lcd_data_sim
414
                       ldi r24, [A]
415
                       rcall lcd_data_sim
416
                       ldi r24, [R]
417
                       rcall lcd_data_sim
418
419
                       ldi clear_lcd, 0x01
420
421
              dont_show_clear_205:
422
                       clr gas_detected
423
                       ldi gas_led, 0x01
424
                       jmp ISR_ADC_EXIT
425
426
              under_256:
427
```

```
ldi gas_led, 0x01
428
              imp ISR ADC EXIT
429
430
        under_384:
431
              ldi gas_led, 0x03
432
              jmp ISR_ADC_EXIT
433
434
        under_512:
435
              ldi gas_led, 0x07
436
              jmp ISR_ADC_EXIT
437
438
        under_640:
439
              ldi gas led, 0x0F
440
              jmp ISR_ADC_EXIT
441
442
        under_768:
443
              ldi gas_led, 0x1F
444
              jmp ISR_ADC_EXIT
445
446
        under_896:
447
              ldi gas_led, 0x3F
448
449
        ISR_ADC_EXIT:
450
              pop r24
451
              out SREG, r24
452
              pop r24
453
              pop r25
454
455
              reti
456
457
458
        scan row sim:
459
        460
        push r24 ; ????? ?????? ??? ???????? ??? ?? ?????
461
        462
        ldi r24,low(500) ; ?????????
463
        ldi r25,high(500)
464
        rcall wait_usec
465
        pop r25
466
        pop r24 ; ????? ????? ??????
467
        nop
468
        469
        470
        471
        ret ; ?? ?????????
472
473
        scan_keypad_sim:
474
        push r26 ; ????????? ???? ????????? r27:r26 ????? ????
475
        push r27 ; ????????? ???? ????? ???????
476
        477
        rcall scan row sim
478
        479
        mov r27, r24 ; ??? 4 msb ??? r27
480
        481
```

```
rcall scan_row_sim
482
         add r27, r24 ; ????????? ?? ????????? ??? 4 lsb ??? r27
483
         ldi r25 , 0x40 ; ?????? ??? ?????? ??? ?????????? (PC6: 7 8 9 C)
         rcall scan row sim
485
         swap r24 ; ????????? ?? ?????????
486
         mov r26, r24; ??? 4 msb ??? r26
487
         488
         rcall scan row sim
489
         490
         491
         492
         493
         pop r27 ; ????????? ???? ?????????? r27:r26
494
         pop r26
495
         ret
496
497
         scan_keypad_rising_edge_sim:
498
         push r22 ; ????????? ???? ????????? r23:r22 ??? ????
         push r23 ; r26:r27 ????? ???? ???????? ???? ???? ??????
500
         push r26
         push r27
502
         rcall scan_keypad_sim ; ?????? ?? ????????? ??? ????????? ????
503
         push r24 ; ??? ????????? ?? ?????????
504
         push r25
505
         506
          → $555
         507
         rcall wait msec
508
         509
         pop r23 ; ??? ??????? ???????? ??????????
510
         pop r22
511
         and r24 ,r22
512
         and r25 ,r23
513
         ldi r26 ,low( tmp ) ; ??????? ??? ???????? ??? ???????? ????
514
         ldi r27 ,high(_tmp_) ; ????????? ????? ??? ?????? ????? r27:r26
515
         ld r23 ,X+
516
         ld r22 ,X
         st X ,r24 ; ?????????? ??? RAM ?? ??? ?????????
518
         st -X ,r25 ; ??? ?????????
519
         com r23
520
         com r22 ; ???? ???? ???????? ??? ????? «?????» ???????
521
         and r24 ,r22
522
         and r25 ,r23
523
         pop r27 ; ????????? ???? ?????????? r27:r26
524
         pop r26 ; ??? r23:r22
525
         pop r23
526
         pop r22
527
         ret
528
529
         keypad_to_ascii_sim:
530
         push r26 ; ????????? ???? ????????? r27:r26 ????? ????
531
         push r27 ; ???????? ???? ??? ??????
532
         movw r26 ,r24 ; ??????? '1' ???? ?????? ??? ????????? r26 ????????
533
          : 33 33333333 3333333 333 33333333
534
```

```
ldi r24 , * * '
535
              ; r26
536
              ;C 9 8 7 D # 0 *
537
              sbrc r26,0
538
              rjmp return_ascii
539
              ldi r24 , 0'
540
              sbrc r26 ,1
541
              rjmp return_ascii
542
              ldi r24 , #'
543
              sbrc r26 ,2
544
              rjmp return_ascii
545
              ldi r24 , D
546
              sbrc r26 ,3 ; ?? ??? ????? '1'?????????? ??? ret, ?????? (?? ????? '1')
547
              rjmp return_ascii ; ?????????? ?? ??? ????????? r24 ??? ASCII ???? ??? D.
548
              ldi r24 , '7'
549
              sbrc r26,4
              rjmp return_ascii
551
              ldi r24 , 8
              sbrc r26,5
553
              rjmp return_ascii
              ldi r24 , 9'
555
              sbrc r26,6
556
              rjmp return_ascii ;
557
              ldi r24 , C
558
              sbrc r26 ,7
559
560
              rjmp return_ascii
              ldi r24 , [4] ; ?????? '1' ???? ?????? ??? ????????? r27 ????????
561
              sbrc r27 ,0; ?? ???????? ??????? ??? ????????
562
              rjmp return_ascii
563
              ldi r24 , 5'
564
              ;r27
565
              ;? 3 2 1 B 6 5 4
566
              sbrc r27 ,1
567
              rjmp return_ascii
568
              ldi r24 , 6
569
              sbrc r27 ,2
570
              rjmp return_ascii
571
              ldi r24 , B'
572
              sbrc r27 ,3
              rjmp return_ascii
574
              ldi r24 , 1 1
575
              sbrc r27,4
576
              rjmp return_ascii ;
577
              ldi r24 , 2
578
              sbrc r27 ,5
579
              rjmp return_ascii
580
              ldi r24 , 3
581
              sbrc r27,6
582
              rjmp return ascii
583
              ldi r24 , A'
              sbrc r27 ,7
585
              rjmp return_ascii
586
              clr r24
587
              rjmp return_ascii
588
```

```
return_ascii:
589
                             pop r27 ; ????????? ???? ?????????? r27:r26
590
                             pop r26
591
                             ret
592
593
                             write_2_nibbles_sim:
594
                             push r24; ????? ?????? ??? ???????? ??? ?? ?????
595
                             push \ r25 \ ; \ \ref{push} \
596
                             ldi r24 ,low(6000) ; ?????????
597
                             ldi r25 ,high(6000)
598
                             rcall wait_usec
599
                             pop r25
600
                             pop r24 ; ????? ????? ??????
601
                             push r24 ; ??????? ?? 4 MSB
602
                             in r25, PIND ; ??????????? ?? 4 LSB ??? ?? ?????????????
603
                             604
                             andi r24, 0xf0 ; ??????????????? ?? 4 MSB ???
605
                             add r24, r25 ; ??????????? ?? ?? ??????????? 4 LSB
606
                             out PORTD, r24 ; ??? ??????? ???? ?????
607
                             608
                             cbi PORTD, PD3 ; PD3=1 ??? ???? PD3=0
609
                             610
                             611
                             ldi r24 ,low(6000) ; ?????????
612
                             ldi r25 ,high(6000)
613
                             rcall wait_usec
614
                             pop r25
615
                             pop r24 ; ????? ????? ??????
616
                             pop r24 ; ??????? ?? 4 LSB. ???????? ?? byte.
617
                             swap r24 ; ???????????? ?? 4 MSB ?? ?? 4 LSB
618
                             andi r24 ,0xf0 ; ??? ?? ??? ????? ???? ???????????
619
                             add r24, r25
620
                             out PORTD, r24
621
                             sbi PORTD, PD3 ; ???? ?????? Enable
622
                             cbi PORTD, PD3
623
                             ret
624
625
                             lcd data sim:
626
                             push r24
627
                             push r25
628
                             sbi PORTD, PD2
629
                             rcall write_2_nibbles_sim
630
                             ldi r24,43
631
                             ldi r25,0
632
                             rcall wait_usec
633
                             pop r25
634
                             pop r24
635
                            ret
636
637
                             lcd_command_sim:
638
                             push r24 ; ????????? ???? ????????? r25:r24 ????? ????
639
                             push r25 ; ???????? ???? ??? ???????
640
                             cbi PORTD, PD2 ; ??????? ??? ???????? ??????? (PD2=0)
641
                             rcall write_2_nibbles_sim ; ???????? ??? ??????? ??? ??????? 39?sec
642
```

```
643
         ldi r25, 0 ; ???.: ???????? ??? ???????, ?? clear display ??? return home,
644
         645
         pop r25 ; ????????? ???? ?????????? r25:r24
646
         pop r24
647
         ret
648
649
         lcd_init_sim:
650
         push r24 ; ????????? ???? ????????? r25:r24 ????? ????
651
         push r25 : ????????? ???? ??? ???????
652
653
         654
         655
         rcall wait msec : ??????? 40 msec ????? ???? ?? ???????????
656
         ldi r24, 0x30 ; ?????? ???????? ?? 8 bit mode
657
         658
         sbi PORTD, PD3 ; ??? ?? ???????? ??????? ??? ???????
659
         660
         ldi r24, 39
661
         ldi r25, 0 ; ??? ? ???????? ??? ????????? ?? 8-bit mode
662
         663
         : ??????? 4 bit ?? ??????? ?? ????????? 8 bit
664
         push r24 ; ????? ?????? ??? ???????? ??? ?? ?????
665
         push r25 ; ????????? ??? ????????? ??????????
666
         ldi r24,low(1000); ?????????
667
         ldi r25,high(1000)
668
         rcall wait usec
669
         pop r25
         pop r24 ; ????? ????? ??????
671
         ldi r24, 0x30
672
         out PORTD, r24
673
         sbi PORTD, PD3
674
675
         cbi PORTD, PD3
         ldi r24,39
676
         ldi r25,0
677
         rcall wait_usec
678
         679
         680
         ldi r24 ,low(1000) ; ?????????
         ldi r25 ,high(1000)
682
         rcall wait_usec
683
         pop r25
684
         pop r24 ; ????? ????? ??????
685
         ldi r24.0x20 : ?????? ?? 4-bit mode
686
         out PORTD, r24
687
         sbi PORTD, PD3
688
         cbi PORTD, PD3
689
         ldi r24,39
690
         ldi r25,0
691
         rcall wait usec
692
         push r24: ????? ?????? ??? ???????? ??? ?? ?????
693
         push r25 ; ????????? ??? ????????? ??????????
694
         ldi r24 ,low(1000) ; ?????????
695
         ldi r25 ,high(1000)
696
```

```
rcall wait_usec
697
        pop r25
698
        pop r24 ; ????? ????? ??????
        ldi r24,0x28 ; ??????? ???????? ???????? 5x8 ????????
700
        rcall lcd_command_sim ; ??? ???????? ??? ?????? ????
701
        702
        rcall lcd_command_sim
703
        ldi r24,0x01 ; ????????? ??? ??????
704
        rcall lcd_command_sim
705
        ldi r24, low(1530)
706
        ldi r25, high(1530)
707
        rcall wait_usec
        709
        710
        711
        pop r25 ; ????????? ???? ????????? r25:r24
        pop r24
713
             ret
```

2.C

Σημειώσεις

Στην C η λογική του προγράμματος είναι διαφορετική από αυτή του προγράμματος assembly. Εδώ η έξοδος στα LEDs δίνεται στην ρουτίνα εξυπηρέτησης του ADC.

Κώδικας

```
\#define F\_CPU 8000000UL
    #include <avr/io.h>
    #include <util/delay.h>
    #include <avr/interrupt.h>
5
6
    #define NOP() {__asm__ __volatile__("nop");}
    int temp = 0x0000;
10
    int t_flag = 0; // όταν είναι Ο βγαζει στην εξοδο Ο, όταν είναι 1 βγάζει το επίπεδο του
11
    ⇔ αερίου
    int password = 0;//αν είναι 1 (σωστός κωδικός) κάνει το t_flag 1, για να μην
12
    🛶 αναβοσβήνουν τα 4 δευτερόλεπτα που η ειδική ομάδα είναι στην αίθουσα
    int leds = 0x00, MSB_led = 0x00; //η κατάσταση του επιπέδου του αερίου και του MLSB της
13
    \hookrightarrow portb
14
    ISR(TIMER1_OVF_vect) {
15
            TCCR1B = (1 << CS12) | (0 << CS11) | (1 << CS10);
16
            TCNT1 = 64755; //100ms
17
            ADCSRA \mid = (1<<ADSC);
19
                                                  //ανα 100ms αλλάζουμε αυτήν την τιμή για να
            if(t_flag == 0) t_flag = 1;
             → αναβοσβήσουμε τα led όταν το επίπεδο του αερίου ξεπεράσει τα όρια
            else t_flag = 0;
21
```

```
if(password == 1) t_flag = 1; //σε περίπτωση σωστού κωδικού σταματάμε την
22
                 εναλλαγή
    }
23
24
25
    //Απο τον τύπο Vgas = C*M + Vgas0 βρήκαμε οτι για C = 70ppm, Vgas = 1.
26
    //Επιπλέον η έξοδος του ADC είναι ADC = Vin * (1024/5) = 204.8*Vin, όπου Vin = Vqas.
27
    // Άρα, έχουμε συναγερμό για ADC>205
28
    //Χωρίζουμε τα επίπεδα σε 7, άρα 1024/7 = 147 τιμές το επίπεδο
29
30
    ISR(ADC_vect){
31
             if(ADC < 147) PORTB = 0x01 | (MSB_led << 7);</pre>
32
             else if(ADC >= 147 \&\& ADC < 205) PORTB = 0x03 | (MSB led << 7);
33
             else if(t_flag == 1){
34
                      if(ADC >= 205 \&\& ADC < 294) leds = 0x03;
35
                      else if(ADC >= 294 \&\& ADC < 441) leds = 0x07;
                      else if(ADC >= 441 \&\& ADC < 588) leds = 0x0F;
37
                      else if(ADC >= 588 \&\& ADC < 735) leds = 0x1F;
                      else if(ADC >= 735 && ADC < 882) leds = 0x3F;
39
                      else if(ADC >= 882) leds = 0x7F;
                      PORTB = leds | (MSB_led << 7);</pre>
41
             }
42
             else {
43
                      PORTB = MSB_led << 7;</pre>
44
             }
45
46
47
48
    void adc_init(){
49
             ADMUX = (1 << REFSO);
50
             ADCSRA = (1<<ADEN) | (1<<ADIE) | (1<<ADPS1) | (1<<ADPS1) | (1<<ADPS0);
51
52
53
    int scan_row_sim(int r25) {
54
             PORTC = r25;
             int r24;
56
             _delay_us(500);
             NOP();
58
             NOP();
59
             r24 = PINC;
60
             r24 = r24 \& OxOF;
61
             return r24;
62
63
    }
64
65
    //a is LSB, b is MSB
66
    void scan_keypad_sim(int *a, int *b) {
67
68
             int r25 = 0x10, r24, r27, r26;
69
             r24 = scan_row_sim(r25);
             r27 = ((r24 \& 0x0F) << 4 | (r24 \& 0xF0) >> 4); //swap r24
71
72
             r25 = 0x20;
73
             r24 = scan_row_sim(r25);
```

```
r27 += r24;
75
76
              r25 = 0x40;
77
              r24 = scan_row_sim(r25);
78
              r26 = ((r24 \& 0x0F) << 4 | (r24 \& 0xF0) >> 4); //swap r24
79
80
              r25 = 0x80;
81
              r24 = scan_row_sim(r25);
82
              r26 += r24;
83
              *a = r26;
84
              *b = r27;
85
 86
              PORTC = 0x00;
87
              return;
 88
     }
89
     void scan_keypad_rising_edge_sim (int time, int *a, int *b) {
91
              int r22, r23, r24, r25;
92
               int r26;
93
              scan_keypad_sim(&r24, &r25);
              _delay_ms(15);
95
              scan_keypad_sim(&r22, &r23);
              r24 = r24 \& r22;
97
              r25 = r25 \& r23;
98
              r26 = temp;
99
              r25 = r25 << 8;
100
              temp = r25 | r24;
101
              r26 = ~r26;
102
              *a = r24 \& (r26 \& 0x00FF);
103
              *b = (r25 >> 8) & ((r26 & 0xFF00) >> 8);
104
105
106
              return;
107
              }
108
109
     int keypad_to_ascii_sim (int r24, int r25) {
110
              int r26 = '0';
111
              switch(r24) {
112
                       case 0x01:
113
                                r26 = '*';
114
                                break;
115
                       case 0x02:
116
                                r26 = '0';
117
                                break;
118
                       case 0x04:
119
                                r26 = '#';
120
                                break;
121
                       case 0x08:
122
                                r26 = 'D';
123
                                break;
124
                       case 0x10:
125
                                r26 = '7';
126
                                break;
127
                       case 0x20:
128
```

```
r26 = '8';
129
                                  break;
130
                        case 0x40:
131
                                  r26 = '9';
132
                                  break;
133
                        case 0x80:
134
                                  r26 = 'C';
135
                                  break;
136
                        }
137
               switch(r25) {
138
                        case 0x01:
139
                        r26 = '4';
140
                        break;
141
                        case 0x02:
^{142}
                        r26 = '5';
143
                        break;
144
                        case 0x04:
145
                        r26 = '6';
146
                        break;
147
                        case 0x08:
148
                        r26 = 'B';
149
                        break;
150
                        case 0x10:
151
                        r26 = '1';
152
                        break;
153
                        case 0x20:
154
                        r26 = '2';
155
                        break;
156
                        case 0x40:
157
                        r26 = '3';
158
                        break;
159
                        case 0x80:
160
                        r26 = 'A';
161
                        break;
162
               }
163
164
               return r26;
165
166
167
168
     int main(void)
169
170
          DDRB = OxFF;
                                           //B as output
171
               DDRC = OxFO;
                                                //PCO-3 input and PC4-PC7 output (for the 4x4
172
               \rightarrow keypad)
               adc_init();
173
               TIMSK = (1 << TOIE1);
174
               TCCR1B = (1 << CS12) | (0 << CS11) | (1 << CS10);
175
               TCNT1 = 64755;
176
               sei();
               /* Replace with your application code */
178
          while (1)
179
180
                          int r24, r25, c1, c2;
181
```

```
182
                       do {
183
                               scan_keypad_rising_edge_sim(15, &r24, &r25);
184
                               c1 = keypad_to_ascii_sim(r24, r25);
185
                       }
186
                                                            //while there is no pressed key,
                       while(r24 == 0 \&\& r25 == 0);
187
                       → continue to scan if sth is pressed
                       do
188
                       {
189
                               scan_keypad_rising_edge_sim(15, &r24, &r25);
190
                               c2 = keypad_to_ascii_sim(r24, r25);
191
                       }
192
                       while (r24 == 0 \&\& r25 == 0);
                                                            //continue scanning for the 2nd
193
                       \hookrightarrow digit
                       if (c1 == '7' && c2 == '1') {
                                                            //if the digit were 7 and then 1
194
                               password = 1; //correct password
195
                               MSB led = 1;
196
                               PORTB = 0x80 \mid leds;
                                                                                      //switch on
                               → the PB7 and the state of the leds for 4 seconds
                               for(int i = 0; i < 190; i++) { //for 4 seconds
198
                                       scan_keypad_rising_edge_sim(15, &r24,
199
                                                          //while scanning also (required in
                                        _delay_ms(1);
200
                               }
201
                               PORTB = 0x00 \mid leds;
                                                                                      //after the
202
                               \rightarrow 4 seconds switch off the PB7
                               MSB_led = 0;
203
                               password = 0;
204
205
                      }
206
                       else {
                                                                                        //if the
207
                       \hookrightarrow digits were not 7 and 1
                               for (int i = 0; i < 4; i++) { //switch on and off the
208
                                \rightarrow leds 4 times
                                       MSB_led = 1;
209
                                       for(int i = 0; i < 35; i++) {
                                                                              //for 0.5 sec each
210
                                                scan_keypad_rising_edge_sim(15, &r24,
211
                                                //while scanning (remote
                                                → access program)
                                                _delay_ms(1);
212
                                       }
213
                                       MSB_led = 0;
214
                                       for(int i = 0; i < 35; i++) {
215
                                                scan_keypad_rising_edge_sim(15, &r24, &r25);
216
                                                _delay_ms(1);
217
                                       }
218
219
                               }
220
                       }
221
222
         }
223
     }
224
```