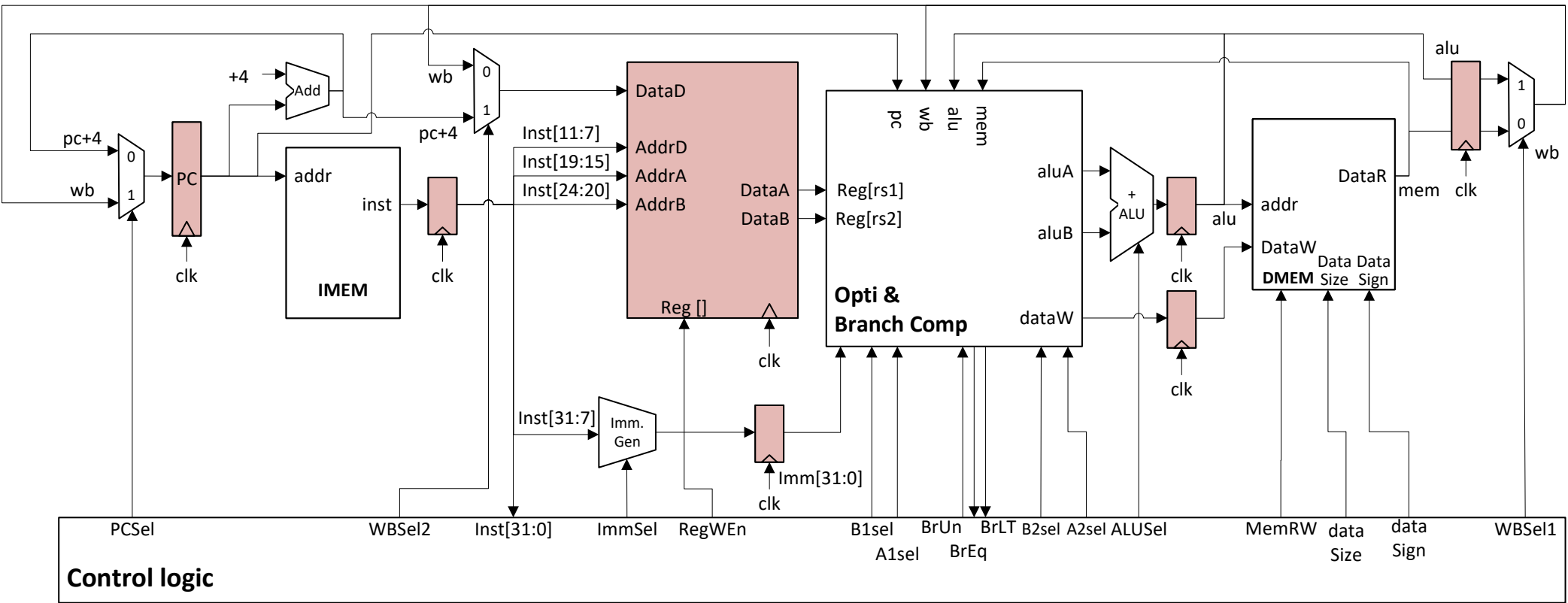
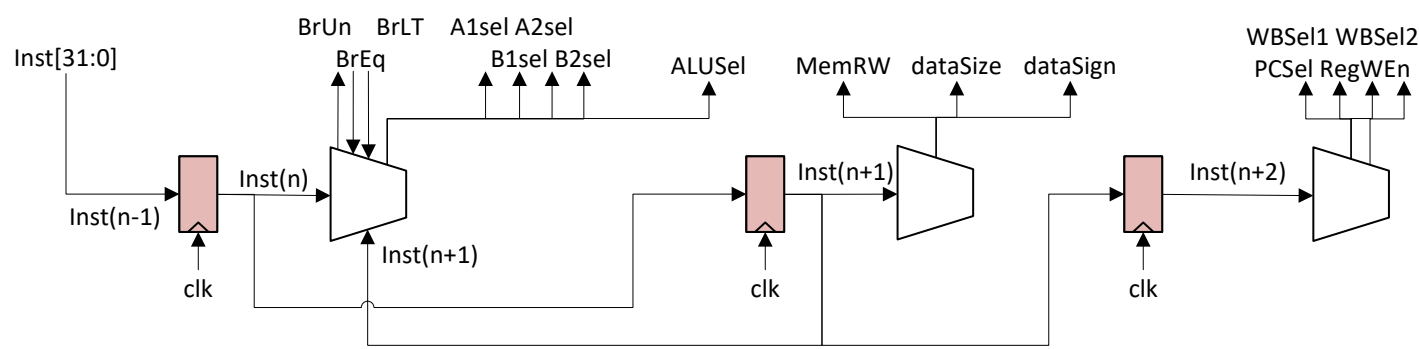


RISC V Project main architecture



Control logic architecture



Opti & Branch Comp architecture

