

# Sai Govardhan

[saigov14@gmail.com](mailto:saigov14@gmail.com) / [saigovardhanmc@pesu.pes.edu](mailto:saigovardhanmc@pesu.pes.edu)

Phone: +91 6360222109, [github.com/govardhnn](https://github.com/govardhnn), [linkedin.com/in/saigovardhan](https://linkedin.com/in/saigovardhan)

I'm an Electronics and Communication student (B.Tech) at PESU Bangalore, with a VLSI domain Specialization and a passion for Digital Design and Computer Architecture.

Education	<b>PES University</b> , RR Campus, Bangalore (2019 – present) BTech in Electronics and Communication Engineering VLSI Specialization Grade up to 8th SEM: 7.59 CGPA
	<b>Allen Career Institute</b> , Basaveshwara Nagar, Bangalore (2019) PESSAT: AIR 1017, JEE Mains 93.61 Percentile
	<b>Presidency School</b> , Nandini Layout, Bangalore (2017) ICSE, Grade 93.67 Percentage
Experience	<b>VLSI Design Intern</b> , (January 2023 - present) Gyanlakshmi Innovations, IIIT Bangalore Working on Computer Architecture, Benchmarking, Memory, and Cognitive Computing, under Dr. G N Srinivasa Prasanna.
	<b>AI Hardware Research Intern</b> , (January 2023 – present) Centre for Innovation and Entrepreneurship, PES University Researching Hardware Architectures for AI and ML workloads, with hands-on exposure to Intel FPGAs and Accelerators, under Prof. Sathya Prasad.
	<b>Satellite Architecture Research Intern (Hardware Domain)</b> , (September 2023 – present) OrbitAID Aerospace, Indian Institute of Science, Bangalore (Work bound by Non-Disclosure Agreement)
	<b>Student Teaching Assistant</b> , PES University
	<ul style="list-style-type: none"><li>• <b>Embedded Firmware Development with UEFI</b> (2023) Using the TianoCore's EDKII platform to demonstrate the UEFI shell.</li><li>• <b>Synthesis, Physical Design and Timing Analysis of Digital Circuits</b> (2023) Implementing HDL to Floorplan demos on the Mentor Oasys tool, and guiding analysis on physical design characteristics and timing analysis.</li><li>• <b>Digital System Design</b> (2022) Was responsible for implementing Advanced Digital Design concepts on the Cadence Tools, with emphasis on hands-on projects for over 190+ students.</li></ul>
Projects	<b>B.Tech Capstone Project</b> , (2022) <b>Hardware Accelerator for Data Sorting</b> , PES University Designed a novel hardware architecture to provide efficient sorting of data, with analysis of different sorters and their low power variants (Total of 14 implementations at 50MHz) under the guidance of Dr. Sudeendra Kumar.
	<b>Project Intern, FarmBot</b> , (2021) Center for Internet of Things, PES University Built a household farming automation bot using CNC principles, with the main functionality of watering and detecting plants and weed. Was in-charge of the electronics domain of the project, and guided the next batch of hardware interns.

<b>Skills</b>	<p><b>Digital Design</b> (ASIC Design, FPGA Prototyping) using Verilog and System Verilog, Static Timing Analysis, Equivalence Checking, Synthesis,  <b>Functional and Formal Verification</b> (Assertions), Coverage,  <b>Computer Architecture</b>, Embedded Systems Design, Hardware Security, C Programming, IoT, Entrepreneurship,  <b>Tools:</b> Cadence (NCSim, Genus, Tempus, Incisive Metrics Center, Conformal LEC, IFV), Mentor (Oasys, QuestaSim), Xilinx Vivado, Intel Quartus.</p>
<b>Certifications</b>	<p>Cadence Digital Badge Programme (Credly Link: <a href="http://www.credly.com/users/sai-govardhan/badges">http://www.credly.com/users/sai-govardhan/badges</a>)</p> <ul style="list-style-type: none"> <li>• Genus Synthesis Solution with Stylus Common UI v21.1</li> <li>• Low-Power Synthesis Flow with Genus Stylus Common UI v21.1</li> <li>• Conformal Equivalence Checking v22.1</li> <li>• Basic Static Timing Analysis v2.0</li> <li>• Tempus Signoff Timing Analysis and Closure v21.1</li> <li>• Fundamentals of IEEE 1801 Low-Power Specification Format v8.0</li> <li>• Cadence RTL-to-GDSII Flow v4.0</li> <li>• Joules Power Calculator v21.1</li> </ul>
<b>Awards</b>	<p>Won <b>2nd place at the Hackezee Hackathon</b> (2021)  for the project- Gesture Controlled Rescue Vehicle in the electronics hackathon organized by the ECE Department PESU.</p> <p>Won <b>3rd place at the Gutsy Entrepreneur 2.0 Contest</b> (2020)  for the EmoBuild business idea, in the entrepreneurship hackathon organized by the Centre of Innovation and Entrepreneurship PESU.</p> <p>Won <b>2nd place at Pioneer</b> (2020)  at the Business Modelling Contest organized by the Centre of Innovation and Entrepreneurship PESU.</p> <p><b>Distinction Awards</b> for the I, II, V and VII semesters for securing more than 7.75 SGPA.</p> <p>Won the <b>Most Disciplined Outgoing Student award</b>, (2017)  at Presidency School, Nandini Layout.</p> <p><b>Won 3rd place at Cryptonite</b> (2016)  a cryptography contest in the Synchronize fest at Bishop Cottons School, Bangalore.</p>
<b>Activities</b>	<p><b>Mental and Emotional Wellness (MEW) PESU</b> – Worked with MEW group in the 3rd Semester of Engineering and moderated three online Emotional Connect (EMC) sessions from March to June 2020</p> <p><b>PESU Student Support Platform</b> – Proposed and helped build: <a href="http://ssp.pes.edu">ssp.pes.edu</a> in the 1st semester of Engineering.</p>
<b>Languages</b>	English, Kannada, Telugu, and Hindi.
<b>Website</b>	<a href="https://govardhnn.github.io">https://govardhnn.github.io</a>