# Sai Govardhan

CONTACT Information	saigov14@gmail.com govardhnn.github.io	linkedin.com/in/saigovardhan github.com/govardhnn	
EDUCATION	<ul> <li>B.Tech in Electronics and Communication</li> <li>PES University, Bangalore</li> <li>VLSI Specialization</li> <li>CGPA: 7.71/10, Capstone: 10/10</li> </ul>	on Engineering	2019 - 2023
Experience	CPU uArch Design Engineer InCore Semiconductors, Bangalore		Nov 2023 – Present
	RTL Design Intern InCore Semiconductors, Chennai		July 2023 – Oct 2023
	VLSI Design Intern International Institute of Information Technol	$logy,\ Bangalore$	Jan 2023 – June 2023
	Hardware Accelerator Research Intern Centre for Innovation and Entrepreneurship,	$PES\ University$	Jan 2023 – June 2023
	Electronics Research Intern OrbitAID Aerospace, Indian Institute of Scien	$ace,\ Bangalore$	Sept 2022 – Dec 2022
	Project Intern – FarmBot Center for Internet of Things, PES University	y	June 2021 – Sept 2021
TEACHING	Embedded Firmware Development with UEFI [GitHub Link] Student Teaching Assistant, PES University		
	Synthesis, Physical Design and Timing Analysis of Digital Circuits [Manual] Student Teaching Assistant, PES University		
	Digital System Design [GitHub Link] [M. Student Teaching Assistant, PES University	Ianual]	
Publications	Low Power Multidimensional Sorters using Clock Gating and Index Sorting Samahith S A, Sai Govardhan, Manogna R, Hitesh D, Dr. Sudeendra Kumar K In the IEEE International Conference on Electronics, Computing and Communication Technologies (CONECCT), July 2023 [Paper Link]		
Workshops	CIE Summer Workshop		March 2023 - June 2023

Was the FPGA Track lead of the CIE Summer workshop, which involved guiding four teams for four weeks on the fundamentals of mapping and implementing algorithms to

hardware architectures on the Intel DE-10 FPGA Board. [GitHub Link]

 $PES\ University,\ RR\ Campus,\ Bangalore$ 

#### CIOT Workshop

October 2021

PES University, RR Campus, Bangalore

Presented the Farmbot project in which I was the electronics lead, and trained students with IOT fundamentals using dev-kits for two days. [GitHub Link]

#### CERTIFICATIONS

## **Advanced Computer Architecture**

NPTEL Online Certification [Certificate Link]

## Genus Synthesis Solution with Stylus Common UI v21.1

Cadence Digital Badge Programme [Credly Link]

# Low-Power Synthesis Flow with Genus Stylus Common UI v21.1

Cadence Digital Badge Programme [Credly Link]

## Conformal Equivalence Checking v22.1

Cadence Digital Badge Programme [Credly Link]

## Basic Static Timing Analysis v2.0

Cadence Digital Badge Programme [Credly Link]

## Tempus Signoff Timing Analysis and Closure v21.1

Cadence Digital Badge Programme [Credly Link]

# Fundamentals of IEEE 1801 Low-Power Specification Format v8.0

Cadence Digital Badge Programme [Credly Link]

#### Cadence RTL-to-GDSII Flow v4.0

Cadence Digital Badge Programme [Credly Link]

## Joules Power Calculator v21.1

Cadence Digital Badge Programme [Credly Link]

#### AWARDS

## Won the Certificate of Appreciation

Was one of the six recipients of the appreciation award for the graduating batch of BTech ECE at PES University, for my contributions to the VLSI Domain

## Won 2nd place at the Hackezee Hackathon

For the IoT and sensors project- 'Gesture Controlled Rescue Vehicle' in the flagship hackathon organized by the ECE Department PESU

#### Won 3rd place at the Gutsy Entrepreneur 2.0 Contest

For the EmoBuild (Emotional Intelligence - Build Platform) business idea and prototype app design at the 14-day hackathon organized by CIE PESU

# Won 2nd place at Pioneer

The Business Modelling Contest, by presenting creative strategies for existing businesses navigating the pandemic, in an event organized by CIE PESU

## Distinction Awards for the I, II, V and VI semesters

by the ECE Department, PES University

# Won the Most Disciplined Outgoing Student award

at Presidency School, Nandini Layout