Ch 3. Arithmetic for Computers



3.2 정수의 뺄셈

Signed Binary Subtraction: 7-6

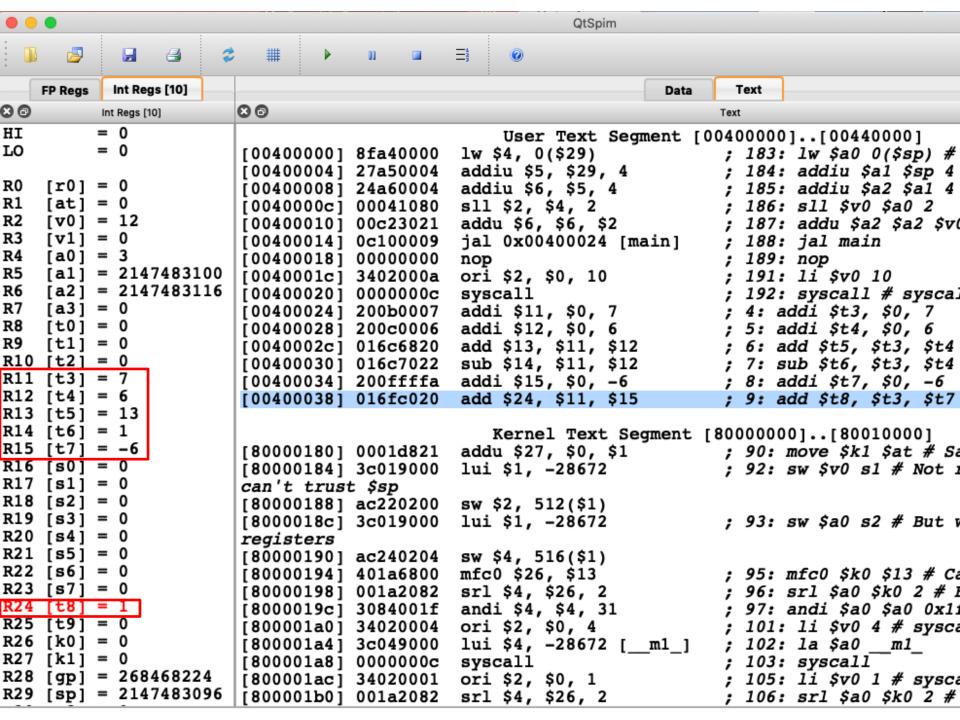
• 7 + (-6)

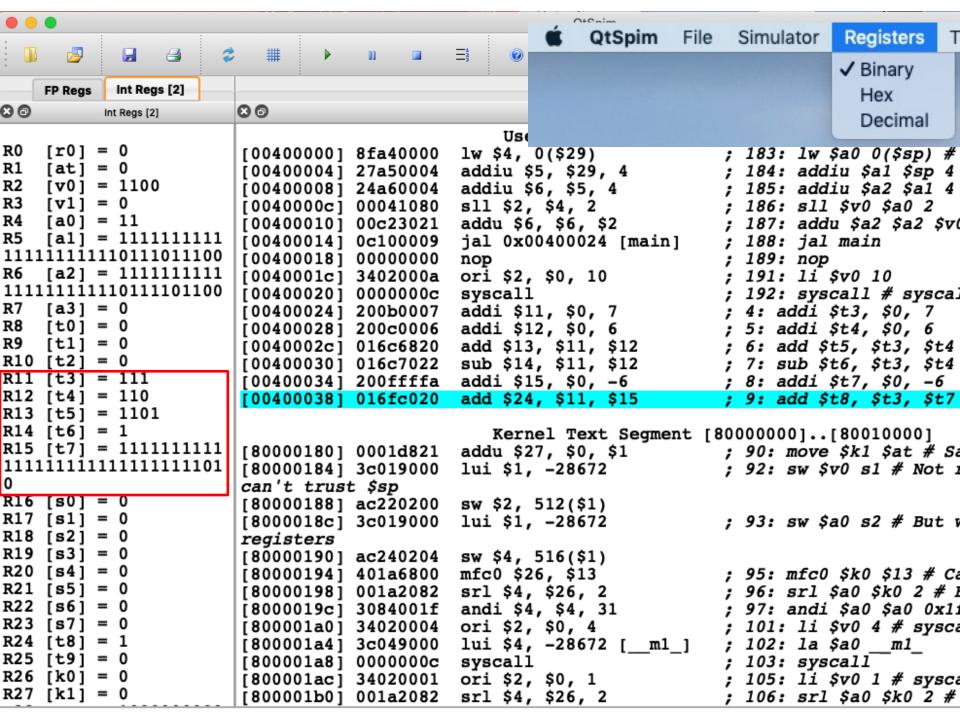
```
0000 0000 0000 0000 0000 0000 0000 0110
+6
의 1의 보수 1111 1111 1111 1111 1111 1111 1001
          +
  0000 0000 0000 0000 0000 0000 0000 0111<sub>two</sub>
  0000 0000 0000 0000 0000 0000 0000 0001_{two} = 1_{ten}
```

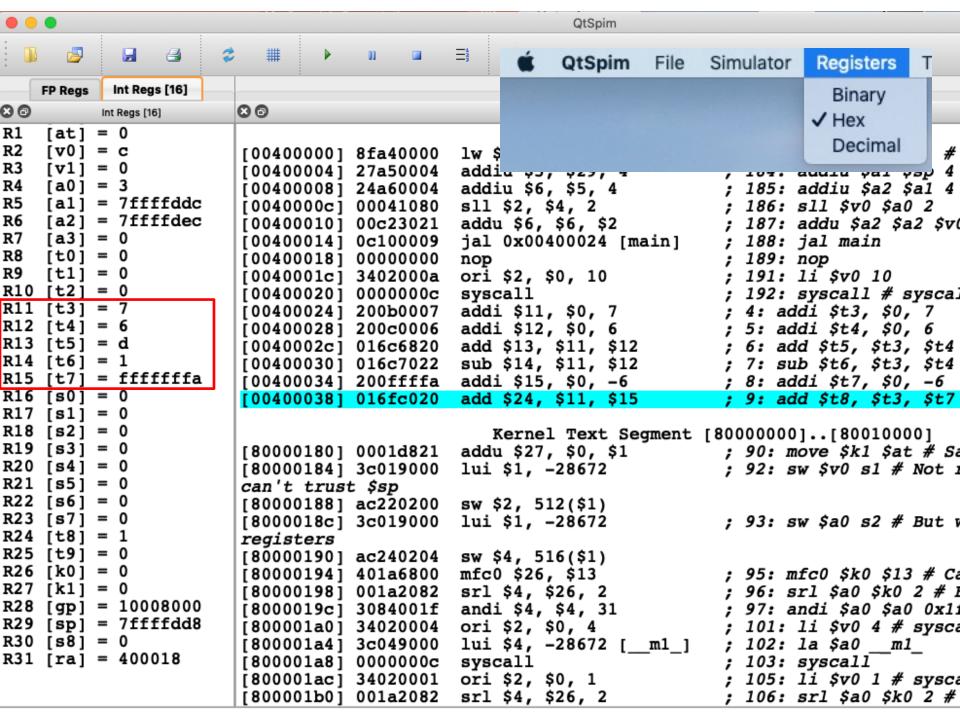
MIPS source file

```
.text
.globl main
main:
   addi $t3, $0, 7
   addi $t4, $0, 6
    add $t5, $t3, $t4 # 7 + 6
    sub $t6, $t3, $t4 # 7 - 6
    addi $t7, $0, -6
    add $t8, $t3, $t7 # 7 + (-6)
```

```
initialize (reset) simulator QtSpim
      <u>_</u>
           00
                                        = 1
   FP Regs
          Int Regs [10]
                                                                 Data
                                                                        Text
                       80
80
         Int Regs [10]
                                                                       Text
PC
         = 0
                                                 User Text Segment [00400000]..[00440000]
EPC
                       [00400000] 8fa40000
                                             lw $4, 0($29)
                                                                       ; 183: lw $a0 0($sp) #
         = 0
Cause
                       [00400004] 27a50004
                                             addiu $5, $29, 4
                                                                       ; 184: addiu $a1 $sp 4
         = 0
                                             addiu $6, $5, 4
BadVAddr = 0
                       [00400008] 24a60004
                                                                       ; 185: addiu $a2 $a1 4
                       [0040000c] 00041080
                                                                       ; 186: sll $v0 $a0 2
Status
         = 805371664
                                             sll $2, $4, 2
                       [00400010] 00c23021
                                             addu $6, $6, $2
                                                                        187: addu $a2 $a2 $v(
HI
         = 0
                       [00400014] 0c100009
                                             jal 0x00400024 [main]
                                                                       ; 188: jal main
LO
         = 0
                       [00400018] 00000000
                                                                       ; 189: nop
                                             nop
                       [0040001c] 3402000a
                                                                       ; 191: li $v0 10
                                             ori $2, $0, 10
                       [00400020] 0000000c
R0
                                             syscall
                                                                       ; 192: syscall # syscal
    [r0] = 0
                       [00400024] 200b0007
                                             addi $11, $0, 7
R1
    [at] = 0
                                                                       ; 4: addi $t3, $0, 7
R2
    [v0] = 0
                       [00400028] 200c0006
                                             addi $12, $0, 6
                                                                       ; 5: addi $t4, $0, 6
R3
    [v1] = 0
                       [0040002c] 016c6820
                                             add $13, $11, $12
                                                                       ; 6: add $t5, $t3, $t4
                                             sub $14, $11, $12
R4
    [a0] = 3
                       [00400030] 016c7022
                                                                       ; 7: sub $t6, $t3, $t4
                       [00400034] 200ffffa
                                             addi $15, $0, -6
                                                                       ; 8: addi $t7, $0, -6
R5
    [a1] = 2147483100
    [a2] = 2147483116
                       [00400038] 016fc020
R6
                                             add $24, $11, $15
                                                                       ; 9: add $t8, $t3, $t7
    [a3] = 0
R7
R8
    [t0] = 0
                                                Kernel Text Segment [80000000]..[80010000]
R9
    [t1] = 0
                       [80000180] 0001d821
                                             addu $27, $0, $1
                                                                       ; 90: move $k1 $at # Sa
R10 [t2] = 0
                       [80000184] 3c019000
                                             lui $1, -28672
                                                                       ; 92: sw $v0 s1 # Not 1
R11 [t3] = 0
                       can't trust $sp
R12 [t4] = 0
                       [80000188] ac220200
                                             sw $2, 512($1)
R13 [t5] = 0
                       [8000018c] 3c019000
                                             lui $1, -28672
                                                                       ; 93: sw $a0 s2 # But v
R14 [t6] = 0
                       registers
R15 [t7] = 0
                       [80000190] ac240204
                                             sw $4, 516($1)
                                             mfc0 $26, $13
R16 [s0] = 0
                       [80000194] 401a6800
                                                                       ; 95: mfc0 $k0 $13 # Ca
                                                                       ; 96: srl $a0 $k0 2 # 1
R17 [s1] = 0
                       [80000198] 001a2082
                                             srl $4, $26, 2
                       [8000019c] 3084001f
R18 [s2] = 0
                                             andi $4, $4, 31
                                                                       ; 97: andi $a0 $a0 0x1:
                                                                         101: li $v0 4 # sysca
R19 [s3] = 0
                       [800001a0] 34020004
                                             ori $2, $0, 4
                                             lui $4, -28672 [
R20 [s4] = 0
                       [800001a4] 3c049000
                                                                       ; 102: la $a0 m1
                                                               m1 ]
                                                                       ; 103: syscall
R21 [s5] = 0
                       [800001a8] 0000000c
                                             syscall
                       [800001ac] 34020001
R22 [s6] = 0
                                             ori $2, $0, 1
                                                                       ; 105: li $v0 1 # sysca
R23 [s7] = 0
                       [800001b0] 001a2082
                                             srl $4, $26, 2
                                                                       ; 106: srl $a0 $k0 2 #
```







3.2 정수의 뺄셈

- a b = a + (-b) 로 계산
- 즉, b 를 양수/음수 관계없이 –b (2의 보수) 로 만들어 덧셈을 한다.

Signed Binary Subtraction: 7-6

• 7 + (-6)

```
0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0111_{two} = 7_{ten} + 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1010_{two} = -6_{ten} = 4\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0001_{two} = 1_{ten}
```

2,147,483,647 - (-2)

- a b = a + (-b) 로 계산
- 즉, b 를 양수/음수 관계없이 -b (2의 보수) 로 만들어 덧셈을 한다.

```
0111 1111 1111 1111 1111 1111 1111 = 2147483647
1111 1111 1111 1111 1111 1111 1110 = -2
0111 1111 1111 1111 1111 1111 1111 = 2147483647
```

 $0000\ 0000\ 0000\ 0000\ 0000\ 0000\ 0010 = -2 \ 2$'s complement

1000 0000 0000 0000 0000 0000 0001 \rightarrow ? = (-2147483647 not +2147483649)

Overflow

• 연산의 결과가 32-bit word 로 표현될 수 없을 때

	Operation	Operand A	Operand B	Result indicating overflow				
\Rightarrow	A + B	/ ≥0	≥ 0	< 0				
	A + B	< 0	< 0	≥ 0				
	A – B	≥0	50	< 0				
	A B	< 0	≥ 0	≥ 0				
	0111 1111 1111 1111 1111 1111 1111 = 2147483647							
	0111 1111 1111 1111 1111 1111 1111 1111 = 2147483647							
	1111 1111 1111 1	1111 1111 11	11 1111 1110	$\overline{0} = -2$				

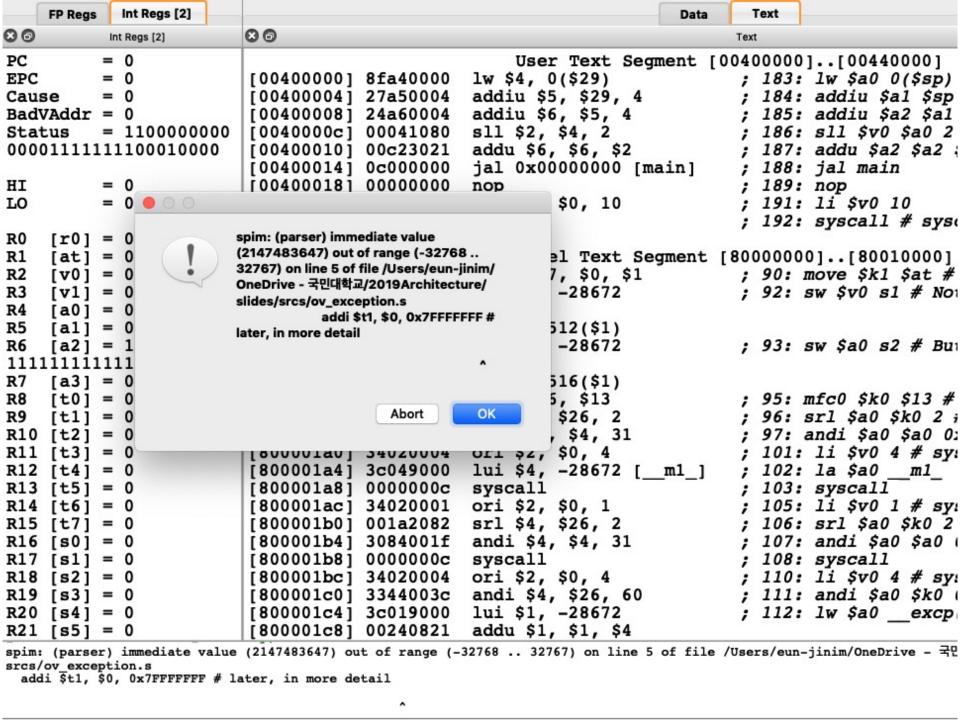
→ overflow exception 발생

add/addi/sub instructions

- add/addi/sub 명령어는 연산 결과 overflow 가 되면 exception 을 발생한다.
- exception (예외) : 프로그램의 정상적인 수행을 방해하는 계획되지 않은 사건

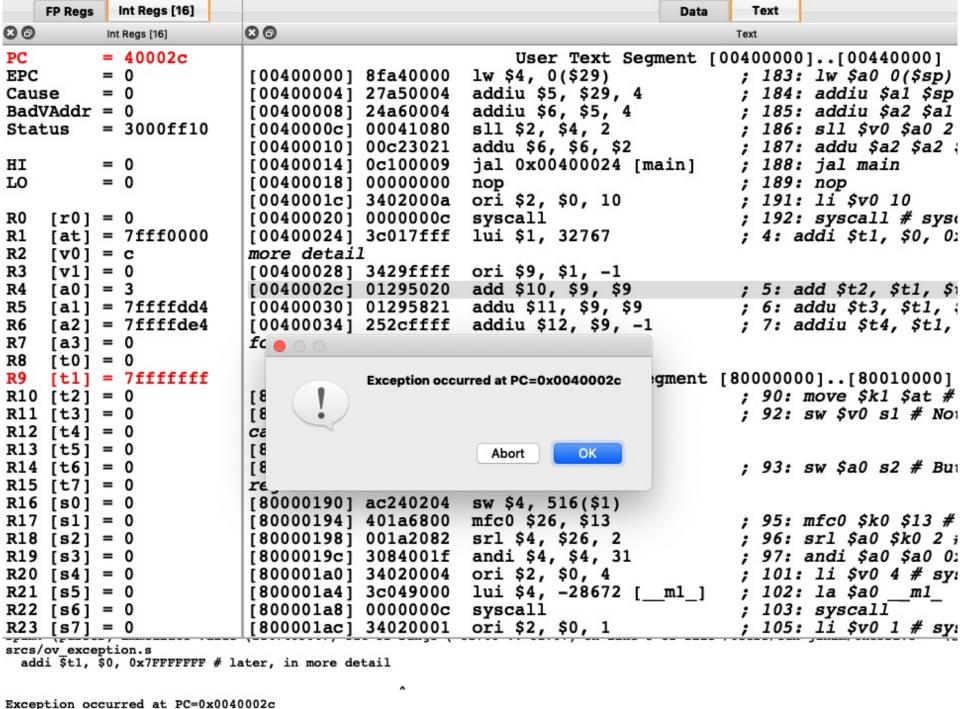
MIPS source file

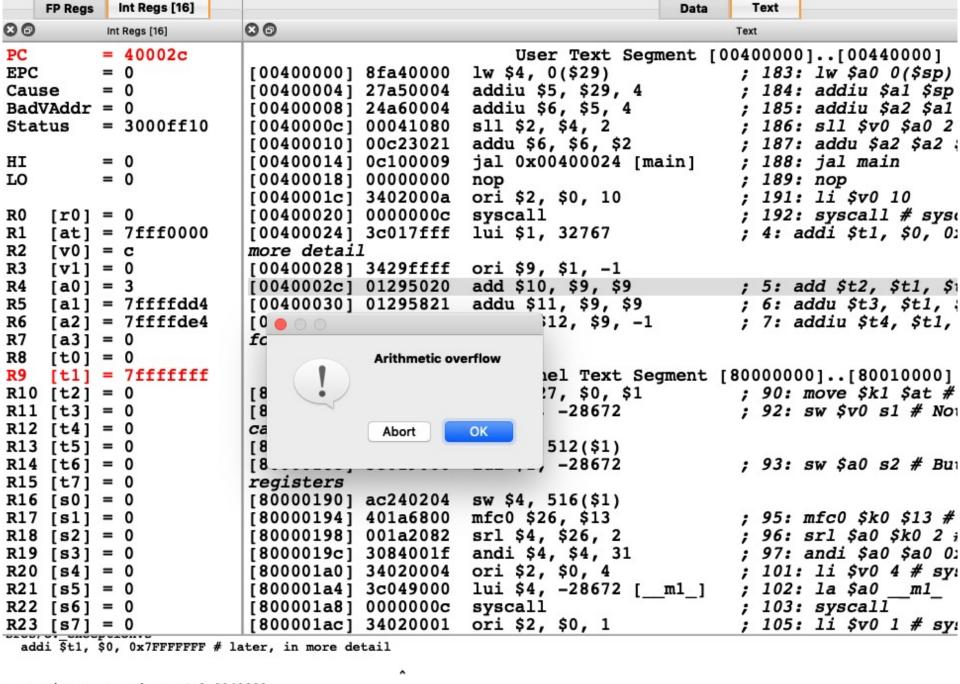
```
.text
.globl main
main:
    addi $t1, $0, 0x7FFFFFFF # later, in more detail
    add $t2, $t1, $t1 # overflow
    addu $t3, $t1, $t1 # no exception
    addiu $t4, $t1, -1 # negative constant for addiu
 0111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ = 2147483647
+0111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111\ 1111
  1111 1111 1111 1111 1111 1111 1111 1110
```



	FP Regs	Int Regs [2]					Data	Text	1		
⊗ ⊕		Int Regs [2]		8 6				Text			
PC		= 0				User Text Segn	ment [0	040000	0][0	00440	000]
EPC		= 0		[00400000]	8fa40000	lw \$4, 0(\$29)		; 183	: lw .	\$a0 0	(\$sp)
Caus	se	= 0		[00400004]	27a50004	addiu \$5, \$29, 4		; 184	: add.	iu \$a	1 \$sp
Bady	VAddr	= 0		[00400008]	24a60004	addiu \$6, \$5, 4		; 185	: add.	iu \$a	2 \$a1
Stat	tus	= 11000000	00	[0040000c]	00041080	sll \$2, \$4, 2		; 186	: sll	\$v0	\$a0 2
0000	011111	1110001000	0	[00400010]	00c23021	addu \$6, \$6, \$2		; 187	: add	u \$a2	\$a2 :
				[00400014]	0c100009	jal 0x00400024 [ma	ain]	; 188	: jal	main	!
ΗI		= 0		[00400018]	00000000	nop	_	; 189	: nop		
LO		= 0		[0040001c]	3402000a	ori \$2, \$0, 10		; 191	: li .	\$v0 1	0
				[00400020]	000000c	syscall		; 192	: sys	call	# sysc
R0	[r0]	= 0		[00400024]	3c017fff	lui \$1, 32767					\$0, O:
R1	[at]	= 0		more detai.	1	4					
R2	[v0]	= 0		[00400028]	3429ffff	ori \$9, \$1, -1					
R3	[v1]	= 0		[0040002c]	01295020	add \$10, \$9, \$9		; 5:	add \$	t2, \$	t1, \$1
R4	[a0]	= 0		[00400030]	01295821	addu \$11, \$9, \$9					\$t1,
R5	[a1]	= 0		[00400034]		addiu \$12, \$9, -1					\$t1,
R6	[a2]	= 11111111	11	for addiu	L			•			
1111	111111	1110001111	00								
R7	[a3]	= 0				Kernel Text Seg	gment [800000	00]	[8001	[0000]
R8	[t0]	= 0		[80000180]	0001d821	addu \$27, \$0, \$1					
	[t1]			[80000184]		lui \$1, -28672		; 92:	sw \$	v0 s1	# No
	[t2]			can't trus		•		•			
	[t3]			[80000188]		sw \$2, 512(\$1)					
	[t4]			[8000018c]		lui \$1, -28672		; 93:	sw \$	a0 s2	# Bu
	[t5]			registers				•	-		
	[t6]			[80000190]	ac240204	sw \$4, 516(\$1)					
	[t7]			[80000194]	401a6800	mfc0 \$26, \$13		; 95:	mfc0	\$k0	\$13 #
	[80]			[80000198]	001a2082	srl \$4, \$26, 2					k0 2 i
	[s1]			[8000019c]		andi \$4, \$4, 31					\$a0 0:
	[s2]			[800001a0]	34020004	ori \$2, \$0, 4		; 101	: li :	\$v0 4	# sy:
R19	[83]	= 0		[800001a41	3c049000	lui \$4, -28672 [m1]	; 102	: la :	\$a0	m1
	[s4]			[800001a8]	000000c	syscall			: sys		
	[85]					ori \$2, \$0, 1					# sy:
			alue			32768 32767) on line 5	of file /				
srcs/	ov excep	otion.s				January em 12ma s	/				
add	li \$t1, \$	0, 0x7FFFFFF	# la	ter, in more de	tail						
					^						

	FP Regs	Int Regs [16]		Data	Text
⊗ ⊕		Int Regs [16]	⊗ @	1	Text
PC		= 40002c		User Text Segment [00	400000][00440000]
EPC	:	= 0	[00400000] 8fa40000 lw		; 183: lw \$a0 0(\$sp)
Cau	ıse	= 0			; 184: addiu \$a1 \$sp
Bad	lVAddr	= 0	[00400008] 24a60004 ad	ldiu \$6, \$5, 4 1 \$2, \$4, 2	; 185: addiu \$a2 \$a1
Sta	tus	= 3000ff10	[0040000c] 00041080 sl	.1 \$2, \$4, 2	; 186: sll \$v0 \$a0 2
				ldu \$6, \$6, \$2	; 187: addu \$a2 \$a2 :
ΗI		= 0			; 188: jal main
LO		= 0	[00400018] 00000000 no	-	; 189: nop
			[0040001c] 3402000a or	:i \$2, \$0, 10	; 191: li \$v0 10
R0	[r0]	= 0		rscall	; 192: syscall # sysc
R1		= 7fff0000		ıi \$1, 32767	; 4: addi \$t1, \$0, 0:
R2	[v0]		more detail		
R3	[v1]	= 0	[00400028] 3429ffff or	:i \$9, \$1, -1	
R4	[a0]	= 3	[0040002c] 01295020 ad	ld \$10, \$9, \$9	; 5: add \$t2, \$t1, \$t
R5	[a1]	<pre>= 7ffffdd4</pre>	[00400030] 01295821 ad	ldu \$11, \$9, \$9	; 6: addu \$t3, \$t1,
R6	[a2]	= 7ffffde4	[00400034] 252cffff ad	ldiu \$12, \$9, -1	; 7: addiu \$t4, \$t1,
R7	[a3]	= 0	for addiu		
R8	[t0]				
R9	[t1]	= 7fffffff		Kernel Text Segment [8	0000000][80010000]
R10	[t2]	= 0	[80000180] 0001d821 ad	ldu \$27, \$0, \$1	; 90: move \$k1 \$at #
R11	[t3]	= 0	[80000184] 3c019000 lu	ıi \$1, – 28672	
	[t4]		can't trust \$sp		
	[t5]		[80000188] ac220200 sw	, \$2, 512(\$1)	
R14	[t6]	= 0	[8000018c] 3c019000 lu	ıi \$1, -28672	; 93: sw \$a0 s2 # Bu
R15	[t7]	= 0	registers		
R16	[s0]	= 0	[80000190] ac240204 sw	7 \$4, 516(\$1)	
	[s1]		[80000194] 401a6800 mf	co \$26, \$13	; 95: mfc0 \$k0 \$13 #
	[s2]			:1 \$4, \$26, 2	; 96: srl \$a0 \$k0 2 ;
	[s3]		[8000019c] 3084001f an	ndi \$4, \$4, 31	; 97: andi \$a0 \$a0 0:
R20	[s4]	= 0	[800001a0] 34020004 or	:i \$2, \$0, 4	; 101: li \$v0 4 # sy:
	[s5]		[800001a4] 3c049000 lu	i \$4, -28672 [m1_]	; 102: la \$a0m1_
	[s6]		[800001a8] 0000000c sy	scall	; 103: syscall
R23	[s7]	= 0	[800001ac] 34020001 or	i \$2, \$0, 1	; 105: li \$v0 1 # sy:
spim	: (parse	r) immediate value	(2147483647) out of range (-3276	8 32767) on line 5 of file /U	sers/eun-jinim/OneDrive - 국민
srcs	/ov_exce	ption.s		,	-
ad	dı Ştl,	\$0, 0x7FFFFFFF # 1a	ter, in more detail		
I			^		





Exception Handler 수행

• source code 의 6번 행으로 돌아올 때까지 계속 single step

	FP Regs	Int Regs [16]		Data Text
8 a		Int Regs [16]	⊗ ⊚	Text
PC		= 80000180		User Text Segment [00400000][00440000]
EPC	:	= 40002c	[00400000] 8fa40000	lw \$4, 0(\$29) ; 183: lw \$a0 0(\$sp)
Cau	ıse	= 30	[00400004] 27a50004	addiu \$5, \$29, 4 ; 184: addiu \$a1 \$sp
Bad	lVAddr	= 0	[00400008] 24a60004	addiu \$6, \$5, 4 ; 185: addiu \$a2 \$a1
Sta	itus	= 3000ff12	[0040000c] 00041080	sll \$2, \$4, 2 ; 186: sll \$v0 \$a0 2
			[00400010] 00c23021	addu \$6, \$6, \$2 ; 187: addu \$a2 \$a2
ΗI		= 0	[00400014] 0c100009	jal 0x00400024 [main] ; 188: jal main
LO		= 0	[00400018] 00000000	nop ; 189: nop
			[0040001c] 3402000a	ori \$2, \$0, 10 ; 191: 1i \$v0 10
R0	[r0]	= 0	[00400020] 0000000c	syscall ; 192: syscall # sysc
R1	[at]	<pre>= 7fff0000</pre>	[00400024] 3c017fff	lui \$1, 32767 ; 4: addi \$t1, \$0, 0
R2	[v0]	= c	more detail	
R3	[v1]	= 0	[00400028] 3429ffff	ori \$9, \$1, -1
R4	[a0]	= 3	[0040002c] 01295020	add \$10, \$9, \$9 ; 5: add \$t2, \$t1, \$
R5	[a1]	<pre>= 7ffffdd4</pre>	[00400030] 01295821	addu \$11, \$9, \$9 ; 6: addu \$t3, \$t1,
R6	[a2]	= 7ffffde4	[00400034] 252cffff	addiu \$12, \$9, -1 ; 7: addiu \$t4, \$t1,
R7	[a3]	= 0	for addiu	
R8	[to]			
R9		<pre>= 7fffffff</pre>		<pre>Kernel Text Segment [80000000][80010000]</pre>
R10	[t2]	= 0	[80000180] 0001d821	addu \$27, \$0, \$1 ; 90: move \$k1 \$at #
R11	[t3]	= 0	[80000184] 3c019000	lui \$1, -28672 ; 92: sw \$v0 s1 # No
	[t4]		can't trust \$sp	
R13	[t5]	= 0	[80000188] ac220200	sw \$2, 512(\$1)
R14	[t6]	= 0	[8000018c] 3c019000	lui \$1, -28672 ; 93: sw \$a0 s2 # But
R15	[t7]	= 0	registers	
	[s0]		[80000190] ac240204	sw \$4, 516(\$1)
R17	[s1]	= 0	[80000194] 401a6800	mfc0 \$26, \$13 ; 95: mfc0 \$k0 \$13 #
R18	[s2]	= 0	[80000198] 001a2082	srl \$4, \$26, 2 ; 96: srl \$a0 \$k0 2 ;
	[s3]		[8000019c] 3084001f	andi \$4, \$4, 31 ; 97: andi \$a0 \$a0 0
	[s4]		[800001a0] 34020004	ori \$2, \$0, 4 ; 101: li \$v0 4 # sys
	[s5j		[800001a4] 3c049000	lui \$4, -28672 [ml] ; 102: la \$a0 m1
	[s6]		[800001a8] 0000000c	syscall ; 103: syscall
	[s7]		[800001ac] 34020001	ori \$2, \$0, 1 ; 105: li \$v0 1 # sys
ad	di \$t1,	\$0, 0x7FFFFFFF # 1a	ater, in more detail	

Exception 12 [Arithmetic overflow] occurred and ignored

Unsigned Add/Sub

Category	Instruction	E	xample	Meaning	Comments
	add	add	\$s1,\$s2,\$s3	\$s1 = \$s2 + \$s3	Three operands; overflow detected
	subtract	sub	\$s1,\$s2,\$s3	\$s1 = \$s2 - \$s3	Three operands; overflow detected
	add immediate	addi	\$s1.\$s2.100	\$s1 = \$s2 + 100	+ constant; overflow detected
	add unsigned	addu	\$s1,\$s2,\$s3	\$s1 = \$s2 + \$s3	Three operands; overflow undetected
	subtract unsigned	subu	\$s1,\$s2,\$s3	\$s1 = \$s2 - \$s3	Three operands; overflow undetected
	add immediate unsigned	addiu	\$s1,\$s2,100	\$s1 = \$s2 + 100	+ constant; overflow undetected
	move from coprocessor register	mfc0	\$s1,\$epc	\$s1 = \$epc	Copy Exception PC + special regs
Arithmetic	multiply	mult	\$s2,\$s3	$Hi, Lo = $s2 \times $s3$	64-bit signed product in Hi, Lo
	multiply unsigned	multu	\$s2,\$s3	Hi, Lo = $$s2 \times $s3$	64-bit unsigned product in Hi, Lo
	divide	div	\$s2,\$s3	Lo = \$s2 / \$s3, Hi = \$s2 mod \$s3	Lo = quotient, Hi = remainder
	divide unsigned	divu	\$s2,\$s3	Lo = \$s2 / \$s3, Hi = \$s2 mod \$s3	Unsigned quotient and remainder
	move from Hi	mfhi	\$s1	\$s1 = Hi	Used to get copy of Hi
	move from Lo	mflo	\$s1	\$s1 = Lo	Used to get copy of Lo

addu/addiu/subu instructions

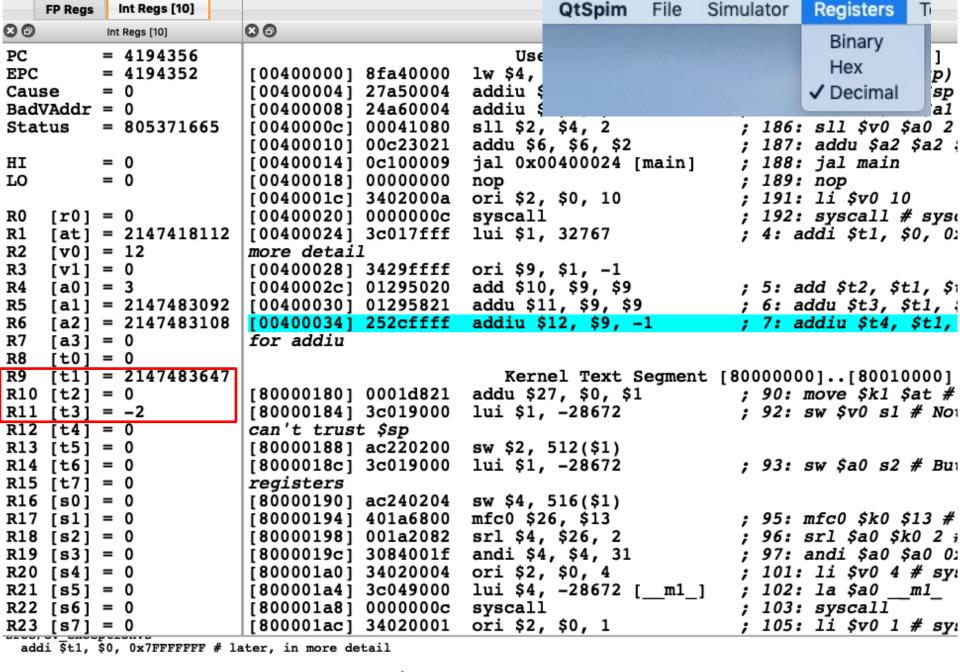
- MIPS unsigned 명령어의 의미 : 연산의 결과 overflow 가 되어도 exception 이 발생하지 않는다.
- add/addi/sub 명령어는 overflow 가 되면 exception
 을 발생한다.
- exception (예외) : 프로그램의 정상적인 수행을 방해하는 계획되지 않은 사건

MIPS source file

```
.text
.globl main
main:
   addi $t1, $0, 0x7FFFFFFF # later, in more detail
   add $t2, $t1, $t1 # overflow
   addu $t3, $t1, $t1 # no exception
   addiu $t4, $t1, -1 # negative constant for addiu
```

	FP Regs	Int Regs [16]		Data Text
⊗ ⊕		Int Regs [16]	⊗ ⊚	Text
PC		= 400030		User Text Segment [00400000][00440000]
EPC		= 400030	[00400000] 8fa40000	lw \$4, 0(\$29) ; 183: lw \$a0 0(\$sp)
Caus	se	= 0	[00400004] 27a50004	addiu \$5, \$29, 4 ; 184: addiu \$a1 \$sp
Bady	VAddr	= 0	[00400008] 24a60004	addiu \$6, \$5, 4 ; 185: addiu \$a2 \$a1
Stat	tus	= 3000ff11	[0040000c] 00041080	sll \$2, \$4, 2 ; 186: sll \$v0 \$a0 2
			[00400010] 00c23021	addu \$6, \$6, \$2 ; 187: addu \$a2 \$a2 ;
HI		= 0	[00400014] 0c100009	jal 0x00400024 [main] ; 188: jal main
LO		= 0	[00400018] 00000000	nop ; 189: nop
			[0040001c] 3402000a	ori \$2, \$0, 10 ; 191: li \$v0 10
R0	[r0]	= 0	[00400020] 0000000c	syscall ; 192: syscall # sysc
R1		= 7fff0000	[00400024] 3c017fff	lui \$1, 32767 ; 4: addi \$t1, \$0, 0:
R2		= c	more detail	,
R3		= 0	[00400028] 3429ffff	ori \$9, \$1, -1
R4	[a0]		[0040002c] 01295020	add \$10, \$9, \$9 ; 5: add \$t2, \$t1, \$1
R5		= 7ffffdd4	[00400030] 01295821	addu \$11, \$9, \$9 ; 6: addu \$t3, \$t1,
R6		= 7ffffde4	[00400034] 252cffff	addiu \$12, \$9, -1 ; 7: addiu \$t4, \$t1,
R7		= 0	for addiu	, , , , , , , , , , , , , , , , , , , ,
R8		= 0		
R9		= 7fffffff		Kernel Text Segment [80000000][80010000]
		= 0	[80000180] 0001d821	addu \$27, \$0, \$1 ; 90: move \$k1 \$at #
			[80000184] 3c019000	lui \$1, -28672 ; 92: sw \$v0 s1 # No:
	[t4]		can't trust \$sp	, , , , , , , , , , , , , , , , , , , ,
	[t5]		[80000188] ac220200	sw \$2, 512(\$1)
	[t6]		[8000018c] 3c019000	lui \$1, -28672 ; 93: sw \$a0 s2 # But
			registers	, sor su que se u su
			[80000190] ac240204	sw \$4, 516(\$1)
		= 0	[80000194] 401a6800	mfc0 \$26, \$13 ; 95: mfc0 \$k0 \$13 #
		= 0	[80000198] 001a2082	srl \$4, \$26, 2 ; 96: srl \$a0 \$k0 2;
		= 0	[8000019c] 3084001f	andi \$4, \$4, 31 ; 97: andi \$a0 \$a0 0:
		= 0	[800001a0] 34020004	ori \$2, \$0, 4 ; 101: li \$v0 4 # sy:
		= 0	[800001a4] 3c049000	lui \$4, -28672 [ml] ; 102: la \$a0 m1
		= 0	[800001a8] 0000000c	syscall ; 103: syscall
			[800001ac] 34020001	ori \$2, \$0, 1 ; 105: li \$v0 1 # sy:
DEUDI	or onde	POTOTIO		
add	ıı ştı,	ου, UX/FFFFFF # .	later, in more detail	

[FP Regs		Int Regs [16]				Data	Text	
⊗ ⊕		Int	Regs [16]		⊗ ⊚			Text	
PC		=	400034			User Text Segm	ent [0	040000	01[00440000]
EPC		=	400030		[00400000] 8fa40000	lw \$4, 0(\$29)	•		: lw \$a0 0(\$sp)
Cau	se	=	0		[00400004] 27a50004	addiu \$5, \$29, 4			: addiu \$al \$sp
Bad	VAddr	=	0		[00400008] 24a60004	addiu \$6, \$5, 4		; 185	: addiu \$a2 \$a1
Sta	tus	=	3000ff11		[0040000c] 00041080	sll \$2, \$4, 2		; 186	: sll \$v0 \$a0 2
					[00400010] 00c23021	addu \$6, \$6, \$2		; 187	: addu \$a2 \$a2
ΗI		=	-		[00400014] 0c100009	jal 0x00400024 [ma	in]		: jal main
LO		=	0		[00400018] 00000000	nop		; 189	
					[0040001c] 3402000a	ori \$2, \$0, 10			: li \$v0 10
R0	[r0]				[00400020] 0000000c	syscall			: syscall # sys
R1			7fff0000		[00400024] 3c017fff	lui \$1, 32767		; 4: 8	addi \$t1, \$0, 0
R2	[v0]				more detail				
R3	[v1]				[00400028] 3429ffff	ori \$9, \$1, -1			
R4	[a0]				[0040002c] 01295020	add \$10, \$9, \$9			add \$t2, \$t1, \$
R5			7ffffdd4		[00400030] 01295821	addu \$11, \$9, \$9			addu \$t3, \$t1,
R6			7ffffde4		[00400034] 252cffff	addiu \$12, \$9, -1		; 7: 8	addiu \$t4, \$t1,
R7	[a3]				for addiu				
R8	[t0]			_	_+42억 or -2?	.			
R9			7fffffff			Kernel Text Seg			
	[t2]			X	[80000180] 0001d821	addu \$27, \$0, \$1		; 90:	move \$k1 \$at #
			fffffffe		[80000184] 3c019000	lui \$1, -28672		; 92:	sw \$v0 s1 # No
	[t4]				can't trust \$sp	40 540/441			
	[t5]				[80000188] ac220200				400"-
	[t6]				[8000018c] 3c019000	lui \$1, -28672		; 93:	sw \$a0 s2 # Bu
	[t7]				registers	44 - 516(41)			
	[s0]				[80000190] ac240204	sw \$4, 516(\$1)			
	[s1]				[80000194] 401a6800	mfc0 \$26, \$13			mfc0 \$k0 \$13 #
	[s2]				[80000198] 001a2082	srl \$4, \$26, 2			srl \$a0 \$k0 2
	[s3]				[8000019c] 3084001f	andi \$4, \$4, 31			andi \$a0 \$a0 0
	[s4]				[800001a0] 34020004	ori \$2, \$0, 4	1 .		: li \$v0 4 # sy
	[s5]				[800001a4] 3c049000	lui \$4, -28672 [:	mT_]		: la \$a0m1_
	[s6]				[800001a8] 0000000c	syscall			: syscall
DEVDI		P			[800001ac] 34020001	ori \$2, \$0, 1		; 105	: li \$v0 1 # sy
ado	di \$t1,	\$0,	0x7FFFFFFF	# la	ater, in more detail				
					^				

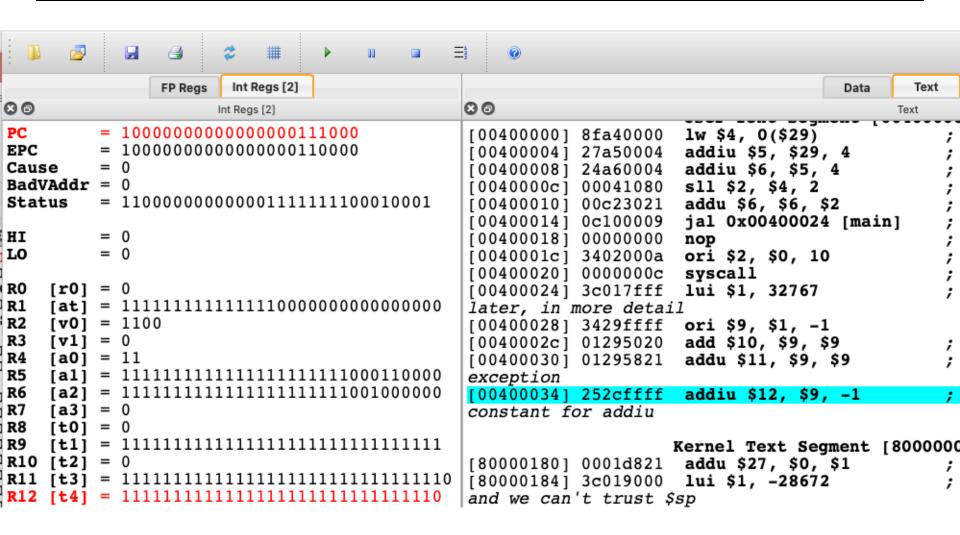


addiu instruction

• addiu 명령의 상수 operand 는 음수일 수 있다.

```
addiu $t4, $t1, -1 # negative constant for addiu
 1 \ 0111 \ 1111 \ 1111 \ 1111 \ 1111 \ 1111 \ 1111 \ 1110 \ = 2147483646
```

	FP Regs		Int Regs [10]		Data		Text				
8		Int	t Regs [10]	80		Text	t				
PC		=	4194360		User Text Segment	[0040	0000	0]	0044	1000	0]
EPC		=	4194352	[00400000] 8fa40000	lw \$4, 0(\$29)	-		: lw	-		_
Cau	se	=	0	[00400004] 27a50004	addiu \$5, \$29, 4			: add	-	• •	
Bad	VAddr	=	0	[00400008] 24a60004	addiu \$6, \$5, 4			: add			
Sta	tus	=	805371665	[0040000c] 00041080	sll \$2, \$4, 2			: sl			
				[00400010] 00c23021	addu \$6, \$6, \$2			: add			
ΗI		=	0	[00400014] 0c100009	jal 0x00400024 [main]			: ja:	-	-	i i
LO		=	0	[00400018] 00000000	nop			: nop			
				[0040001c] 3402000a	ori \$2, \$0, 10			: li		10	
R0	[r0]	=	0	[00400020] 0000000c	syscall			: sys			sys
R1			2147418112	[00400024] 3c017fff	lui \$1, 32767			addi			
R2	[0v]	=	12	more detail		•					
R3	[v1]		0	[00400028] 3429ffff	ori \$9, \$1, -1						
R4	[a0]			[0040002c] 01295020	add \$10, \$9, \$9	;	5:	add \$	3t2,	\$t1	, \$1
R5			2147483092	, ,	addu \$11, \$9, \$9			addu			
R6	[a2]		2147483108	[00400034] 252cffff	addiu \$12, \$9, -1			addiı			
R7			0	for addiu							
R8		=	0								
R9		=	2147483647		Kernel Text Segment	[800	0000	00].	. [800)100	00]
R10	[t2]	=	0	[80000180] 0001d821	addu \$27, \$0, \$1	· ;	90:	move	∍ \$k:	l \$ai	t#
R11	[t3]	=	-2	[80000184] 3c019000	lui \$1, -28672	;	92:	sw s	\$v0 :	s1 #	No:
R12	[t4]	=	2147483646	can't trust \$sp							
R13	[t5]	=	0	[80000188] ac220200	sw \$2, 512(\$1)						
R14	[t6]	=	0	[8000018c] 3c019000	lui \$1, -28672	;	93:	sw \$	3a0 :	s2 #	Bu
R15	[t7]	=	0	registers							
R16	[s0]	=	0	[80000190] ac240204	sw \$4, 516(\$1)						
R17	[s1]	=	0	[80000194] 401a6800	mfc0 \$26, \$13	;	95:	mfc() \$k() \$1.	3#
R18	[s2]	=	0	[80000198] 001a2082	srl \$4, \$26, 2	;	96:	srl	\$a0	\$k0	2 i
R19	[s3]	=	0	[8000019c] 3084001f	andi \$4, \$4, 31	;	97:	and:	i \$a() \$a(0 0:
	[s4]	=	0	[800001a0] 34020004	ori \$2, \$0, 4			: li			
R21	[s5]	=	0	[800001a4] 3c049000	lui \$4, -28672 [m1_]			: la			1_
R22	[s6]	=	0	[800001a8] 0000000c	syscall		103	: sys	scal:		_
R23	[s7]	=	0	[800001ac] 34020001	ori \$2, \$0, 1	;	105	: lī	\$v0	1 #	sy
ade	di \$t1,	\$0,	0x7FFFFFFF # 1	ater, in more detail							



HW2: due 9/14 (목) 자정

- subu instruction 을 사용하여 0번 레지스터(\$0)에서 \$t1을 뺄셈을 했을 때 오버플로우가 발생하도록 \$t1 의 값을 초기화한 다음 뺄셈을 하는 MIPS assembly source code 를 작성한 다음, SPIM 에서 수행하여 작성한 코드가 load 된 text segment 부분과 연산 결과 레지스터 내용이 16진수로 함께 보이는 화면을 캡춰하여 ecampus 에 제출 (단 명령어실행 시 overflow exception 이 발생하지 않도록 한다.)
- source register로는 \$0 와 \$t1 를 사용하고 destination register로 \$t2를 사용한다.

Operation	Operand A	Operand B	Result indicating overflow
A + B	≥0	≥ 0	< 0
A + B	< 0	< 0	≥ 0
A – B	≥ 0	< 0	< 0
A - B	< 0	≥ 0	≥ 0



숙제 제출 시 주의할 점

마지막 화면 1개만 캡춰하여 제출할 것.

여러 개 화일을 압축하여 제출하지 마세요.

HW2 는 화일 첨부가 아니라 직접 작성으로 제출 방식을 바꾸었습니다.

그림 화일의 크기는 1M 미만으로.

HW2 제출 방법

hw2

- subu instruction 을 사용하여 0번 레지스터(\$0)에서 \$t1을 뺄셈을 했을 때 오버플로우가 발생하도록 \$t1 의 값을 초기화한 다음 뺄셈을 하는 MIPS assembly source code 를 작성한 다음, SPIM 에서 수행하여 작성한 코드가 load 된 text segment 부분과 연산 결과 레지스터 내용이 16진수로 함께 보이는 화면을 캡춰하여 ecampus 에 제출 (단 명령어 실행 시 overflow exception 이 발생하지 않도록 한다.)
- source register로는 \$0 와 \$t1 를 사용하고 destination register로 \$t2를 사용한다.

제출 상황

제출 여부	제출 안 함
채점 상황	채점되지 않음
종료 일시	2020-09-18 11:00
마감까지 남은 기한	8 일 11 시간
최종 수정 일시	-
제출물 설명	▶ 댓글 (0)

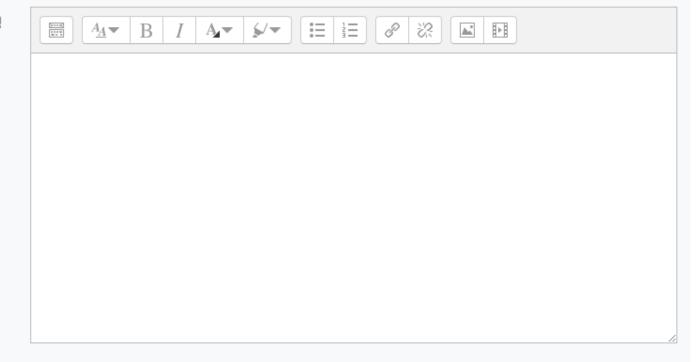
과제 제출하기

캡춰한 그림 화일을 drag-and-drop

hw2

- subu instruction 을 사용하여 0번 레지스터(\$0)에서 \$t1을 뺄셈을 했을 때 오버플로우가 발생하도록 \$t1 의 값을 초기화한 다음 뺄셈을 하는 MIPS assembly source code 를 작성한 다음, SPIM 에서 수행하여 작성한 코드가 load 된 text segment 부분과 연산 결과 레지스터 내용이 16진수로 함께 보이는 화면을 캡춰하여 ecampus 에 제출 (단 명령어 실행 시 overflow exception 이 발생하지 않도록 한다.)
- source register로는 \$0 와 \$t1 를 사용하고 destination register로 \$t2를 사용한다.

직접 작성



저장 취소

[저장] 버튼을 클릭 시 최종 과제 제출일이 업데이트 되오니 주의하세요.