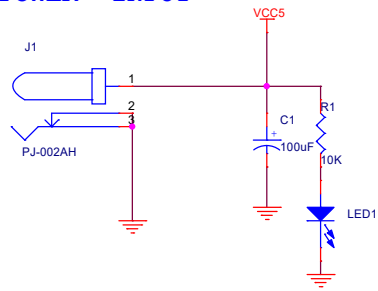
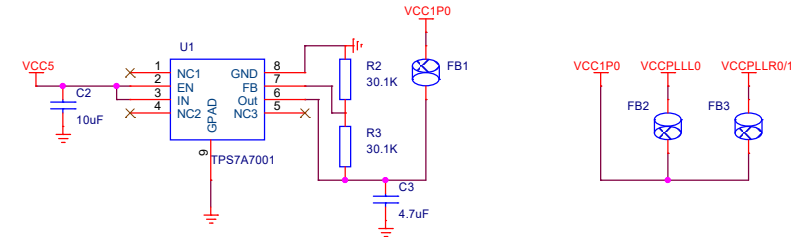


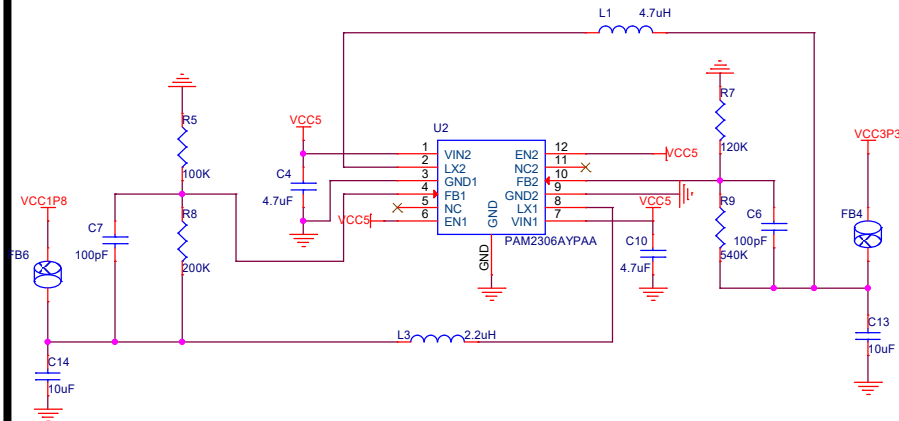
SCHEMATIC	CONTENT	PAGE
PAGE1	COVER PAGE	1
PAGE2-POWER	DC5V to 3.3V,2.5V,1.8V,1.2V,1.0V	2
PAGE3-FPGA	GW1NSR-4C-MG64P	3
PAGE4-FPGA	GW2AR-18K-QN88P	4
PAGE5-GPIO	GPIO	5
PAGE6-USB1	USB3250	6
PAGE7-USB2	USBRC	7
PAGE8-USB3	USB-FPGA	8
PAGE9-JTAG	JTAG	9



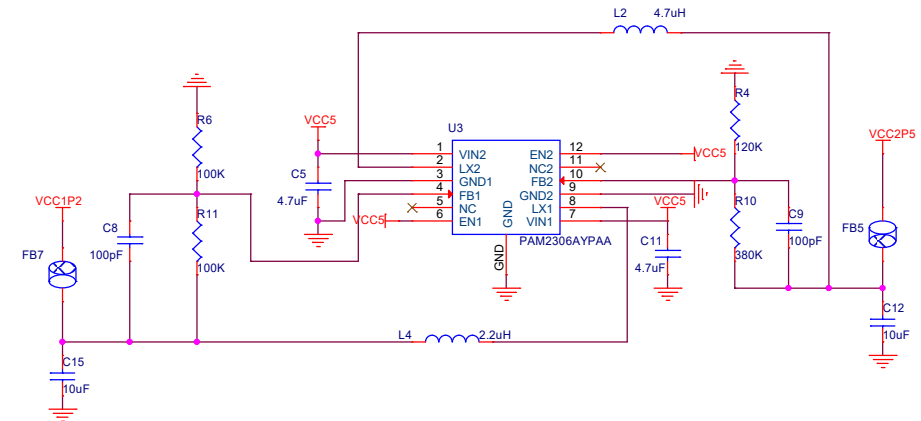
VCC5V 转 VCC 1V



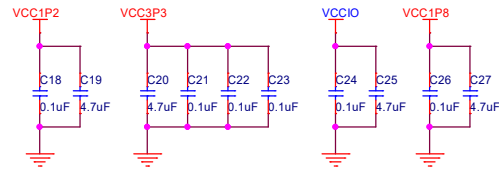
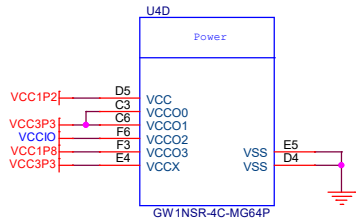
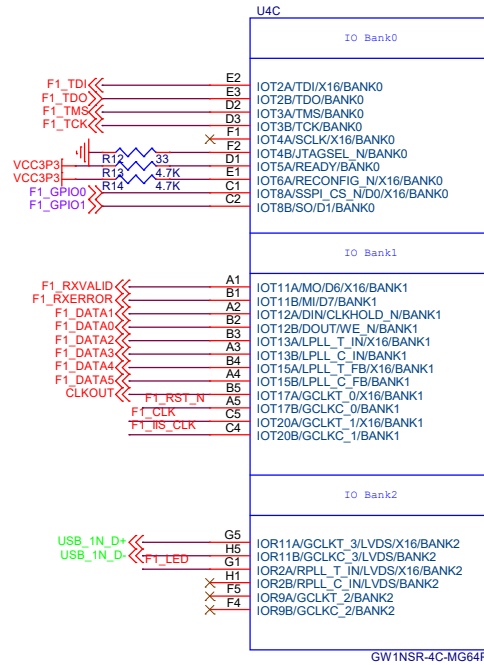
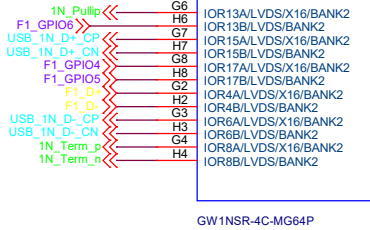
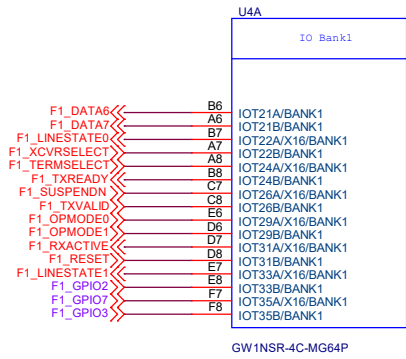
VCC5V 转 VCC3.3V 、 VCC1.8V



VCC5V 转 VCC2.5V 、 VCC1.2V



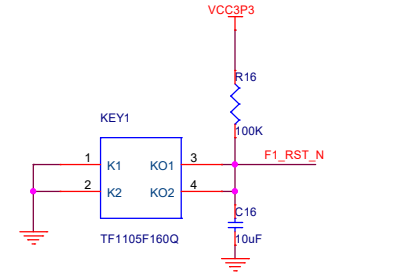
GW1NSR-4C-MG64P



LED

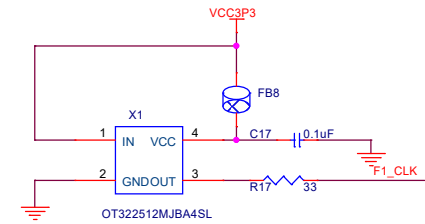


按键

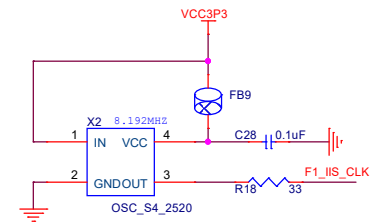


时钟

12MHZ 1.8V-3.3V 4Pin



IIS-CLK 8.192MHz

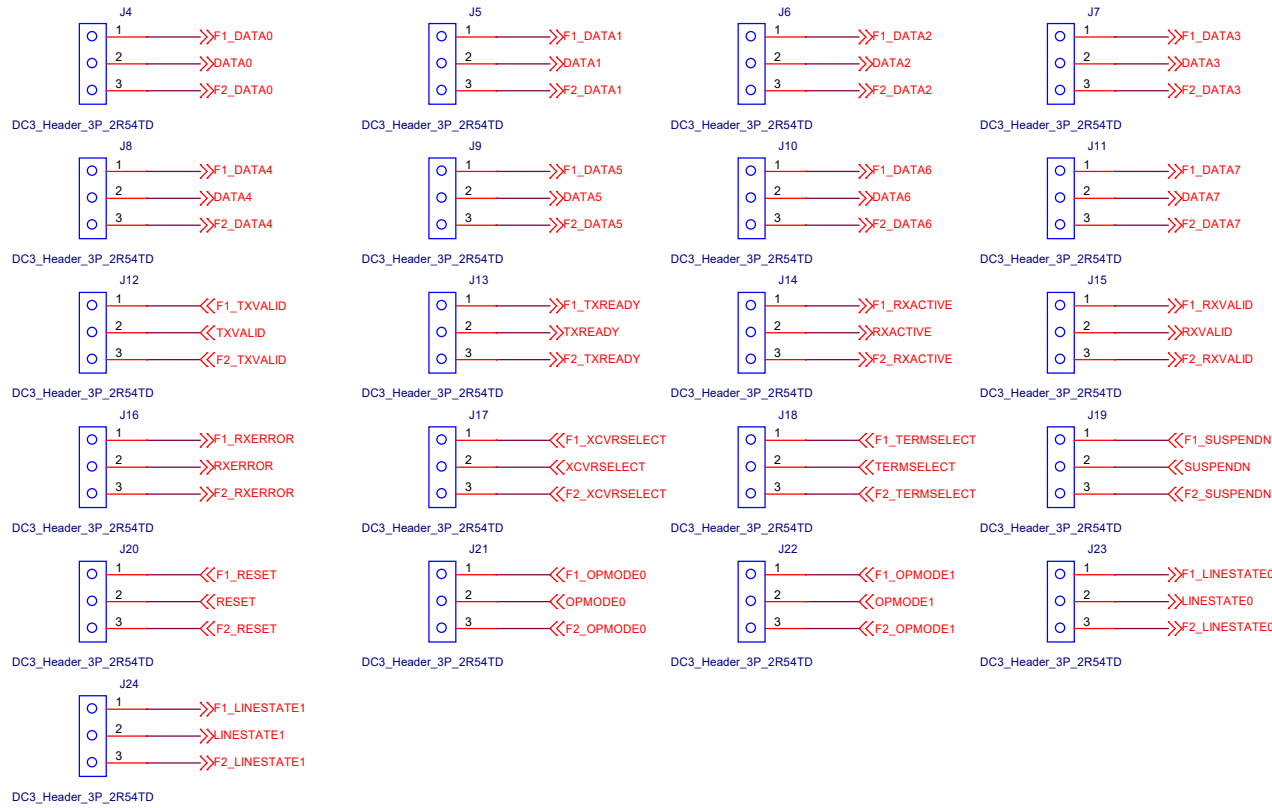


1

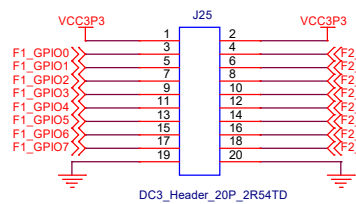


Rev	<RevCod
-----	---------

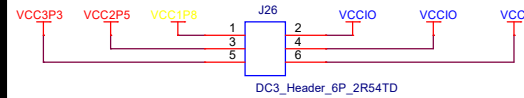
USB3280 Signal Line



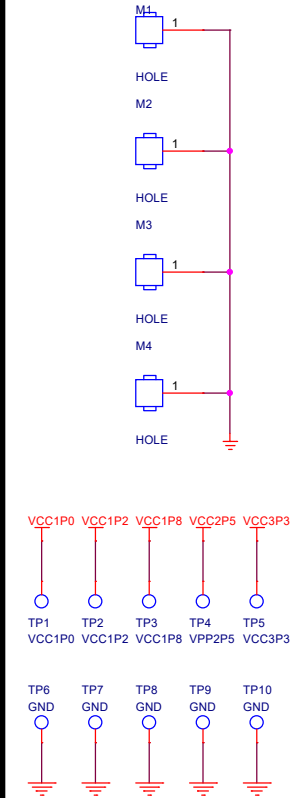
GPIO

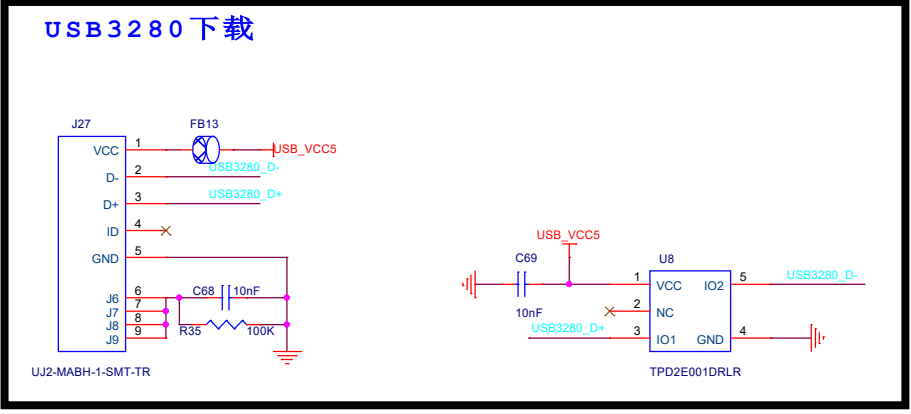
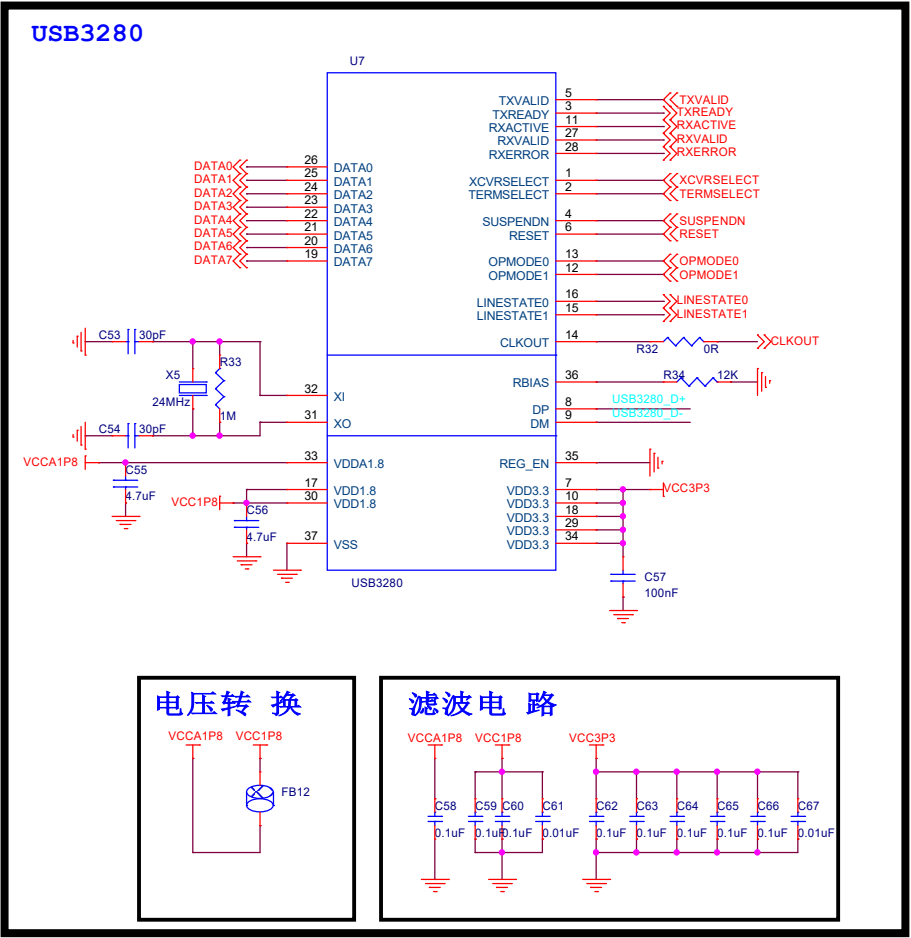


电压可 调



测试孔



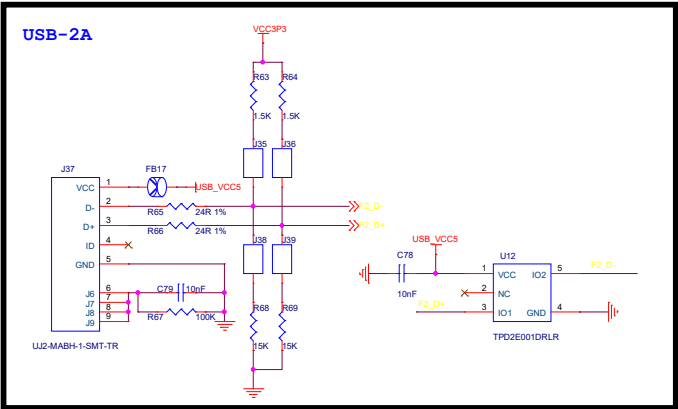
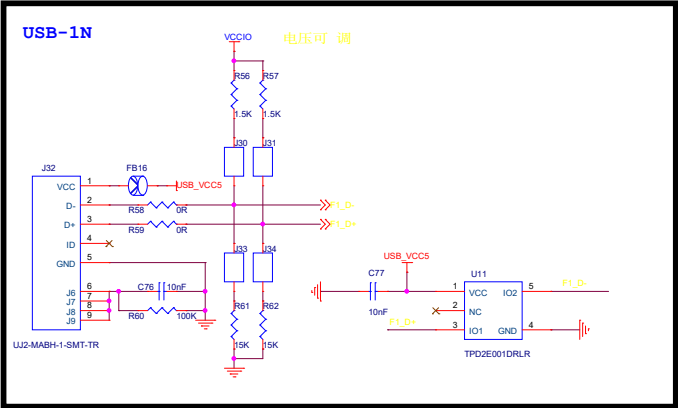


D

C

B

A



D

C

B

A

