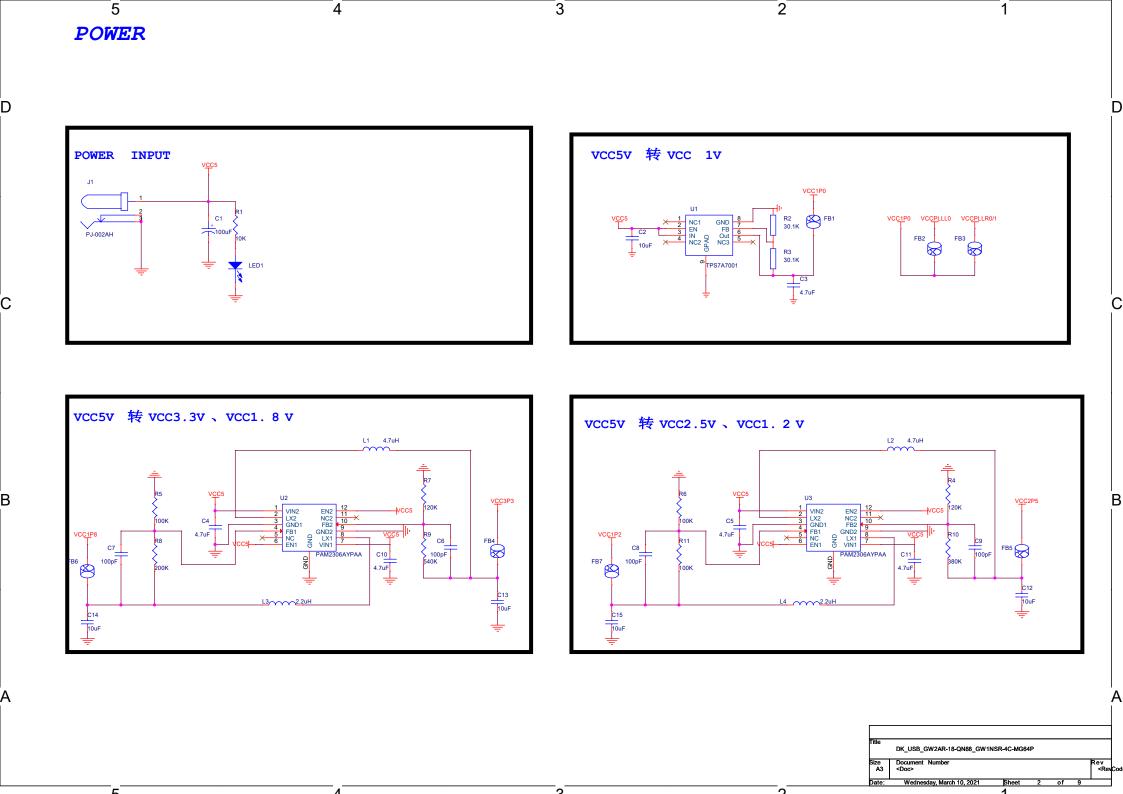
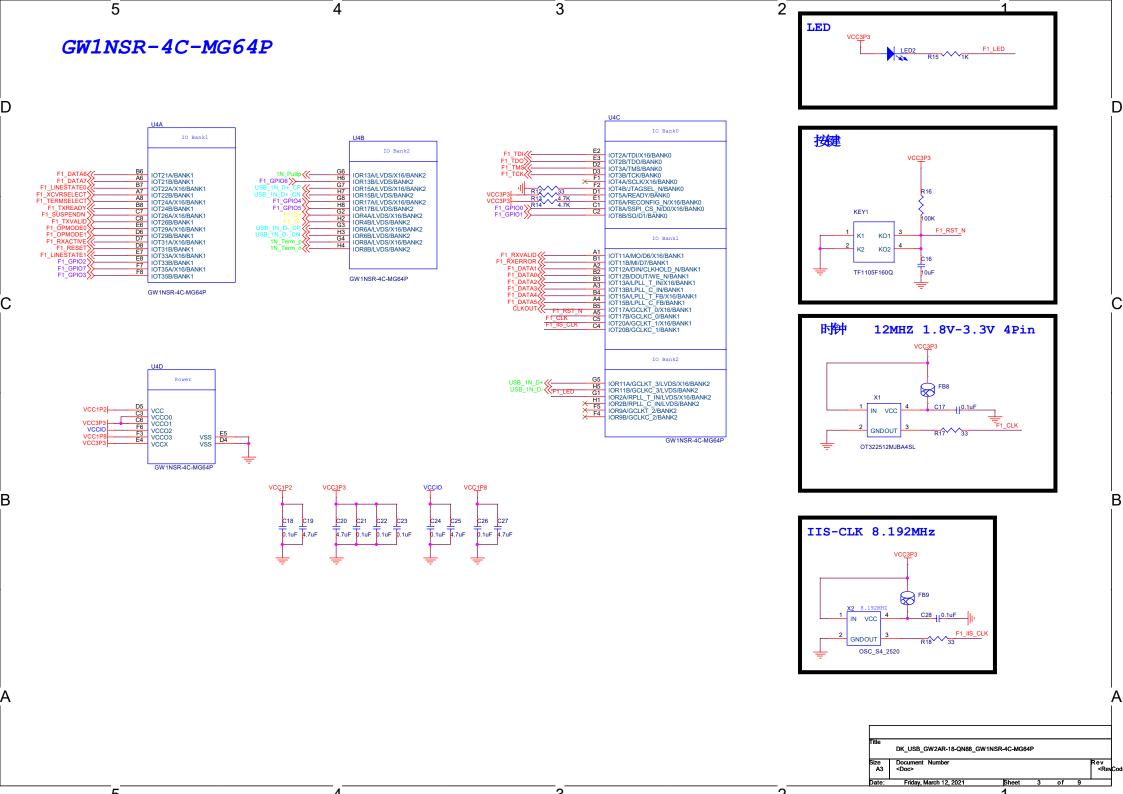
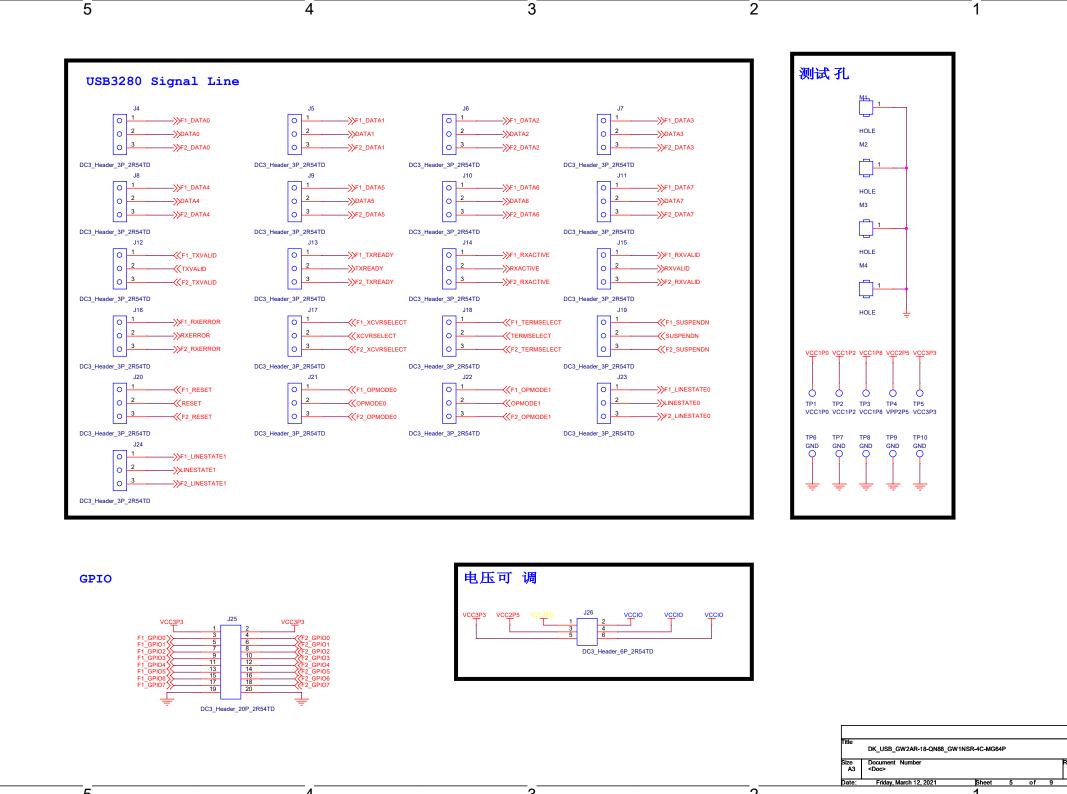
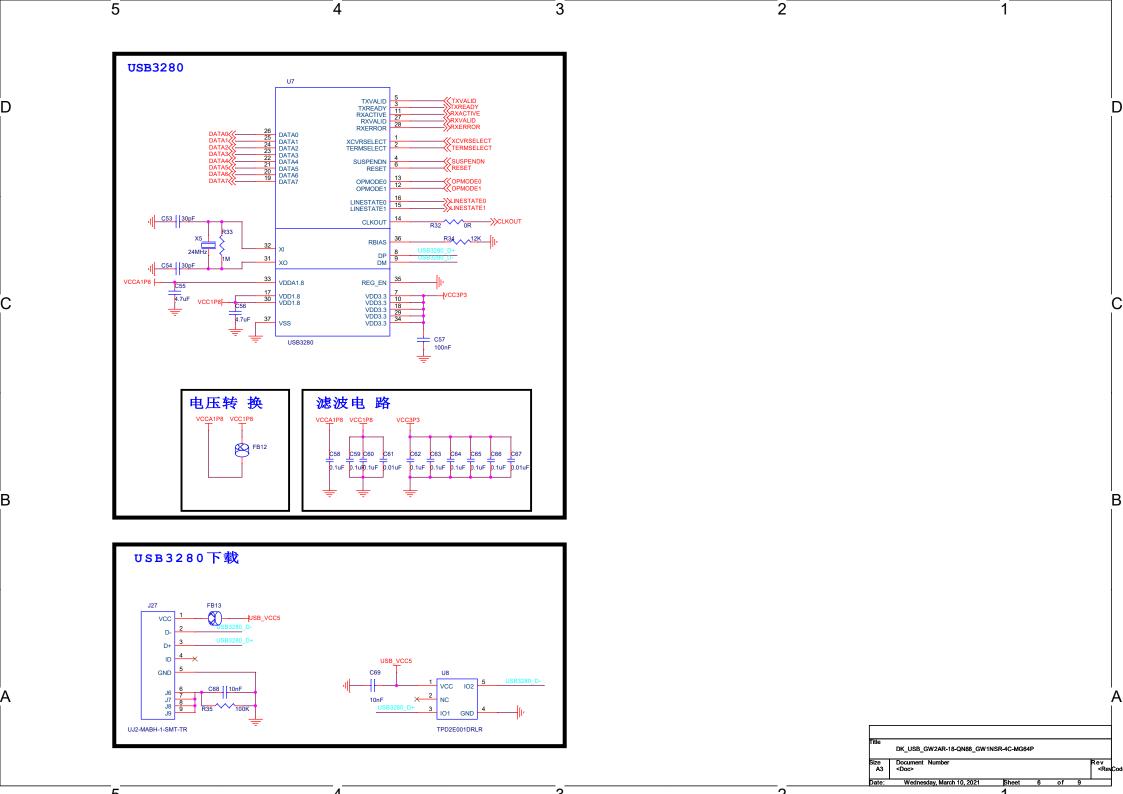
SCHEMATIC PAGE CONTENT PAGE1 COVER PAGE 1 DC5V to 3.3V, 2.5V, 1.8V, 1.2V, 1.0VPAGE2-POWER 2 PAGE3-FPGA GW1NSR-4C-MG64P 3 PAGE4-FPGA GW2AR-18K-QN88P 4 PAGE5-GPIO GPIO 5 PAGE6-USB1 USB3250 6 PAGE7-USB2 USBRC 7 PAGE8-USB3 USB-FPGA 8 PAGE9-JTAG 9 JTAG



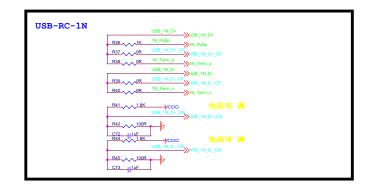


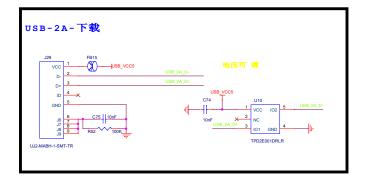
5 GW2AR-18K-QN88P Flash C29 0.1uF MSPI_DO MSPI_HOLD CLK MSPI DI GND W25Q64 R22 100K VCC3P3 F2_RST_N C30 10uF KO2 VCC VSS VCC VCCO2/VCCO7 IOL7A/LPLL1 T IN/DQ0/BANK7 VCCO2/VCCO7 时钟 12MHZ 1.8V-3.3V 4Pin IOR25B/TMS/DQ10/BANK2 IOR29A/GCLKT_3/LVDS/DQ9/BANK3 IOR33A/MI/D7/LVDS/DQ9/BANK3 IOR26A/TCK/LVDS/DQ10/BANK2 IOR26B/TDI/LVDS/DQ10/BANK2 IOR33B/MO/D6/LVDS/DQ9/BANK3 IOR25A/TDO/DQ10/BANK2 IOR31B/RECONFIG N/LVDS/DQ9/BANK3 IOR34A/MCS_N/D5/DQ9/BANK3 IOR34B/MCLK/D4/DQ9/BANK3 FB10 IOL29A/GCLKT_6/LVDS/DQ2/BANK6 IOL29B/GCLKC_6/LVDS/DQ2/BANK6 VCCO3
IOR35A/FASTRD_N/D3/LVDS/DQ9/BANK3 -VCC3P3 VCCX/VCCO1/VCCO6 IOL45A/LPLL2_T_IN/DQ2/BANK6 IOR36A/SO/D1/DQS9/BANK3 IOR36B/SSPI CS N/D0/DQS9/BANK3 2_OPMODE1 VCCPLLL0[-IOR38A/DIN/CLKHOLD_N/LVDS/DQ9/BANK3 IOR38B/DOUT/WE_N/LVDS/DQ9/BANK3 \$\frac{1}{2}\$ IOR39A/SCLK/DQ9/BANK3 IOL47A/LPLL2 T FB/LVDS/DQ3/BANK6 IOL47B/LPLL2_C_FB/LVDS/DQ3/BANK6 IOL49A/LVDS/DQ3/BANK6 F2 LINESTATE0 GNDOUT IOR45A/RPLL2_T_IN/DQ9/BANK3 VCCPLLR1 OT322512MJBA4SL VCCPLLR0/1 IOR49A/LVDS/DQ8/BANK3 IOR49B/LVDS/DQ8/BANK3 EXTR VSS 10K LED LED3 R26 1K IIS-CLK 8.192MHz GW2AR-18K-QFN88P X4 8.192MH IN VCC GNDOUT OSC S4 2520 模式选 择 0 0 C37 C39 C40 C45 C50 DC3_Header_3P_2R54TD 0.1uF 0.1uF 0.1uF DK_USB_GW2AR-18-QN88_GW1NSR-4C-MG64P Size A3 Document Number

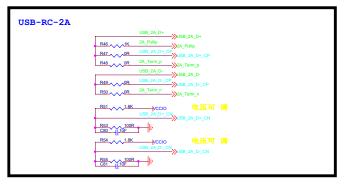




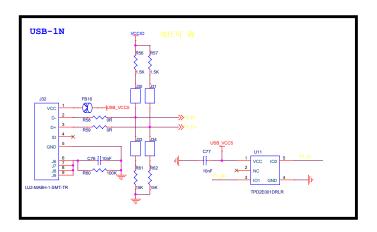
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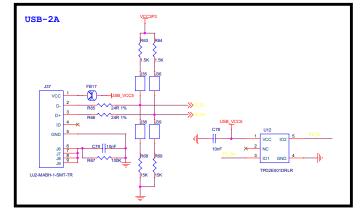




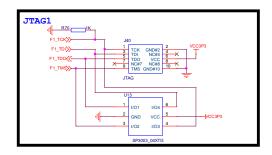


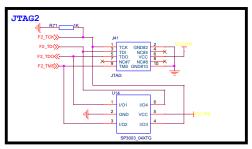
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