

ZENER DIODE AS VOLTAGE REGULATOR

AIM:

To simulate the line and load regulation operation zener diode.

APPARATUS REQUIRED:

Laptop with Proteus software

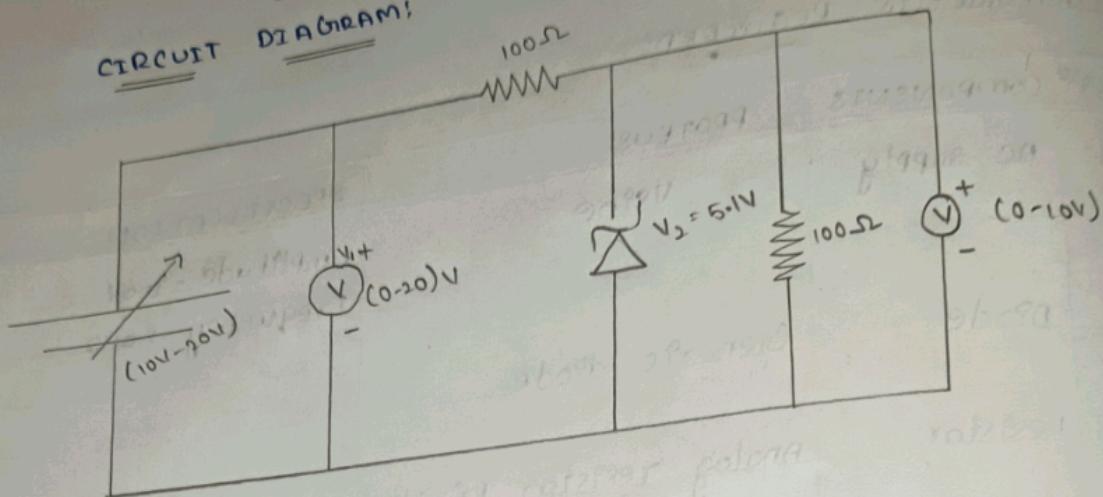
THEORY:

A heavily doped PN junction diode is called zener diode. Due to heavily doped nature zener diode works under forward bias and reverse bias conditions.

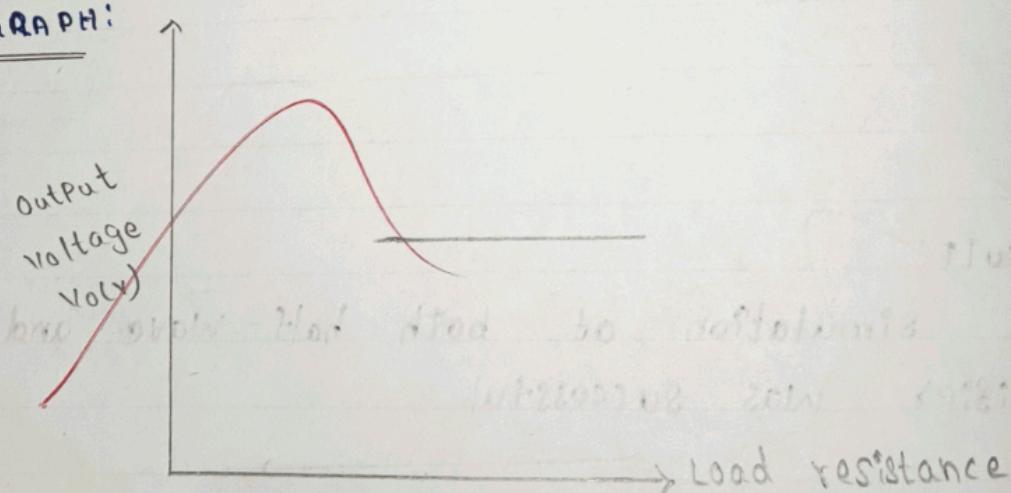
Zener diode under reverse bias condition is used as a voltage regulator. Even if input voltage changes the output is constant. This is called line regulation.

If the load resistance changes the output voltage is constant this is called load regulation.

Line regulation
CIRCUIT DIAGRAM:

TABULATION:

SNO	Input voltage V_i (V)	Output voltage V_o (V)
1.	+12.0	+5.14
2.	+13.0	+5.16
3.	+14.0	+5.17
4.	+15.0	+5.17
5.	+16.0	+5.18
6.	+17.0	+5.19

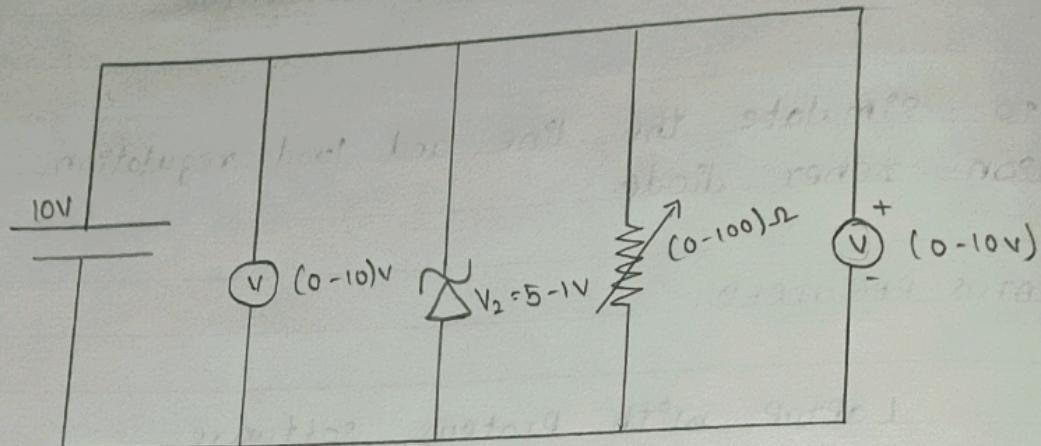
MODEL GRAPH:

PROCEDURE:

1. Drag the required components from the Proteus library.
2. connect the components (required) as per circuit diagram
3. For line regulation vary the input voltage and note the output voltages in tabulation.
Draw the graph between input voltage and output voltage.
4. For load regulations vary the load resistance and note the output voltage in tabulation. Draw the graph between load resistance and output voltage.

Load Regulation

CIRCUIT DIAGRAM



TABULATION:

S.NO	Load Resistance R ₂ (Ω)	Output Voltage V ₀ (V)
1.	100Ω	+5.19
2.	200Ω	+5.20
3.	300Ω	+5.20
4.	400Ω	+5.20
5.	500Ω	+5.20
6.	600Ω	+5.21

MODEL GRAPH:

