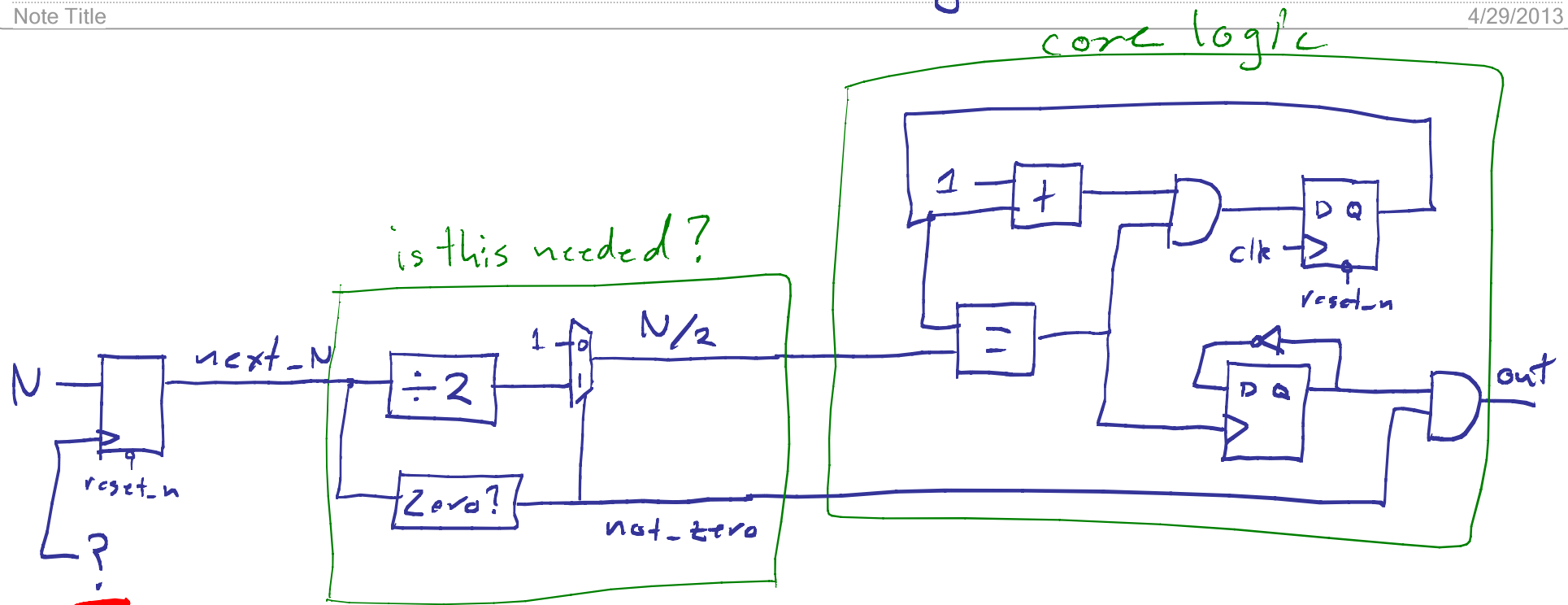


HW 2 Block Diagram

Note Title

4/29/2013



enable pin is not modeled here

use this to synchronize N with output clock