Snorkling clock

Variant: EUROPE

Block diagram:

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Introduction

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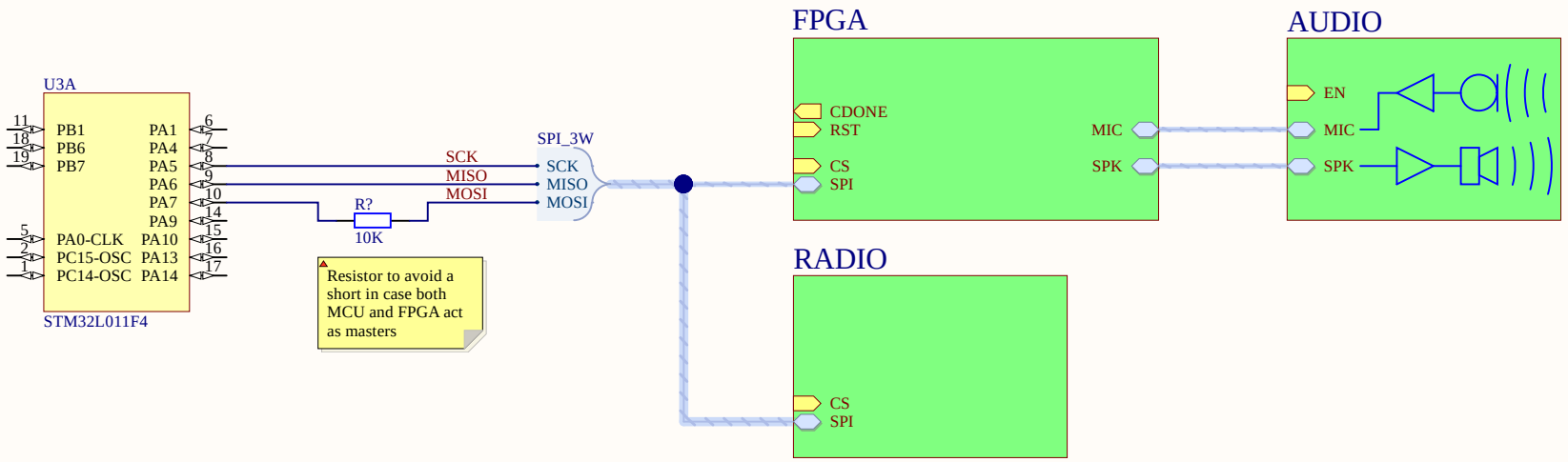
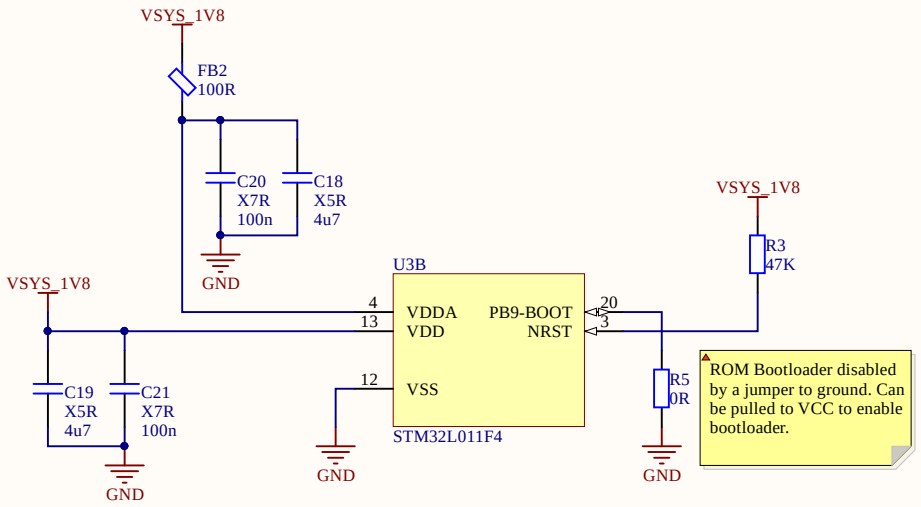
Specifications

Text

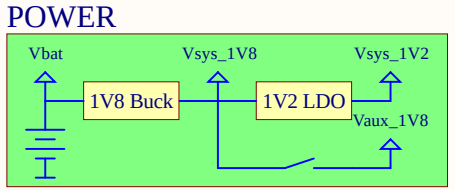
Title *Snorkling clock - Cover*			*GPA & FRYK Industries*
Size: A4	Number:1	Revision:1	
Date: 2018-10-09 Time: 18:29:57		Sheet 1 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			



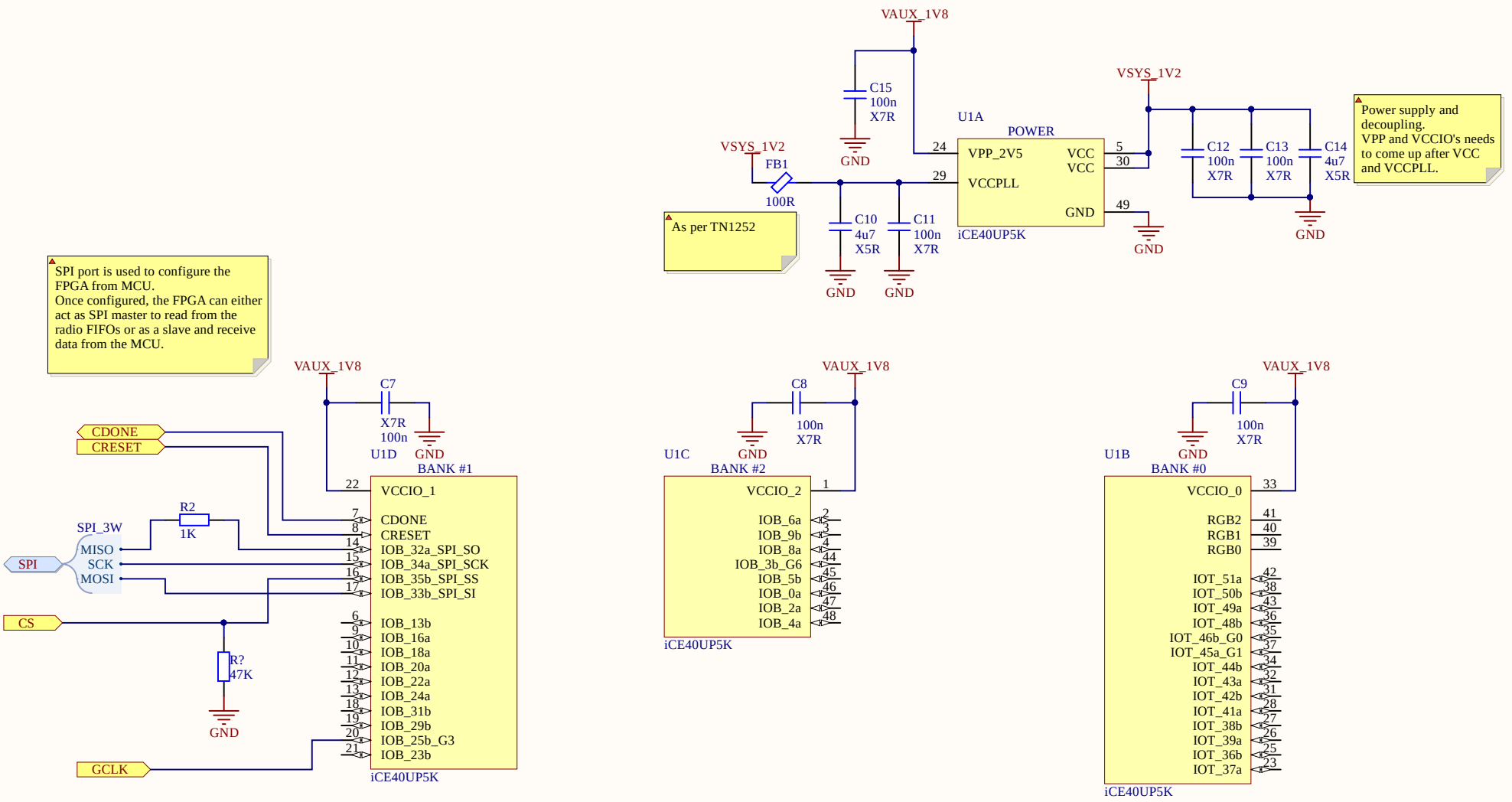


TODO:

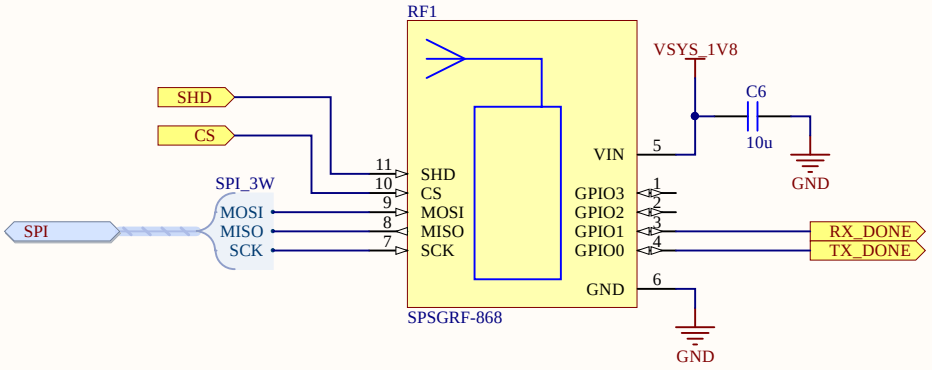
* Check decoupling according to manufacturer
* MCO to fpga
* Reset switch



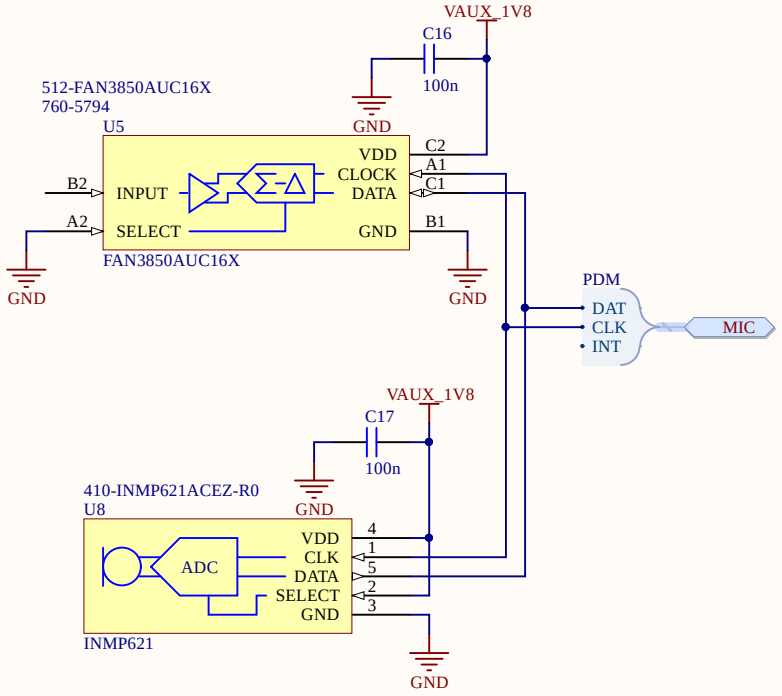
Title: ***Snorkling clock - MCU***			*GPA & FRYK Industries*
Size: A4	Number: 2	Revision: 1	
Date: 2018-10-09	Time: 18:29:57	Sheet 2 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			

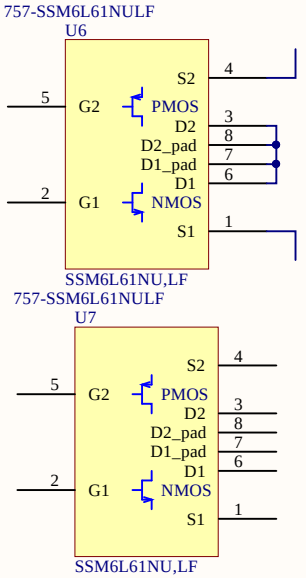


Title	***Snorkling clock - FPGA***		*GPA & FRYK Industries*
Size: A4	Number:3	Revision:1	
Date: 2018-10-09	Time: 18:29:58	Sheet 3 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			

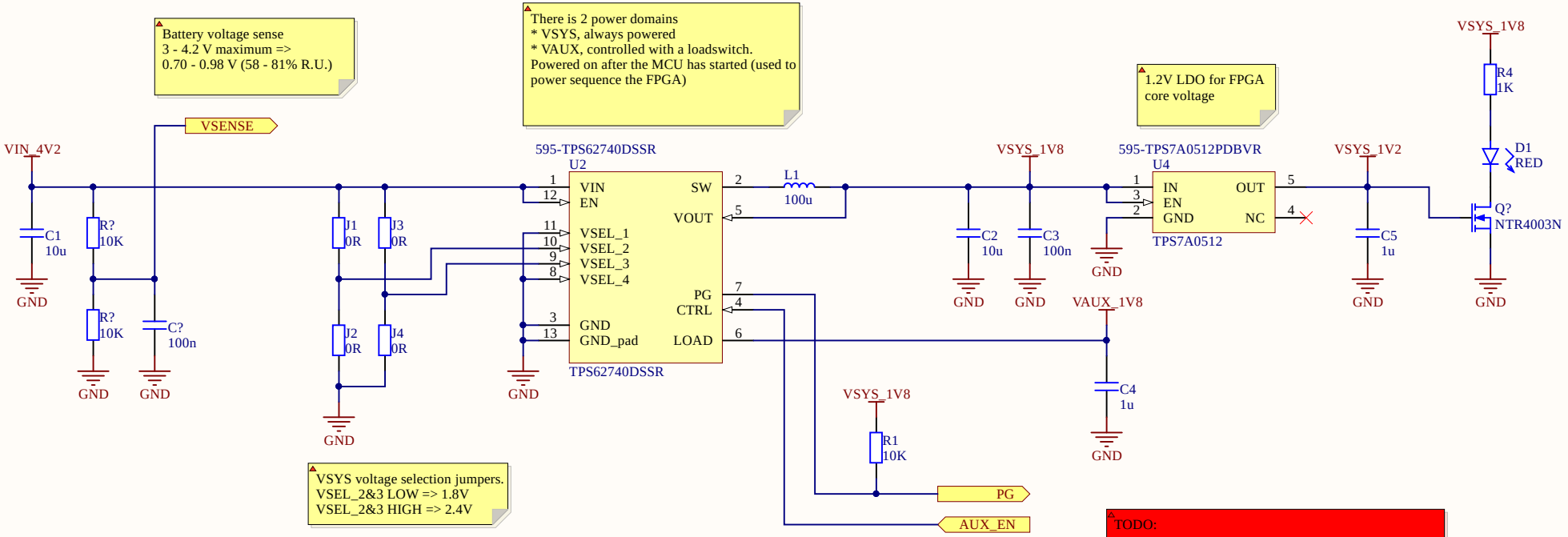


Title **Snorkling clock - Radio module**			*GPA & FRYK Industries*
Size: A4	Number:4	Revision:1	
Date: 2018-10-09 Time: 18:29:58		Sheet 4 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			





Title	***Snorkling clock - Audio frontend***		*GPA & FRYK Industries*
Size: A4	Number:5	Revision:1	
Date: 2018-10-09	Time: 18:29:58	Sheet5 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			



Title	***Snorkling clock - Power supply***		*GPA & FRYK Industries*
Size: A4	Number:6	Revision:1	
Date: 2018-10-09	Time: 18:29:58	Sheet6 of 6	
Repository: https://github.com/gpa-fryk-industries/CDIO			