



Block diagram:

Snorkling clock

Variant: EUROPE

Table of content

s.1 - Cover

s.2 - MCU

s.3 - FPGA

s.4 - Radio module

s.5 - Audio frontend

s.6 - Power supply

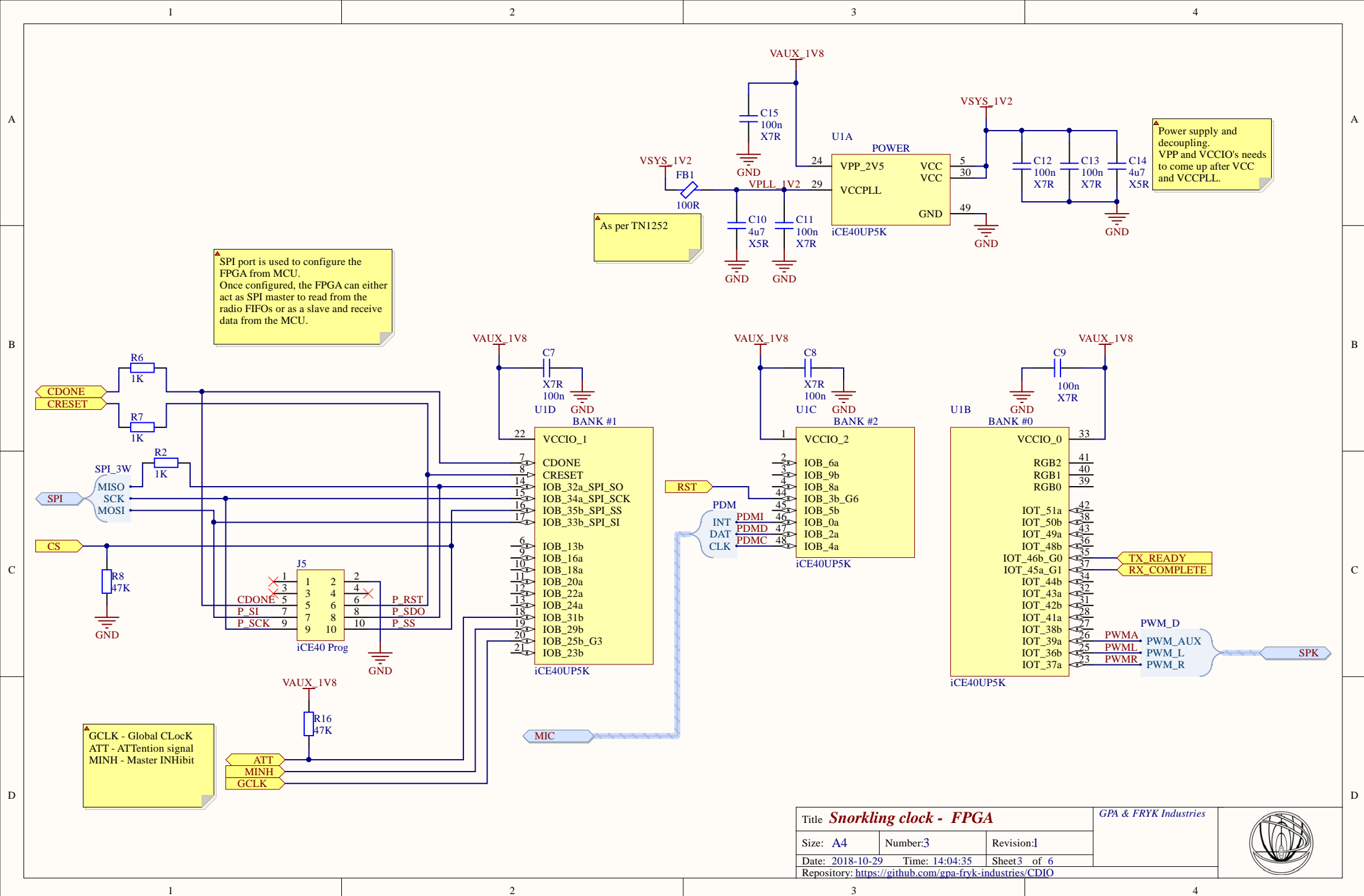
Introduction

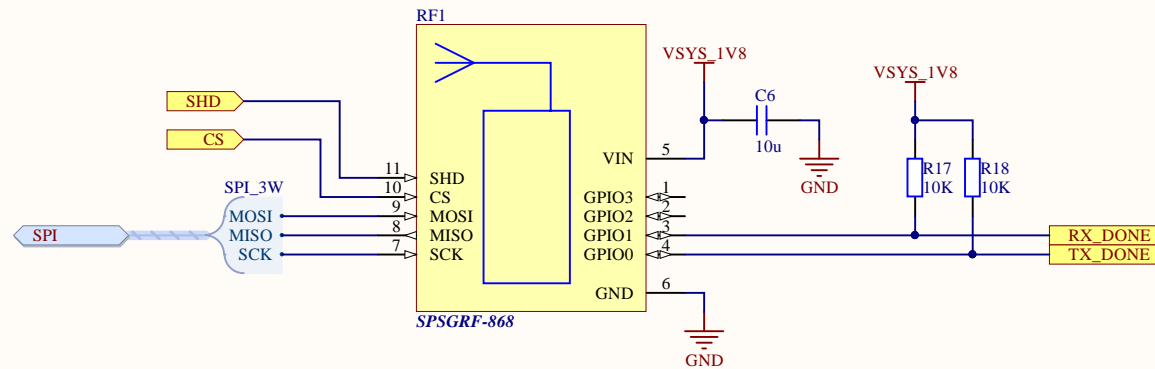
Text

Specifications

Text







Title **Snorkling clock - Radio module**

GPA & FRYK Industries

Size: **A4**

Number: **4**

Revision: **1**

Date: **2018-10-29**

Time: **14:04:35**

Sheet **4** of **6**

Repository: <https://github.com/gpa-fryk-industries/CDIO>



Speaker & driver

