

Snorkling clock

Variant: [No Variations]

Table of content

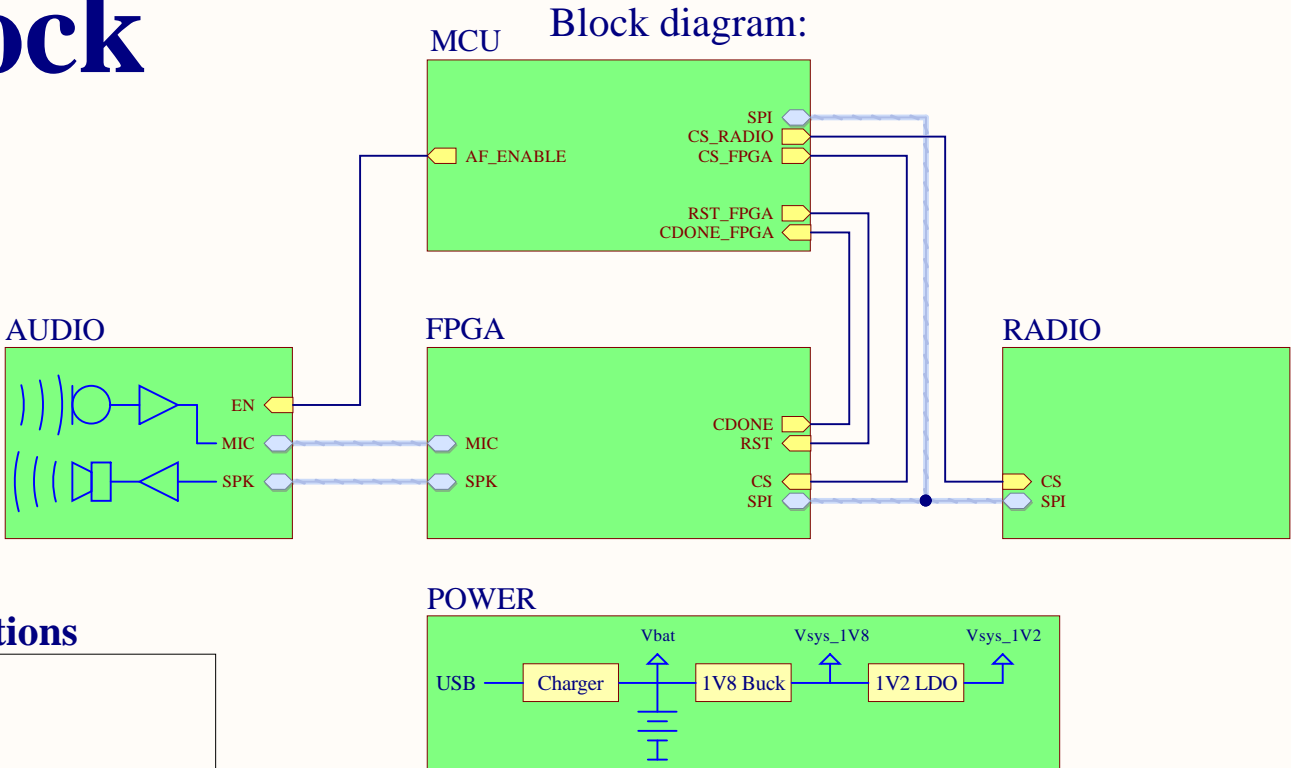
- s.1 - Cover
- s.2 - MCU
- s.3 - FPGA
- s.4 - Radio module
- s.5 - Audio frontend
- s.6 - Power supply

Introduction

Text

Specifications

Text



Title **Snorkling clock - Cover**

GPA & FRYK Industries

Size: **A4**

Number:1

Revision:1

Date: 2018-09-20


Time: 19:22:23

Sheet 1 of 6


Repository: <https://github.com/gpa-fryk-industries/CDIO>

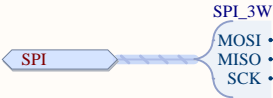


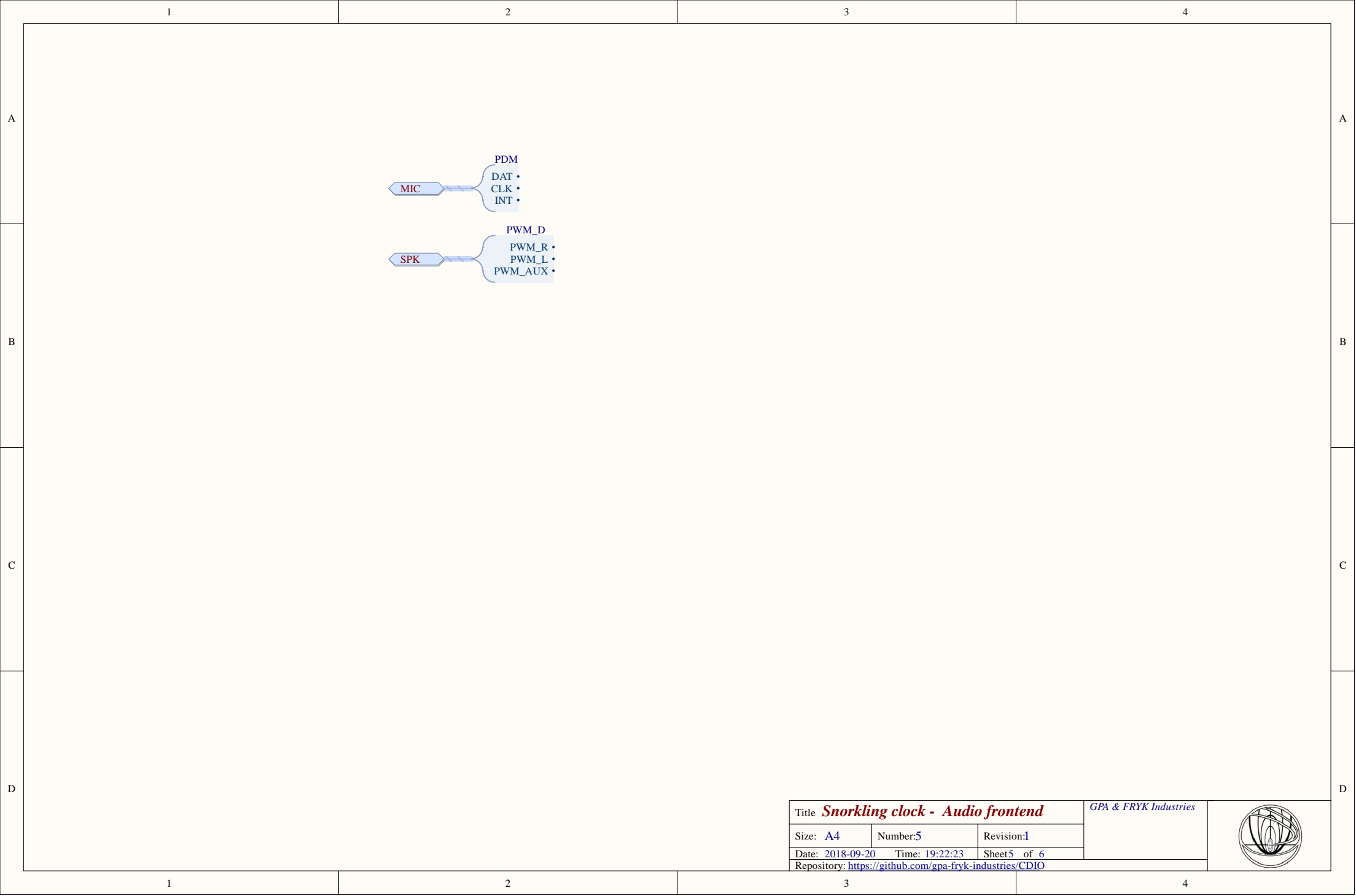
	1	2	3	4
A				
B				
C				
D				
	1	2	3	4

Title <i>Snorkling clock - MCU</i>			GPA & FRYK Industries	
Size: A4	Number:2	Revision:1		
Date: 2018-09-20	Time: 19:22:23	Sheet2 of 6		
Repository: https://github.com/gpa-fryk-industries/CDIO				

	1	2	3	4
A				
B				
C				
D				
	1	2	3	4

Title <i>Snorkling clock - FPGA</i>			GPA & FRYK Industries	
Size: A4	Number:3	Revision:1		
Date: 2018-09-20	Time: 19:22:23	Sheet3 of 6		
Repository: https://github.com/gpa-fryk-industries/CDIO				





1	2	3	4
A			A
B			B
C			C
D			D
1	2	3	4

Title <i>Snorkling clock - Power supply</i>			<i>GPA & FRYK Industries</i>
Size: <i>A4</i>	Number: <i>6</i>	Revision: <i>1</i>	
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Repository: https://github.com/gpa-fryk-industries/CDIO			

