

DEPARTMENT OF COMPUTER ENGINEERING**LAB TIME TABLE**

Academic Year : 2016-17 Semester :II (ver-1.0)

DAYS	10.00 to 12.00		LONG RECESS : 12.00 - 12.45	12.45 to 14.45		SHORT RECESS : 14.45 - 15.00	15.00 to 17.00	
	batch	Location		batch	Location		batch	Location
MON				S1 DS (VSG)	OSL		B1 CL1(AYP)	ESL
				S2 OPP (DAL)	LL		B2 CL2 (RMS)	SL
				T1 ESDL (PRN)	CL-II		B3 CL1 (BJP)	MML
				T2 ESDL (AAP)	ESL			
TUE	T1 PL2 (DAL)	LL		S1 OPP (DAL)	LL		B1 CL1 (BJP)	MML
	T2 PL1 (KVU)	OSL		S2 DS (VSG)	OSL		B2 CL2 (PRN)	LL
							B3 CL1 (AYP)	ESL
WED	T1 PL1 (KVU)	OSL		S1 DS (VSG)	OSL		B1 CL2 (RMS)	SL
	T2 PL2 (DAL)	LL		S2 DELD (SNM)	MML		B2 CL1 (AYP)	ESL
							B3 CL2 (PRN)	LL
THU	T1 PL2 (DAL)	LL		S1 DELD (SNM)	MML		B1 CL2 (RMS)	SL
	T2 PL1 (KVU)	OSL		S2 DS (VSG)	OSL		B2 CL1 (AYP)	ESL
							B3 CL2 (PRN)	LL
FRI	T1 PL2 (DAL)	LL						
	T2 PL1 (KVU)	OSL						
	Project Work			Project Work			Project Work	
SAT	Academic Activity		Academic Activity	CL-II : 307, LL : 308, SL : 309, OSL : 313, ESL : 315, MML : 316				
	Academic Activity		Academic Activity					
	Academic Activity		Academic Activity					