## DSI\_Ch0\_results

Gain = 63							
					1ADU=21.4uV		
RC (6-bit)	out mean (ADU)	out sigma (ADU)	RC meas (us)	RC err (us)	out ampl (V)	out err (V)	
1	51595.1	31.9	1.52	0.16	1.104	0.001	
3	64860.6	51.8	1.21	0.13	1.388	0.001	
5	81633.8	80.7	0.96	0.10	1.747	0.002	
7	93754.7	103.8	0.84	0.09	2.006	0.002	
9	127466.8	179.4	0.62	0.07	2.728	0.004	

Gain = 45							
					1ADU=21.4uV		
RC (6-bit)	out mean (ADU)	out sigma (ADU)	RC meas (us)	RC err (us)	out ampl (V)	out err (V)	
1	48852.7	29.4	1.26	0.14	1.045	0.001	
3	58519.2	41.2	1.05	0.11	1.252	0.001	
5	70820.9	61.4	0.87	0.09	1.516	0.001	
7	79803.1	79.8	0.77	0.08	1.708	0.002	
9	104307.0	134.0	0.59	0.06	2.232	0.003	
11	113104.7	154.0	0.55	0.06	2.420	0.003	
13	124382.7	167.1	0.50	0.05	2.662	0.004	

	Gain = 30							
					1ADU=21.4uV			
RC (6-bit)	out mean (ADU)	out sigma (ADU)	RC meas (us)	RC err (us)	out ampl (V)	out err (V)		
1	46582.5	28.4	0.96	0.10	0.997	0.001		
3	53235.7	33.3	0.84	0.09	1.139	0.001		
5	61730.4	46.9	0.73	0.08	1.321	0.001		
7	67963.2	58.4	0.66	0.07	1.454	0.001		
9	85063.3	90.1	0.53	0.06	1.820	0.002		
11	90960.0	102.8	0.49	0.05	1.947	0.002		
13	98618.0	118.1	0.46	0.05	2.110	0.003		
15	104549.5	136.1	0.43	0.05	2.237	0.003		
17	120571.7	175.9	0.37	0.04	2.580	0.004		
19	125921.0	179.8	0.36	0.04	2.695	0.004		

Gain = 15							
					1ADU=21.4uV		
RC (6-bit	out mean (ADU)	out sigma (ADU)	RC meas (us)	RC err (us)	out ampl (V)	out err (V)	
	1 44313.0	28.1	0.58	0.06	0.948	0.001	
	3 47927.6	28.5	0.53	0.06	1.026	0.001	
	5 52573.0	34.9	0.49	0.05	1.125	0.001	
	7 56000.6	38.9	0.46	0.05	1.198	0.001	
	9 65485.9	55.0	0.39	0.04	1.401	0.001	
	11 68796.4	63.0	0.37	0.04	1.472	0.001	
	13 73171.5	75.1	0.35	0.04	1.566	0.002	
	15 76403.7	79.4	0.34	0.04	1.635	0.002	
	17 85114.3	100.2	0.30	0.03	1.821	0.002	
	19 88220.0	107.2	0.29	0.03	1.888	0.002	
	21 92296.0	117.2	0.28	0.03	1.975	0.003	
	23 95292.7	125.4	0.27	0.03	2.039	0.003	
	25 103789.0	158.9	0.25	0.03	2.221	0.003	
·	27 106845.5	167.8	0.24	0.03	2.286	0.004	
	29 110863.7	182.8	0.23	0.03	2.372	0.004	
	31 113855.3	189.4	0.23	0.02	2.437	0.004	

DSI\_Ch0\_results

CF1							
					1ADU=21.4uV		
RC (6-bit)	out mean (ADU)	out sigma (ADU)	RC meas (us)	RC err (us)	out ampl (V)	out err (V)	
1	45972.6	29.3	0.92	0.10	0.984	0.001	
3	52063.4	42.0	0.81	0.09	1.114	0.001	
5	59926.9	64.2	0.70	0.08	1.282	0.001	
7	65700.1	83.1	0.64	0.07	1.406	0.002	
9	81543.5	132.6	0.52	0.06	1.745	0.003	
11	87019.2	152.9	0.49	0.05	1.862	0.003	
13	94198.7	178.4	0.45	0.05	2.016	0.004	
15	99420.1	210.9	0.43	0.05	2.128	0.005	
17	114332.0	276.9	0.37	0.04	2.447	0.006	
19	119618.0	292.9	0.35	0.04	2.560	0.006	
21	126256.0	307.5	0.33	0.04	2.702	0.007	

