Waveform 1 - SimVision

Cursor = 20,000ns
Baseline = 0
Cursor-Baseline = 20,000ns

Signal	Value	5100ns	5200ns	5300ns	5400ns	5500ns	5600ns	5700ns
CK	0							
In[31:0]	'h00001034	00001024	00001028	0000102C	00001030	00001018	00001034	
Out[31:0]	'h00001034	00001020	00001024	00001028	0000102C	00001030	00001018	00001034
pc[31:0]	'h00001034	00001020	00001024	00001028	0000102C	00001030	00001018	00001034
CK	0							
ReadData1[31:0]	'h00000000	00005018		0000503C		00000000	00000007	00000000
ReadData2[31:0]	'h00000000	00000014	00000004	00000014	00000004	00000000	00000007	00000000
ReadRegster1[4:0]	'h00	0B		0D		00	08	00
ReadRegster2[4:0]	'h00	0E	0C	0E	0C	00	09	00
RegWrite	0							
WriteData[31:0]	'hxxxxxxxx	00000014	0000501C	xxxxxxxx	00005040	xxxxxxxx		
WriteRegster[4:0]	'h00	0E	0B	0X	0D	00	0X	00
i	'dx	x						
index	'd32	32						
regfile[1:31]	31 x 32 b	[31 x 32 bits]						
A[31:0]	'h00000000	00005018		0000503C		00000000	00000007	00000000
ALUoperation[3:0]	'hx	2				x	6	x
ALUresult[31:0]	'h00000000	00005018	0000501C	0000503C	00005040	00000000		
B[31:0]	'h00000X0X	00000000	00000004	00000000	00000004	00000X0X	00000007	00000X0X
Zero	1							
aluresult[31:0]	'h00000000	00005018	0000501C	0000503C	00005040	00000000		
tmp[32:0]	'hxxxxxxxxx	000005018	000005014	00000503C	000005038	xxxxxxxxx	000000000	xxxxxxxxx
Address[31:0]	'h00000000	00005018	0000501C	0000503C	00005040	00000000		
CK	0							
MemRead	0							
MemWrite	0							
ReadData[31:0]	'hxxxxxxxx	x x 00000014	xxxxxxxx					
WriteData[31:0]	'h00000000	00000014	00000004	00000014	00000004	00000000	00000007	00000000