Module Interface Specification for CVT Simulator

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1 Revision History

Date	Version	Notes
Date 1	1.0	Notes
Date 2	1.1	Notes

2 Symbols, Abbreviations and Acronyms

See SRS Documentation at [give url —SS]
    [Also add any additional symbols, abbreviations or acronyms —SS]

Contents

3 Introduction

The following document details the Module Interface Specifications for [Fill in your project name and description —SS]

Complementary documents include the System Requirement Specifications and Module Guide. The full documentation and implementation can be found at .... [provide the url for your repo —SS]

4 Notation

[You should describe your notation. You can use what is below as a starting point. —SS]

The structure of the MIS for modules comes from Hoffman and Strooper (1995), with the addition that template modules have been adapted from Ghezzi et al. (2003). The mathematical notation comes from Chapter 3 of Hoffman and Strooper (1995). For instance, the symbol := is used for a multiple assignment statement and conditional rules follow the form $(c_1 \Rightarrow r_1 | c_2 \Rightarrow r_2 | ... | c_n \Rightarrow r_n)$.

The following table summarizes the primitive data types used by CVT Simulator.

Data Type	Notation	Description
character	char	a single symbol or digit
integer	$\mathbb{Z}$	a number without a fractional component in $(-\infty, \infty)$
natural number	$\mathbb{N}$	a number without a fractional component in $[1, \infty)$
real	$\mathbb{R}$	any number in $(-\infty, \infty)$

The specification of CVT Simulator uses some derived data types: sequences, strings, and tuples. Sequences are lists filled with elements of the same data type. Strings are sequences of characters. Tuples contain a list of values, potentially of different types. In addition, CVT Simulator uses functions, which are defined by the data types of their inputs and outputs. Local functions are described by giving their type signature followed by their specification.

5 Module Decomposition

The following table is taken directly from the Module Guide document for this project.

Level 1	Level 2
Hardware-Hiding	
Behaviour-Hiding	Input Parameters
	Output Format
	Output Verification
	Temperature ODEs
	Energy Equations
	Control Module
	Specification Parameters Module
Software Decision	Sequence Data Structure
	ODE Solver
	Plotting

Table 1: Module Hierarchy

6 MIS of Hardware Hiding Module

[Use labels for cross-referencing —SS]

[You can reference SRS labels, such as R**??**. —SS]

[It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

6.1 Module

[Short name for the module —SS]

6.2 Uses

None.

6.3 Syntax

6.3.1 Exported Constants

6.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

6.4 Semantics

6.4.1 State Variables

[Not all modules will have state variables. State variables give the module a memory. —SS]

6.4.2 Environment Variables

[This section is not necessary for all modules. Its purpose is to capture when the module has external interaction with the environment, such as for a device driver, screen interface, keyboard, file, etc. —SS]

6.4.3 Assumptions

[Try to minimize assumptions and anticipate programmer errors via exceptions, but for practical purposes assumptions are sometimes appropriate. —SS]

6.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

[A module without environment variables or state variables is unlikely to have a state transition. In this case a state transition can only occur if the module is changing the state of another module. —SS]

[Modules rarely have both a transition and an output. In most cases you will have one or the other. —SS]

6.4.5 Local Functions

[As appropriate —SS] [These functions are for the purpose of specification. They are not necessarily something that is going to be implemented explicitly. Even if they are implemented, they are not exported; they only have local scope. —SS]

7 Engine Simulator Module

[Use labels for cross-referencing —SS]
  [You can reference SRS labels, such as R**??**. —SS]
  [It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

7.1 Module

[Short name for the module —SS]

7.2 Uses

- Constants Module (15)

7.3 Syntax

7.3.1 Exported Constants

7.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

7.4 Semantics

7.4.1 State Variables

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- output: [if appropriate —SS]

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7.4.5   Local Functions

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8 External Forces Module

[Use labels for cross-referencing —SS]

    [You can reference SRS labels, such as R**??**. —SS]

    [It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

8.1 Module

[Short name for the module —SS]

8.2 Uses

- Constants Module (15)

8.3 Syntax

8.3.1 Exported Constants

8.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

8.4 Semantics

8.4.1 State Variables

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9 MIS of CVT Simulation Module

[Use labels for cross-referencing —SS]

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[It is also possible to use LATEXfor hypperlinks to external documents. —SS]

9.1 Module

[Short name for the module —SS]

9.2 Uses

- Constants Module (15)

9.3 Syntax

9.3.1 Exported Constants

9.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

9.4 Semantics

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9.4.5 Local Functions

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10 MIS of Input Module

[Use labels for cross-referencing —SS]

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[It is also possible to use LATEXfor hypperlinks to external documents. —SS]

10.1 Module

[Short name for the module —SS]

10.2 Uses

None.

10.3 Syntax

10.3.1 Exported Constants

10.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

10.4 Semantics

10.4.1 State Variables

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10.4.2 Environment Variables

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10.4.3 Assumptions

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10.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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10.4.5   Local Functions

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11 MIS of ODE Solver Module

[Use labels for cross-referencing —SS]

[You can reference SRS labels, such as R**??**. —SS]

[It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

11.1 Module

[Short name for the module —SS]

11.2 Uses

- Constants Module (15)

- CVT Simulation Module (9)

- External Forces Module (8)

- Engine Simulator Module (7)

11.3 Syntax

11.3.1 Exported Constants

11.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

11.4 Semantics

11.4.1 State Variables

[Not all modules will have state variables. State variables give the module a memory. —SS]

11.4.2 Environment Variables

[This section is not necessary for all modules. Its purpose is to capture when the module has external interaction with the environment, such as for a device driver, screen interface, keyboard, file, etc. —SS]

11.4.3 Assumptions

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11.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

[A module without environment variables or state variables is unlikely to have a state transition. In this case a state transition can only occur if the module is changing the state of another module. —SS]

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11.4.5 Local Functions

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12 MIS of Main Module

12.1 Module

[Short name for the module —SS]

12.2 Uses

- Communication Module (20)

- Visualizer Module (14)

12.3 Syntax

12.3.1 Exported Constants

12.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

12.4 Semantics

12.4.1 State Variables

[Not all modules will have state variables. State variables give the module a memory. —SS]

12.4.2 Environment Variables

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12.4.3 Assumptions

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12.4.4   Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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12.4.5   Local Functions

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13 MIS of Playback Module

[Use labels for cross-referencing —SS]

    [You can reference SRS labels, such as R**??**. —SS]

    [It is also possible to use L𝖠TₑXfor hypperlinks to external documents. —SS]

13.1 Module

[Short name for the module —SS]

13.2 Uses

None.

13.3 Syntax

13.3.1 Exported Constants

13.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

13.4 Semantics

13.4.1 State Variables

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13.4.2 Environment Variables

[This section is not necessary for all modules. Its purpose is to capture when the module has external interaction with the environment, such as for a device driver, screen interface, keyboard, file, etc. —SS]

13.4.3 Assumptions

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13.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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13.4.5   Local Functions

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14 MIS of Visualizer Module

[Use labels for cross-referencing —SS]

[You can reference SRS labels, such as R**??**. —SS]

[It is also possible to use LATEXfor hypperlinks to external documents. —SS]

14.1 Module

[Short name for the module —SS]

14.2 Uses

- GUI Module (18)

14.3 Syntax

14.3.1 Exported Constants

14.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

14.4 Semantics

14.4.1 State Variables

[Not all modules will have state variables. State variables give the module a memory. —SS]

14.4.2 Environment Variables

[This section is not necessary for all modules. Its purpose is to capture when the module has external interaction with the environment, such as for a device driver, screen interface, keyboard, file, etc. —SS]

14.4.3 Assumptions

[Try to minimize assumptions and anticipate programmer errors via exceptions, but for practical purposes assumptions are sometimes appropriate. —SS]

14.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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14.4.5   Local Functions

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15 MIS of Constants Module

[Use labels for cross-referencing —SS]
   [You can reference SRS labels, such as R**??**. —SS]
   [It is also possible to use LATEXfor hypperlinks to external documents. —SS]

15.1 Module

[Short name for the module —SS]

15.2 Uses

None.

15.3 Syntax

15.3.1 Exported Constants

15.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

15.4 Semantics

15.4.1 State Variables

[Not all modules will have state variables. State variables give the module a memory. —SS]

15.4.2 Environment Variables

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15.4.3 Assumptions

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15.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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15.4.5   Local Functions

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16 MIS of State Module

16.1 Module

[Short name for the module —SS]

16.2 Uses

None.

16.3 Syntax

16.3.1 Exported Constants

16.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

16.4 Semantics

16.4.1 State Variables

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16.4.2 Environment Variables

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16.4.3 Assumptions

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16.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

23

- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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16.4.5 Local Functions

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17 MIS of Backend Controller Module

[Use labels for cross-referencing —SS]

[You can reference SRS labels, such as R**??**. —SS]

[It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

17.1 Module

[Short name for the module —SS]

17.2 Uses

- Input Module (10)

- ODE Solver Module (11)

17.3 Syntax

17.3.1 Exported Constants

17.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

17.4 Semantics

17.4.1 State Variables

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17.4.2 Environment Variables

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18 MIS of GUI Module

18.1 Module

[Short name for the module —SS]

18.2 Uses

- Hardware Hiding Module (6)

18.3 Syntax

18.3.1 Exported Constants

18.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

18.4 Semantics

18.4.1 State Variables

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- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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19 MIS of File Output Module

[Use labels for cross-referencing —SS]

[You can reference SRS labels, such as R**??**. —SS]

[It is also possible to use LaTeXfor hypperlinks to external documents. —SS]

19.1 Module

[Short name for the module —SS]

19.2 Uses

None.

19.3 Syntax

19.3.1 Exported Constants

19.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

19.4 Semantics

19.4.1 State Variables

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19.4.2 Environment Variables

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19.4.4 Access Routine Semantics

[accessProg —SS]():

- transition: [if appropriate —SS]

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- output: [if appropriate —SS]

- exception: [if appropriate —SS]

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19.4.5 Local Functions

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20 MIS of Communication Module

20.1 Module

20.2 Uses

- Backend Controller Module (17)

20.3 Syntax

20.3.1 Exported Constants

20.3.2 Exported Access Programs

Name	In	Out	Exceptions
[accessProg —SS]	-	-	-

20.4 Semantics

20.4.1 State Variables

20.4.2 Environment Variables

20.4.3 Assumptions

20.4.4 Access Routine Semantics

[accessProg —SS]():

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References

Carlo Ghezzi, Mehdi Jazayeri, and Dino Mandrioli. *Fundamentals of Software Engineering.* Prentice Hall, Upper Saddle River, NJ, USA, 2nd edition, 2003.

Daniel M. Hoffman and Paul A. Strooper. *Software Design, Automated Testing, and Maintenance: A Practical Approach.* International Thomson Computer Press, New York, NY, USA, 1995. URL http://citeseer.ist.psu.edu/428727.html.

21   Appendix

[Extra information if required —SS]

Appendix — Reflection

The information in this section will be used to evaluate the team members on the graduate attribute of Problem Analysis and Design.

The purpose of reflection questions is to give you a chance to assess your own learning and that of your group as a whole, and to find ways to improve in the future. Reflection is an important part of the learning process. Reflection is also an essential component of a successful software development process.

Reflections are most interesting and useful when they're honest, even if the stories they tell are imperfect. You will be marked based on your depth of thought and analysis, and not based on the content of the reflections themselves. Thus, for full marks we encourage you to answer openly and honestly and to avoid simply writing "what you think the evaluator wants to hear."

Please answer the following questions. Some questions can be answered on the team level, but where appropriate, each team member should write their own response:

1. What went well while writing this deliverable?

2. What pain points did you experience during this deliverable, and how did you resolve them?

3. Which of your design decisions stemmed from speaking to your client(s) or a proxy (e.g. your peers, stakeholders, potential users)? For those that were not, why, and where did they come from?

4. While creating the design doc, what parts of your other documents (e.g. requirements, hazard analysis, etc), it any, needed to be changed, and why?

5. What are the limitations of your solution? Put another way, given unlimited resources, what could you do to make the project better? (LO_ProbSolutions)

6. Give a brief overview of other design solutions you considered. What are the benefits and tradeoffs of those other designs compared with the chosen design? From all the potential options, why did you select the documented design? (LO_Explores)