STM32:

- Sleep mode - only CPU is stopped, all peripherals will operate. Can wake up CPU when interrupt/event occurs
- Stop mode - all clocks in 1.2V domain stopped, PLL, HSI RC, and HSE crystal disabled
    - Voltage regulator - put in main regulator mode or low power mode
    - Wake from this mode by EXTI line - line source can be one of the 16 external lines, the PVD output, the RTC alarm / wakeup /tamper / time stamp events, the USB OTG FS/HS wakeup
    - SRAM and registers will retain all their data
- Standby - lowest power consumption.
    - SRAM and registers will LOSE all their data
        - Backup SRAM and backup registers still retain info
    - All devices in 1.2V domain are off
    - Exit - external reset (NRST pin), an IWDG reset, a rising edge on the WKUP pin, or an RTC alarm / wakeup / tamper /time stamp event occurs