

write_data_i

[7:0]

write_address_i

[3:0]

write_enable_i

read_X_data_o

[7:0]

read_Y_data_o

[7:0]

**Register
16x8
(1 write, 2 read)**

read_X_address_i

[3:0]

read_Y_address_i

[3:0]

reset_i

clk_i

