		constant_kk_o
		[7:0]
		constant_aaa_o
		[11:0]
		mux_i_o_select_o
instruction_i		slO_write_or_read_o
[17:0]		
		slO_enable_o
carry_i		anno maniatan aalaat a
zoro i		mux_register_select_o
zero_i		[1:0] sRegister_X_adresse_o
		[3:0]
		sRegister_Y_adresse_o
		[3:0]
		sRegister_write_enable_o
		or togister_write_eriable_e
	Instruction	mux_ALU_select_o
	Decoder	
		sALU_select_o
		[5:0]
		sALU_enable_o
		mux_stack_select_o
		sStack_write_or_read_o
		sStack_enable_o
reset_i		mux_PC_select_o
clk_i		sPC_enable_o
		aDAM wester are soon of
		sRAM_write_or_read_o
		sPAM enable o
		sRAM_enable_o