		constant_kk_o
		[7:0]
		constant_aaa_o
_instruction_i		[11:0]
[17:0]		
		mux_i_o_select_o
carry_i		
zoro i		sIO_write_or_read_o
zero_i		slO_enable_o
		mux_register_select_o
		[1:0]
		sRegister_X_adresse_o
		[3:0]
		sRegister_Y_adresse_o
		[3:0]
		sRegister_write_enable_o
	Instruction Decoder	mux_ALU_select_o
	Booddoi	a Al II aglact a
		sALU_select_o [5:0]
		sALU_enable_o
		o, izo_onabio_o
		mux_stack_select_o
		sStack_write_or_read_o
		sStack_enable_o
		mux_PC_select_o
reset_i		sPC_enable_o
clk_i		sRAM_write_or_read_o
	r	sRAM_enable_o