		constant_kk_o
instruction_i		[7:0]
[17:0]		constant_aaa_o
-		[11:0]
		mux_i_o_select_o
		register_X_ID_o
		[3:0]
		register_Y_ID_o
		[3:0]
		mux_register_select_o
ı		[1:0]
		mux_ALU_select_o
	Decoder	sALU_select_o
i	Decodei	[5:0]
ı		(5.1.)
		mux_stack_select_o
		The state of the s
		stack_enable_o
ı		
		mux_PC_select_o
		[1:0]
ı		or_Pc_o
i		01_1 0_0
ı		
		store_fetch_o
reset_i		Store_letor_o
i		sRAM_enable_o
clk_i		SKAW_enable_0