

**ENGINEER** 

Toronto, ON, Canada **P:** (519) 722- 7428

E: Graeme.k.cox@gmail.com

W: Linkedin.com/in/graemekcox/

## PROFESSIONAL PROFILE

I am a motivated and creative recent Engineering graduate seeking a fast paced and collaborative environment for professional growth. Proven record of effectively leading short-term and long-term projects to successful change management, implementation, and benefits realization.

# **EDUCATION**

#### 2013 - 2018

### **Electrical and Biomedical** Engineering

MCMASTER UNIVERSITY

- Bachelor degree with Dean's Honors List
- Graduated with a 3.7 cumulative GPA

### **SKILLS**

- Cross functional teamwork
- Stakeholder engagement
- Project management
- Data analysis
- Attention to detail
- Proficient with C++, C, Python, Systemverilog and Matlab
- Familiar with Java, Swift, Javascript, HTML and CSS
- Proficient with Altium Designer
- Familiar with Tensorflow, OpenCV and DirectX
- Familiar with Simulink, PSpice and Solidworks

### **EXPERIENCE**

#### ASIC Design Verification Engineer

Advanced Micro Devices (AMD) | May 2018 - Present Toronto, ON

- Developed scalable verification environment, including checkers and reference models, to implement test plans, using SystemVerilog and UVM
- Worked closely with design and architecture teams to understand functional and performance goals
- Ran simulations using VCS and OSCI, and debugged tests using waveform analysis
- Developed knowledge of various compute architecture systems, including memory controllers, graphic chips and timing chips
- Experience writing block subsystem tests, and full chip tests
- Implemented covergroups and assertions (SVA), while tracking coverage metrics and bugs
- Experience with industry standards and protocols such as DisplayPort, HDMI and
- Linux & other Unix environment Knowledge of video processing and pixel processing

#### **DISPLAY ARCHITECTURE INTERN**

Advanced Micro Devices (AMD) | May 2016– Aug 2017 Toronto, ON

- Designed, implemented, releases and maintained multiple testing applications used by numerous groups both internally and externally
- Oversaw the design and implementation of various C++, Python and MATLAB testing applications while applying project management skills
- Created and maintained documents such as user guides, errata, test plans and development documents for each application release

#### **Electrical Team Lead**

MAC Formula Electric | July 2015 – May 2018 Hamilton, ON

- Collaborated with fellow students, faculty advisors and industry professionals to design, manufacture and validate all High-voltage and Low-voltage systems
- Helped design and manufacture PCB while considering cost, size and safety requirements
- Demonstrated time management and organization skills to effectively plan separate systems while meeting competition deadlines
- Developed interpersonal skills by leading large team meetings and managing multiple electrical projects simultaneously

# INTERESTS

- Consumer goods
- Technology
- Piano and music
- Film
- Skateboarding and skiing
- Running and fitness
- Travel