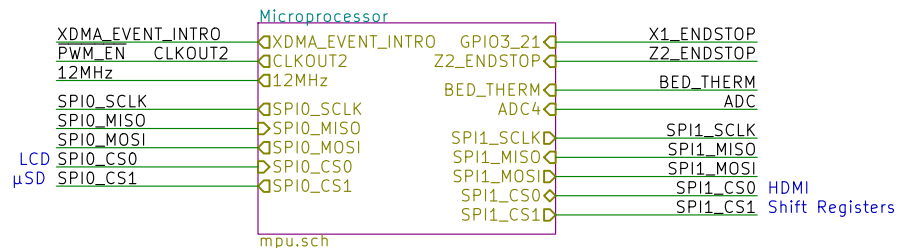
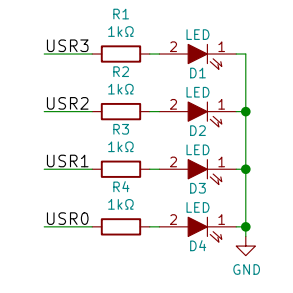
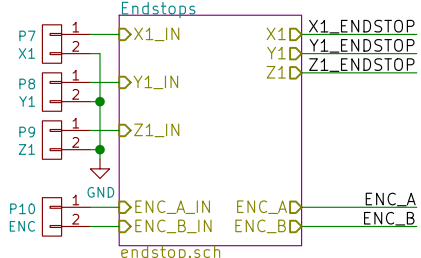


Power

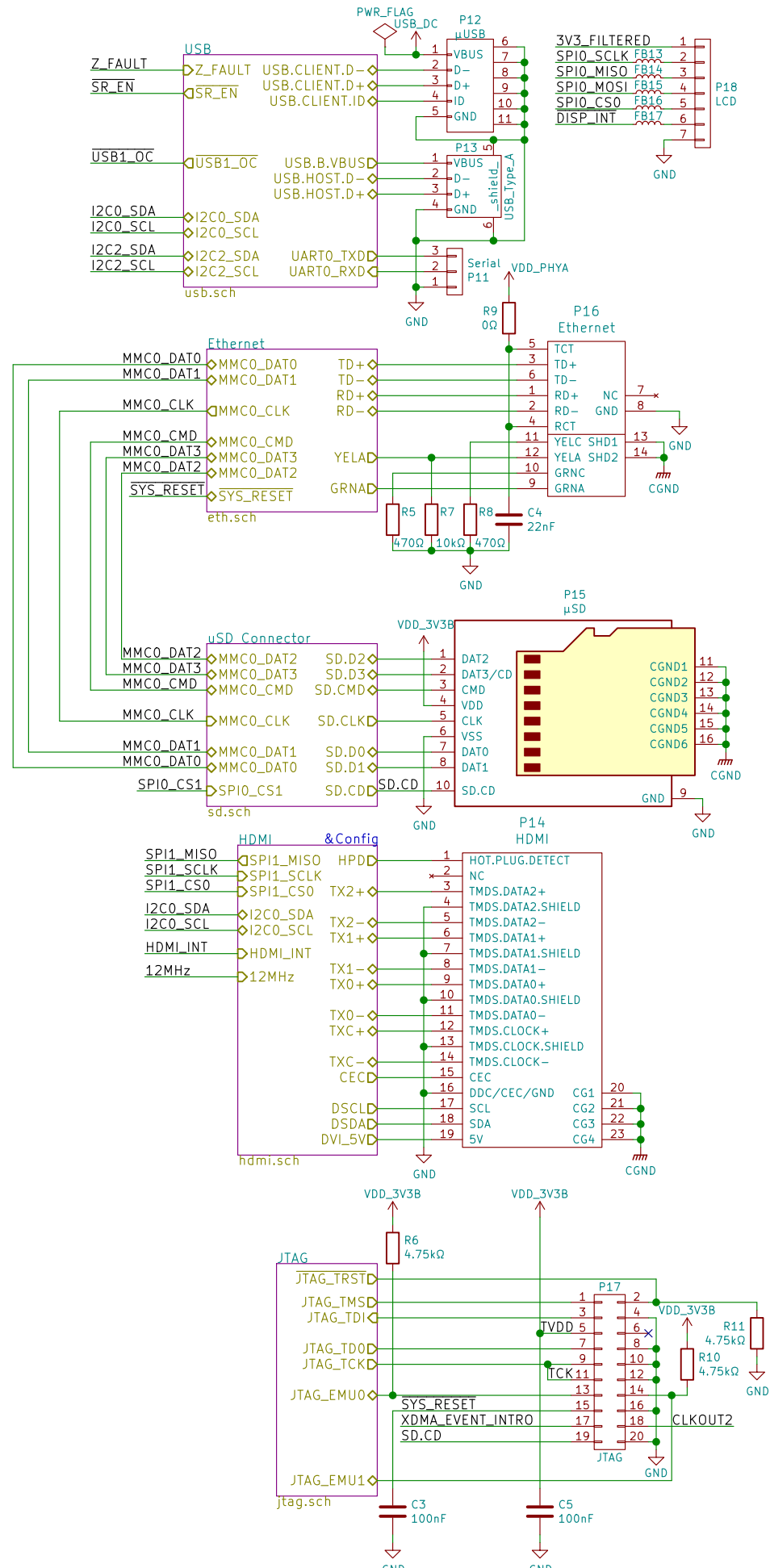
power.sch



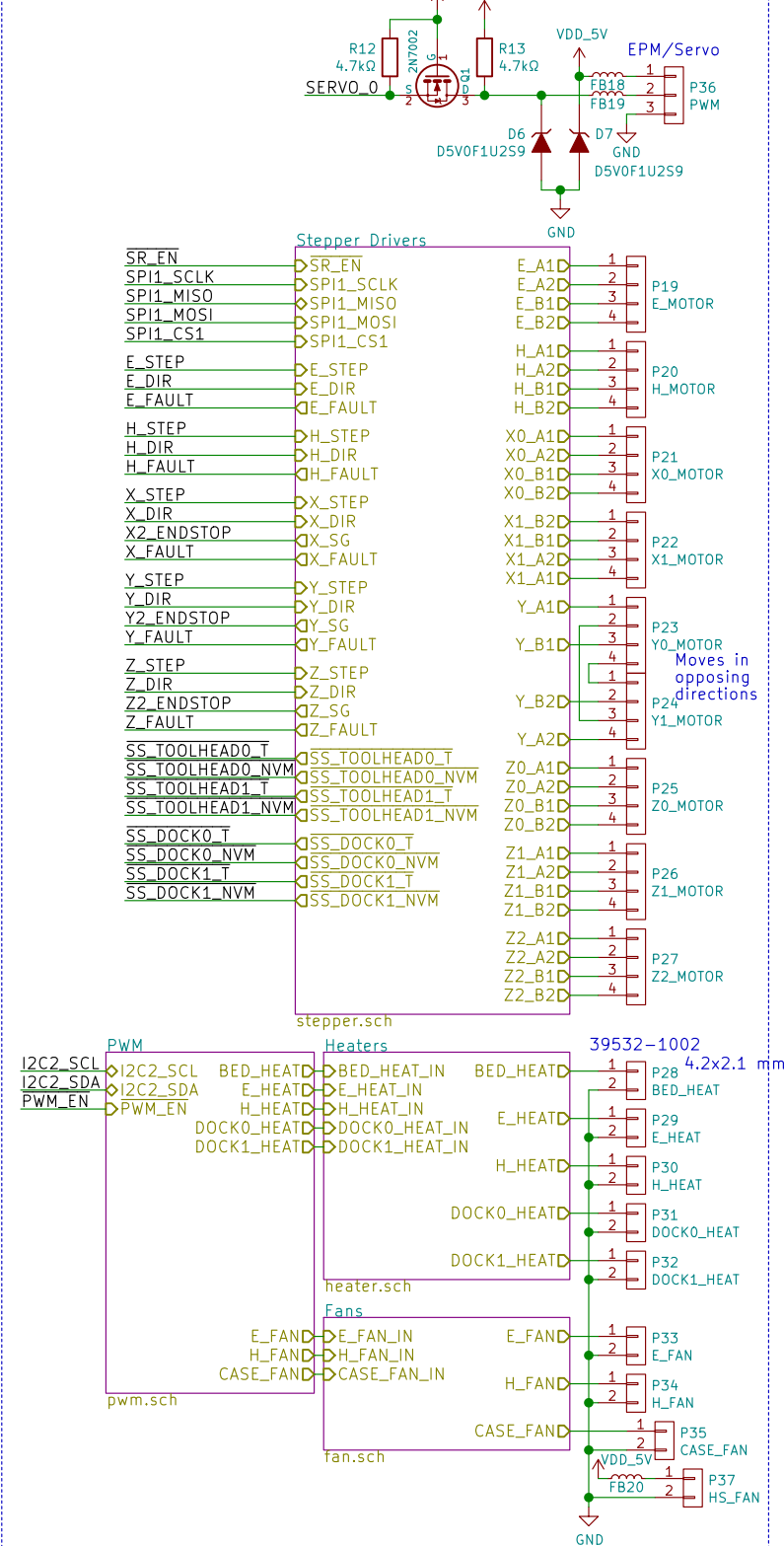
Thermistors



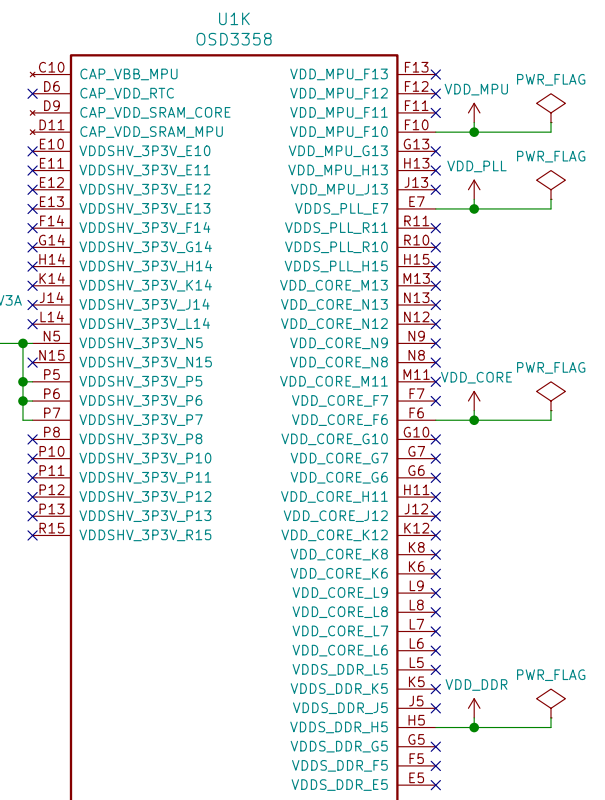
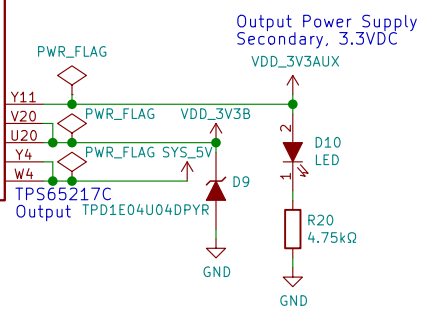
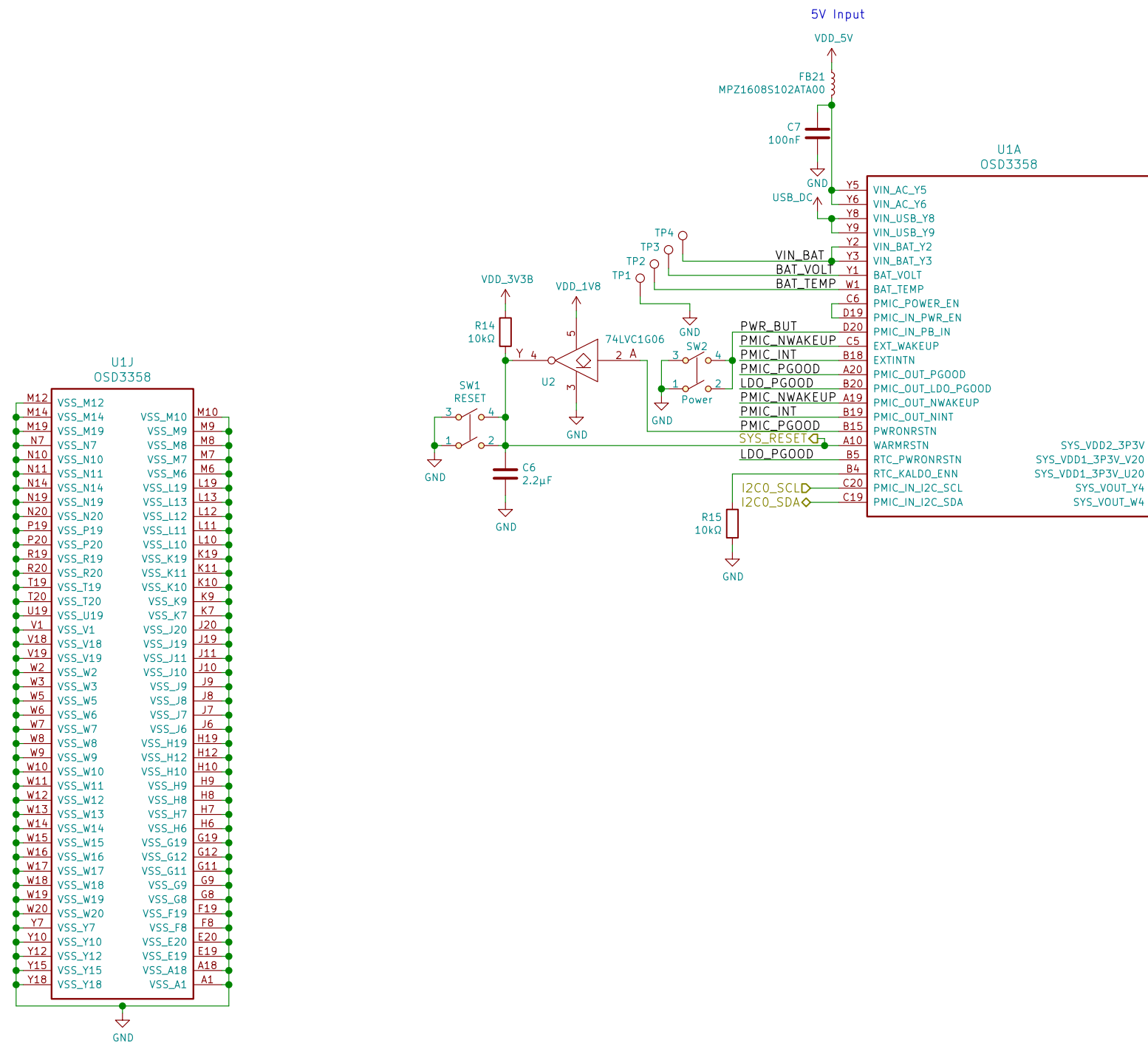
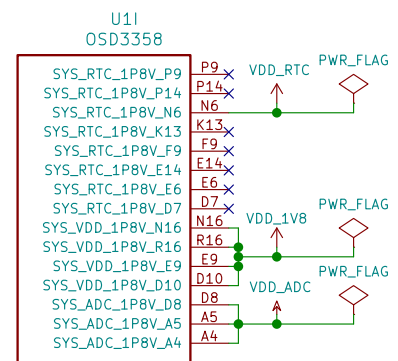
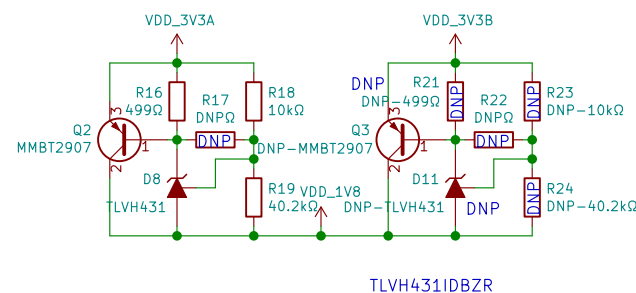
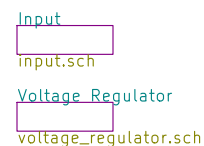
PWR\_FLAG  
USB\_DC



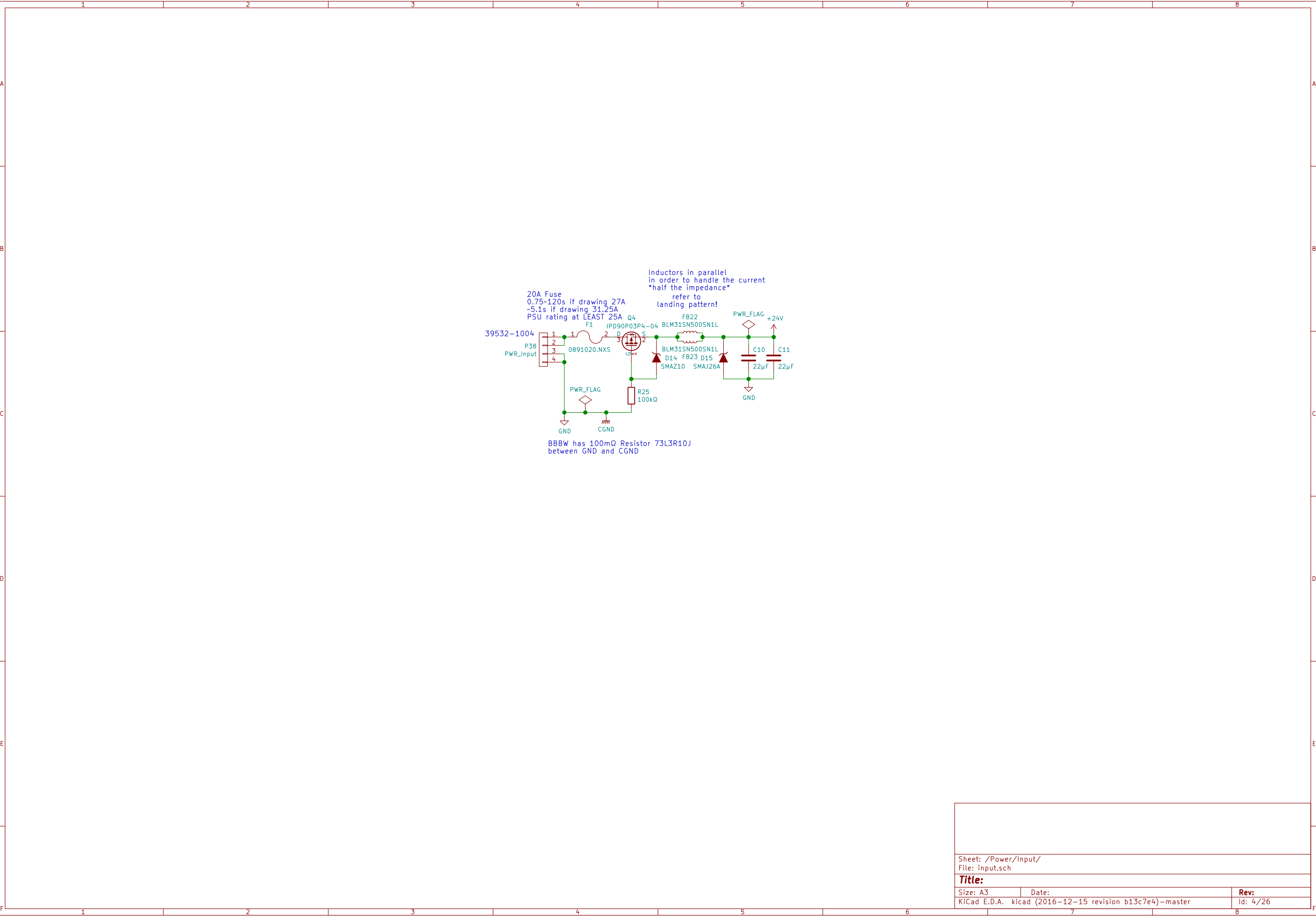
VDD\_3V3B  
VDD\_5V

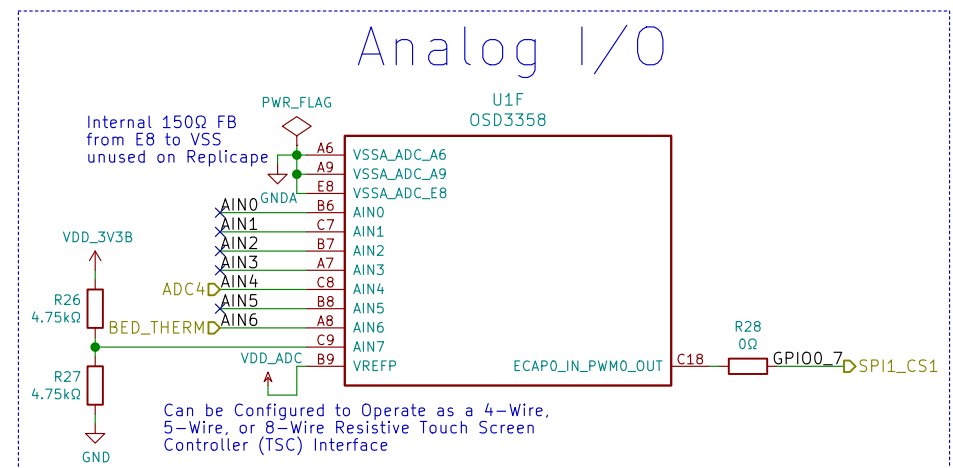
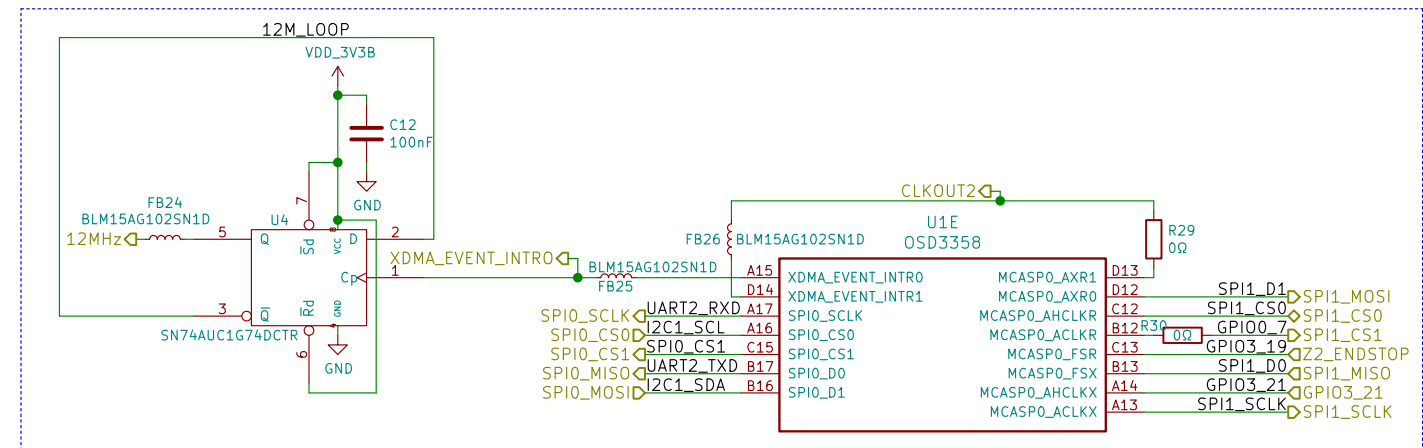
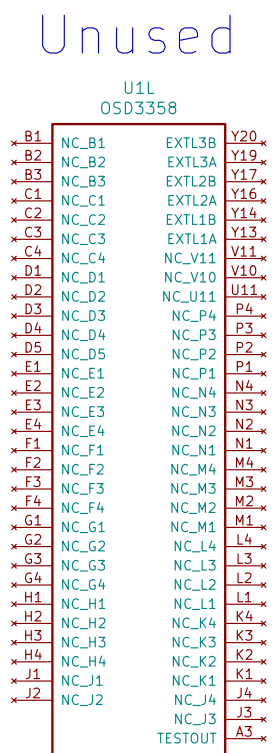


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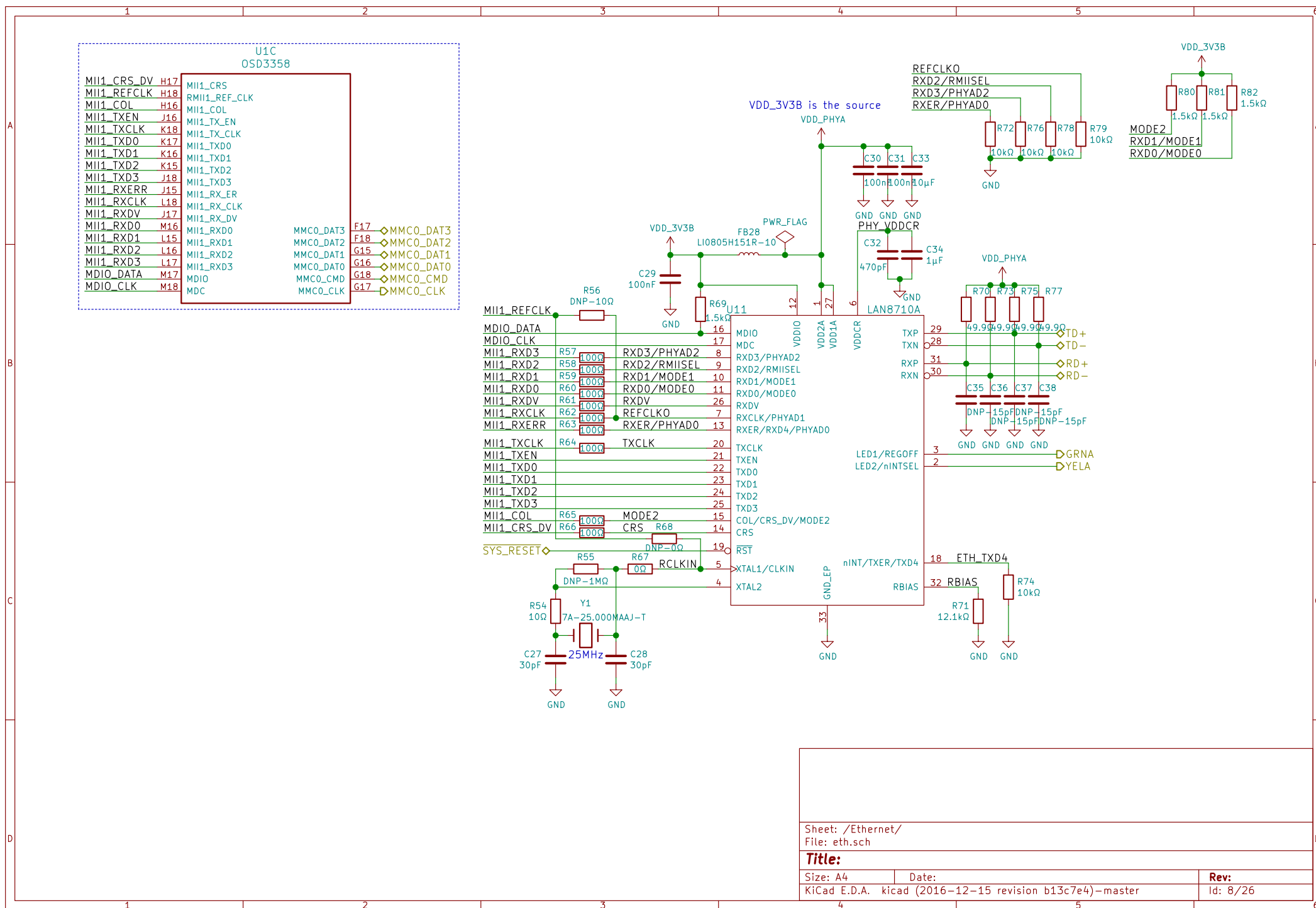




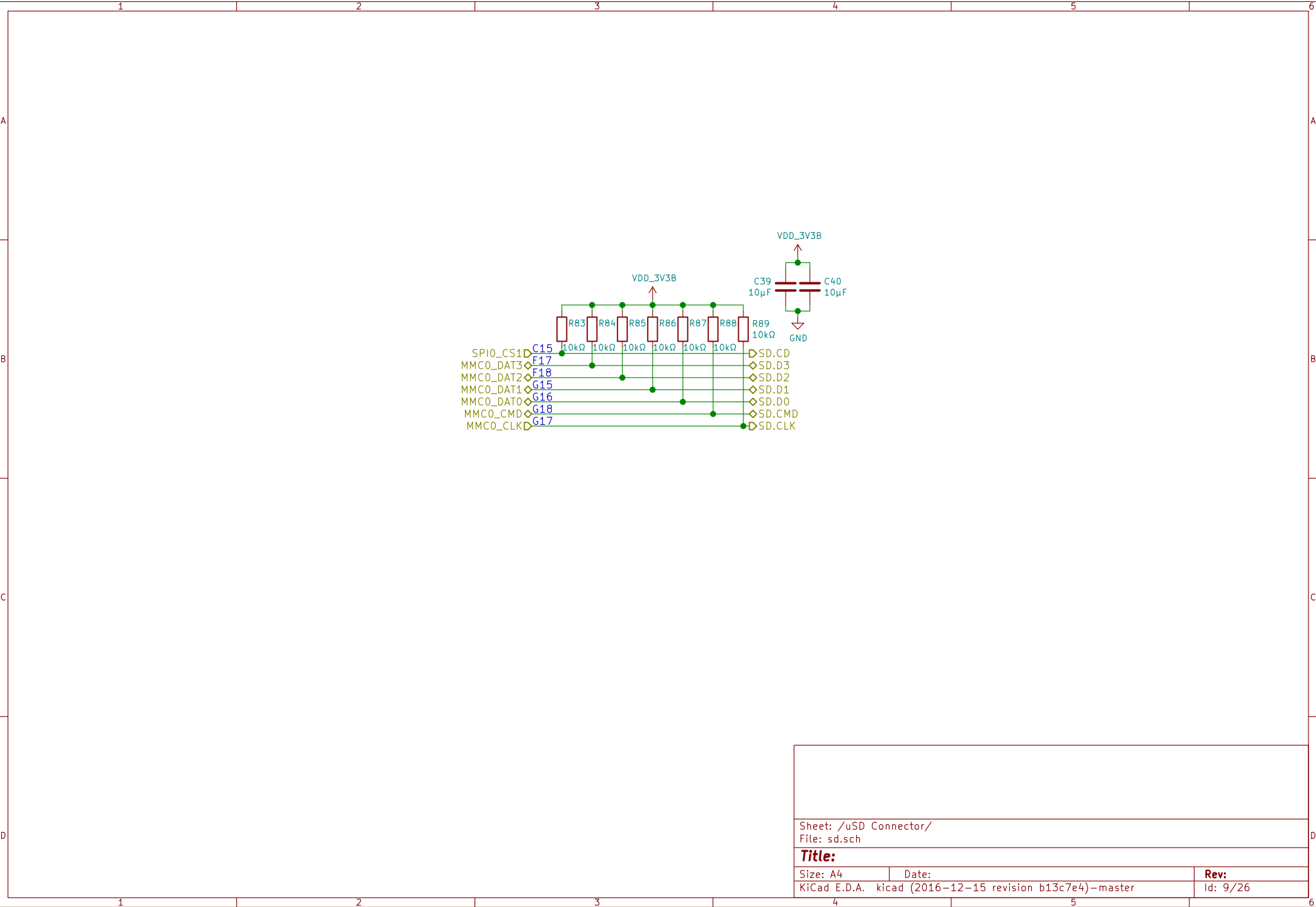




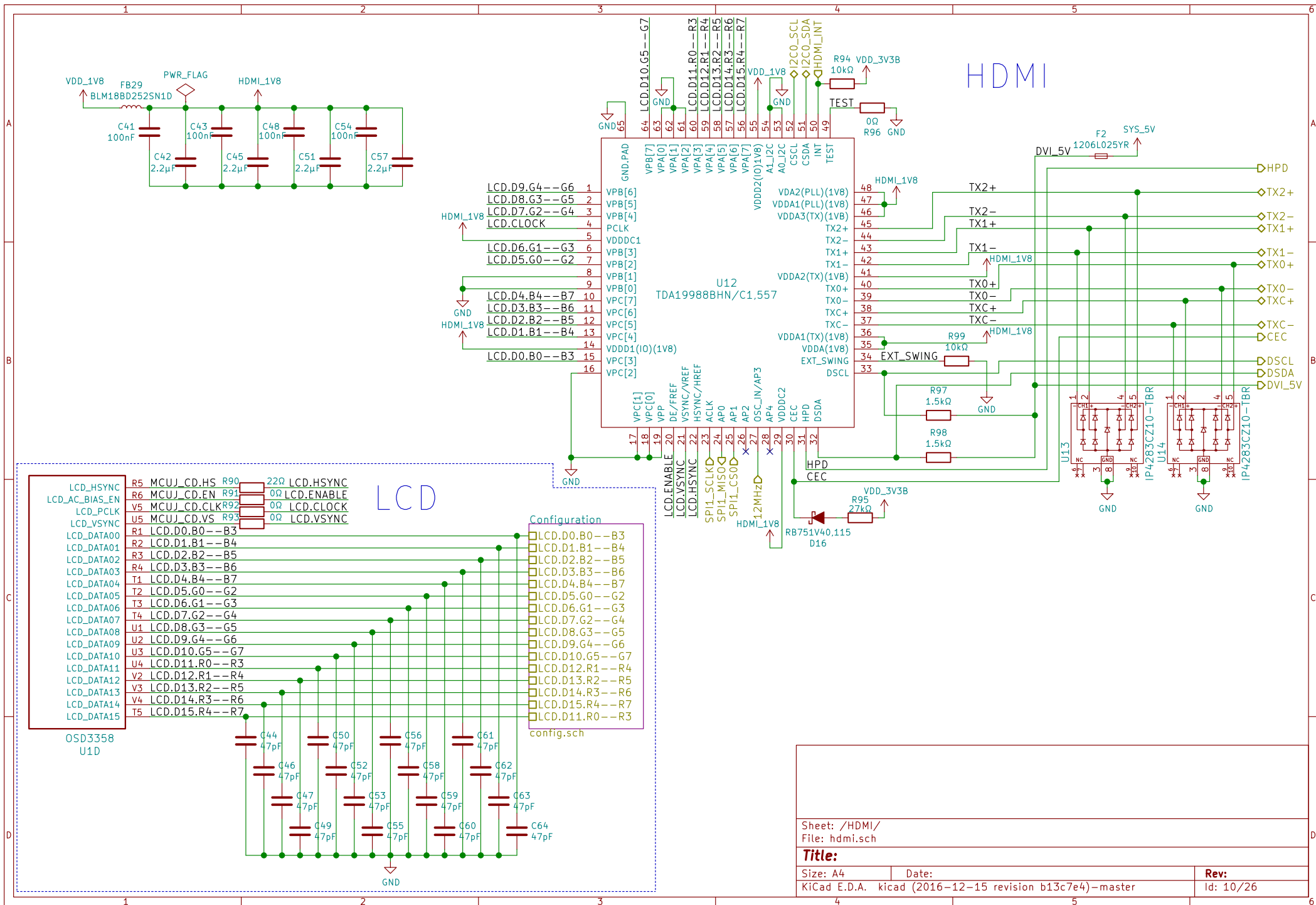


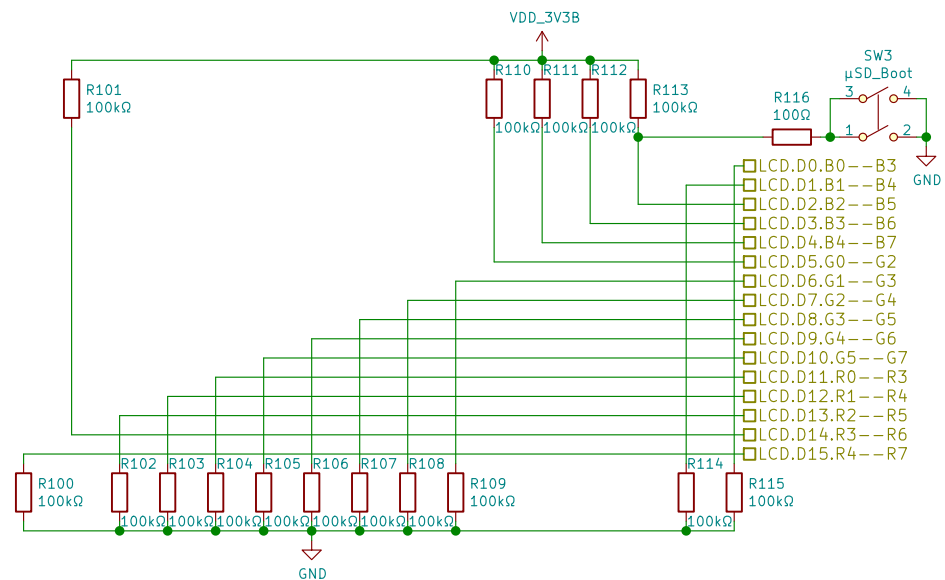






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Size: A4	Date:	Rev:
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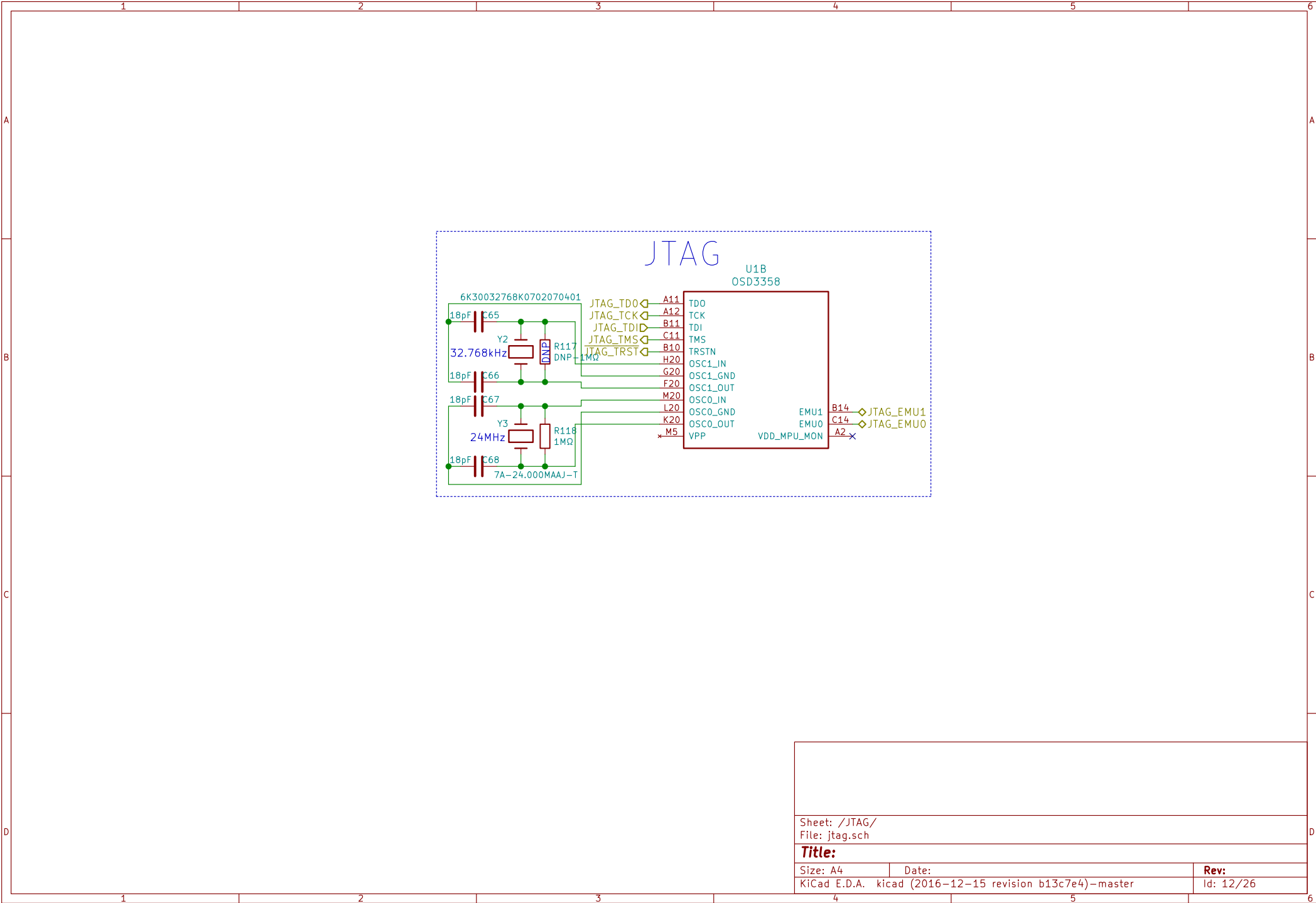


Sheet: /HDMI/Configuration/  
File: config.sch

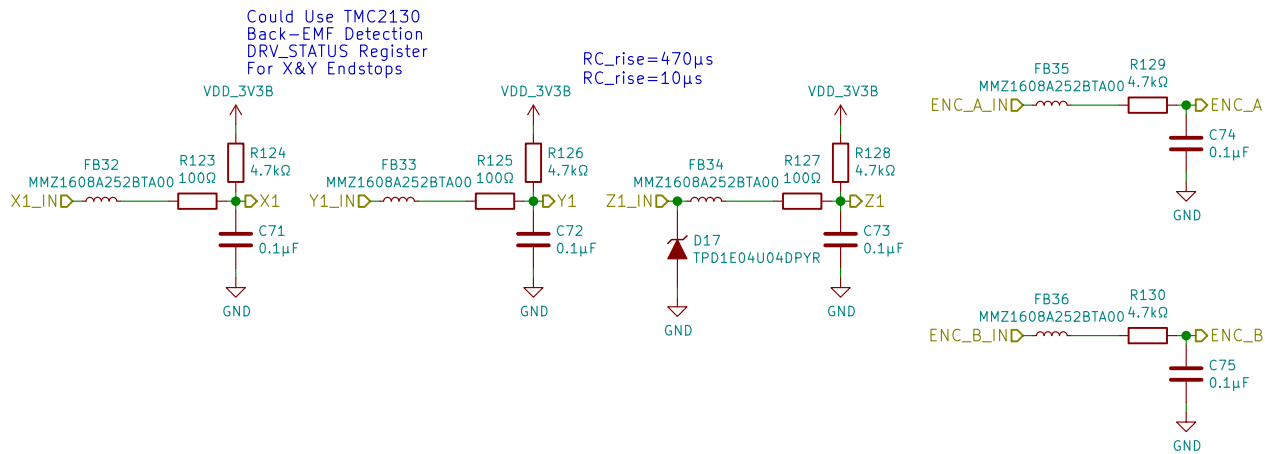
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Size: A4 Date: KICad E.D.A. kicad (2016-12-15 revision b13c7e4)-master

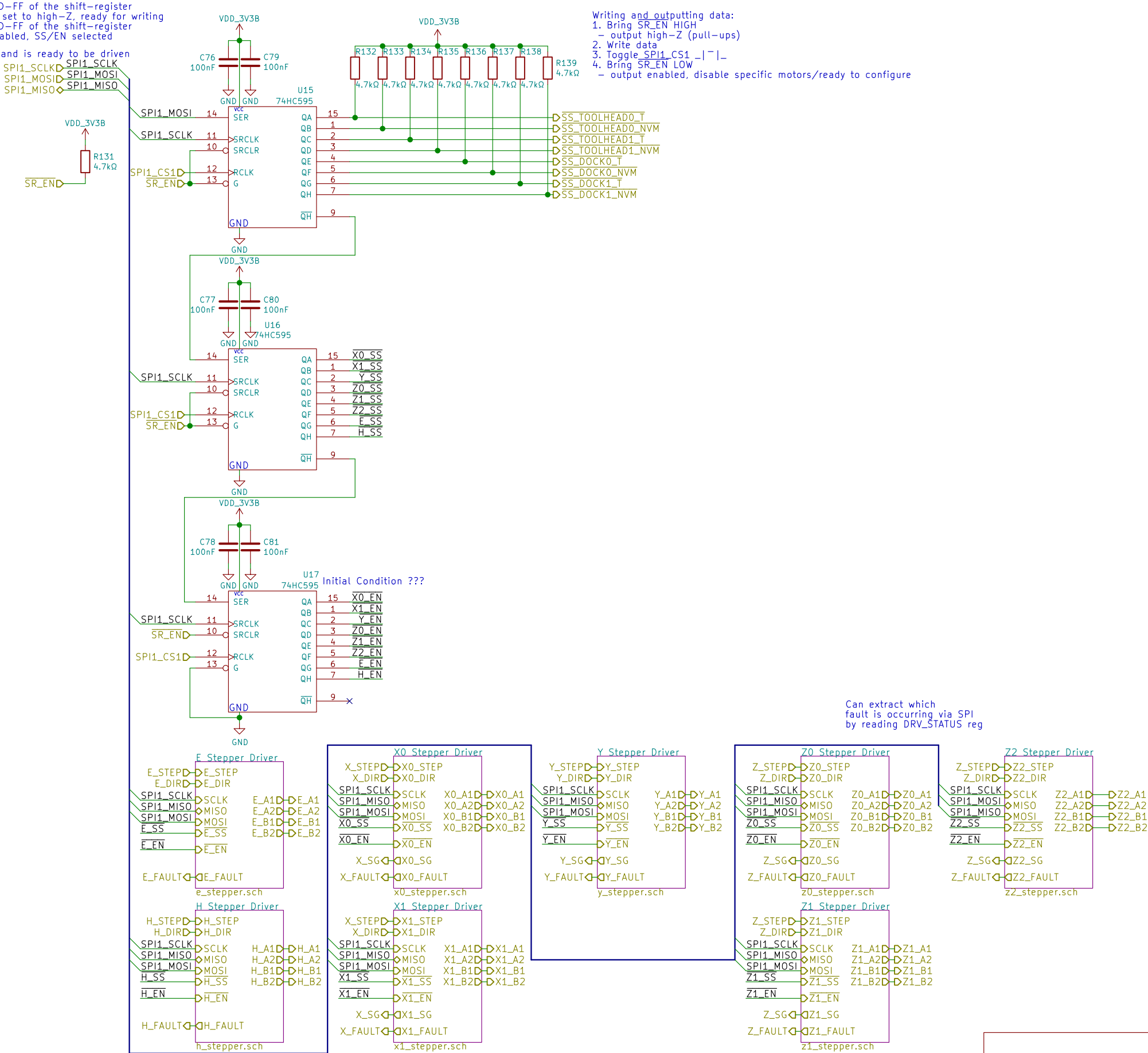
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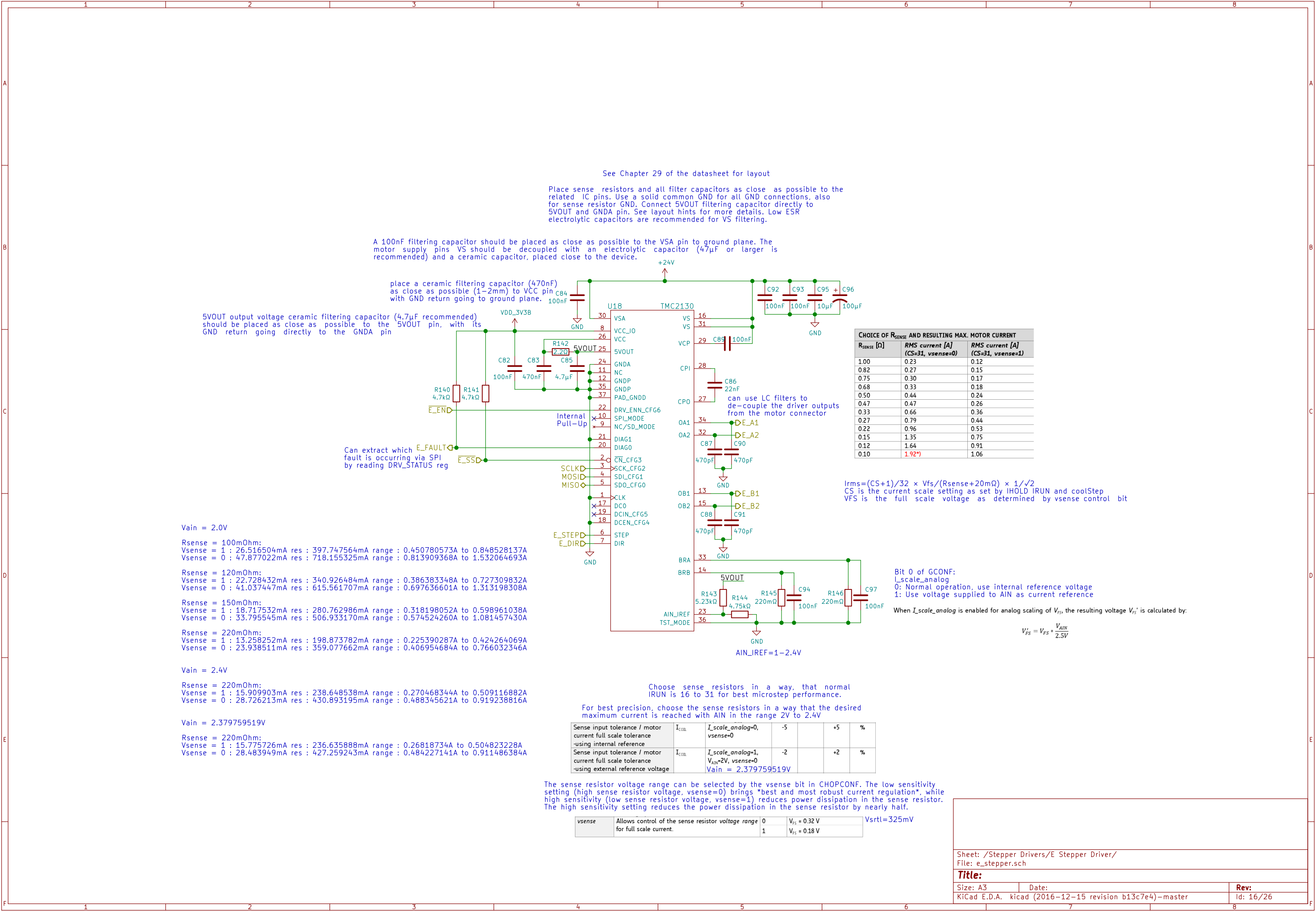




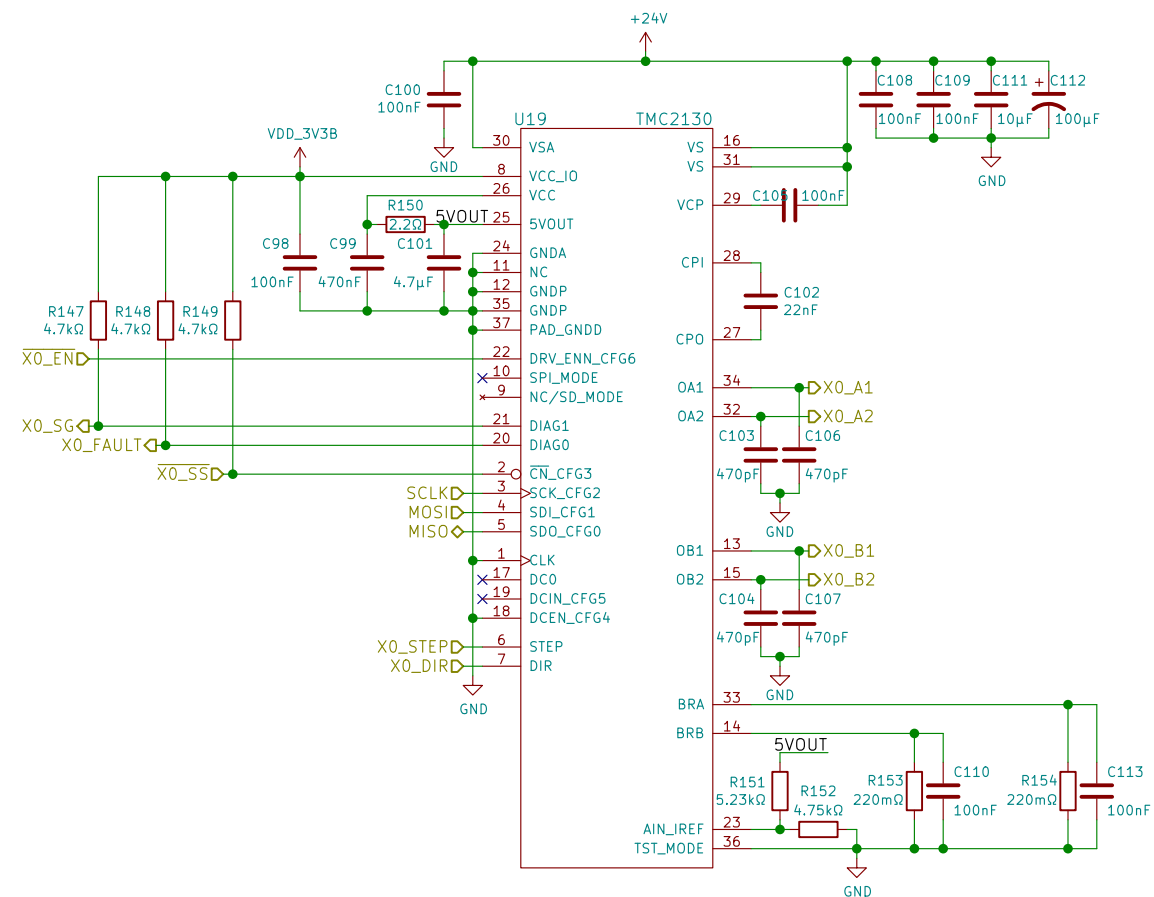


When SR\_EN transitions from LOW to HIGH each D-FF of the shift-register come out of their reset states and the output is set to high-Z, ready for writing  
When SR\_EN transitions from HIGH to LOW each D-FF of the shift-register goes into their reset states and the output is enabled, SS/EN selected  
SPI1\_CS1 captures the data in the shift register and is ready to be driven by the second stage of D-FFs









Sheet: /Stepper Drivers/X0 Stepper Driver/  
File: x0\_stepper.sch

**Title:**

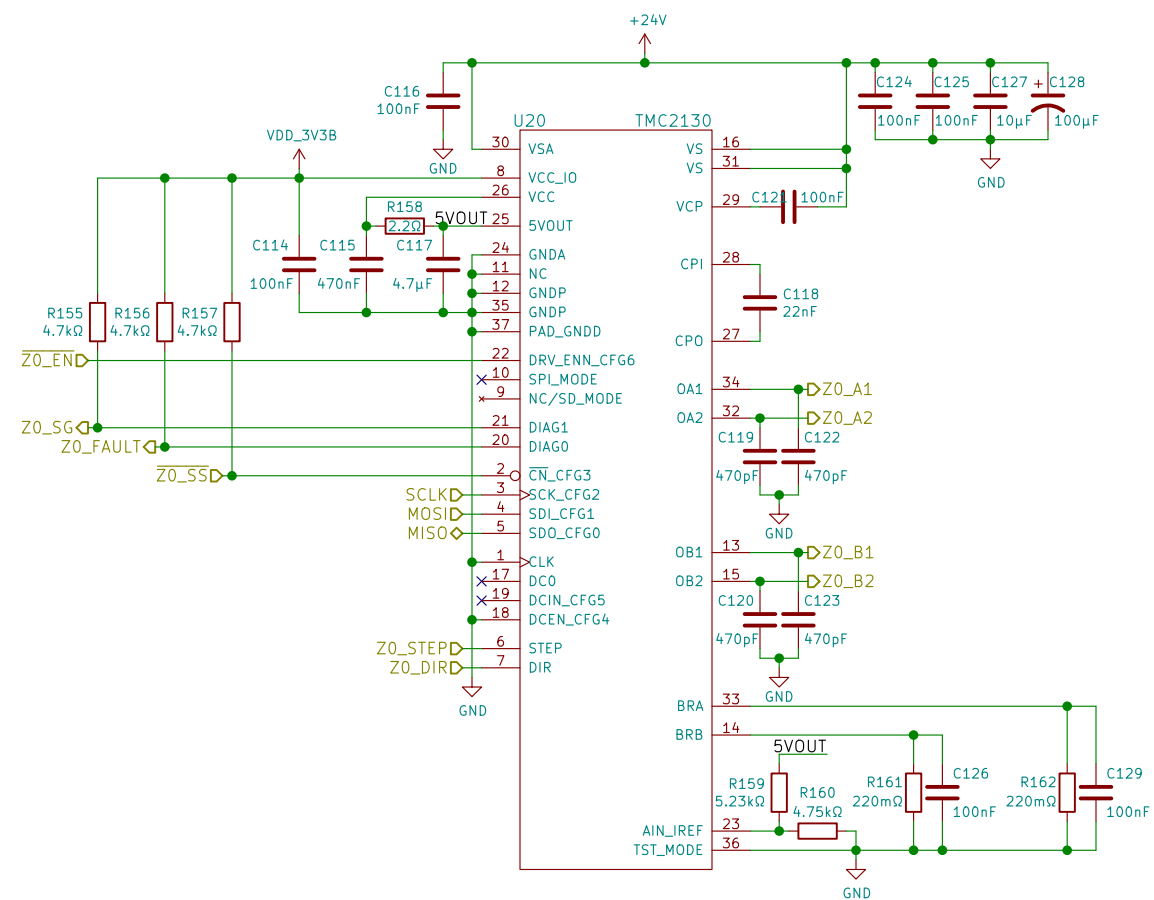
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Date:

Size: A5	Date:
KiCad E.D.A. kicad (2016-12-15 revision b13c7e4)-master	

Rev:

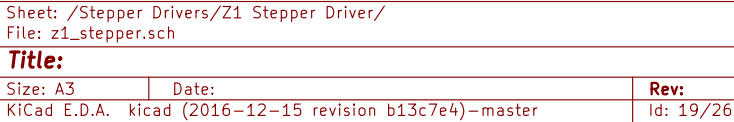
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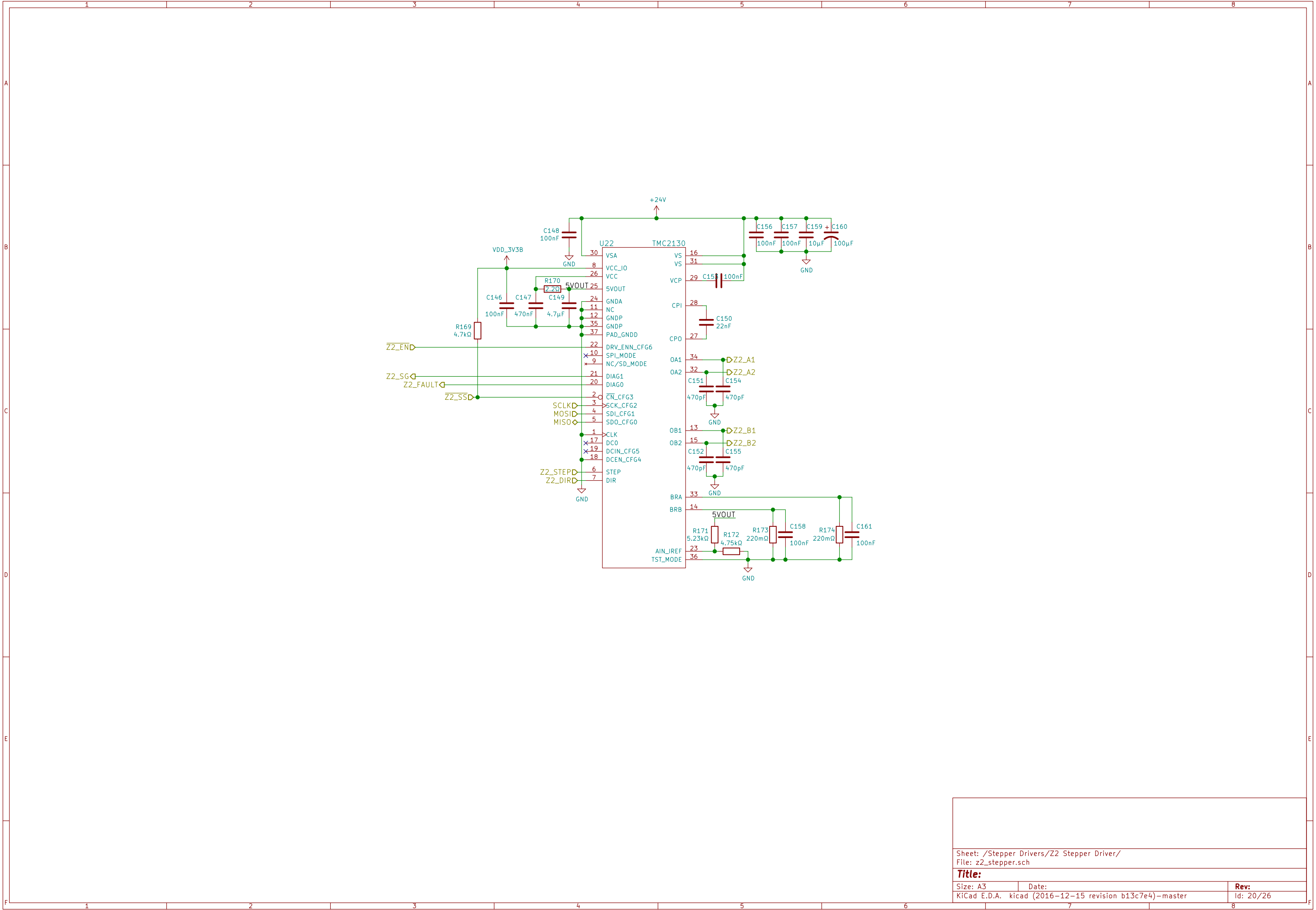


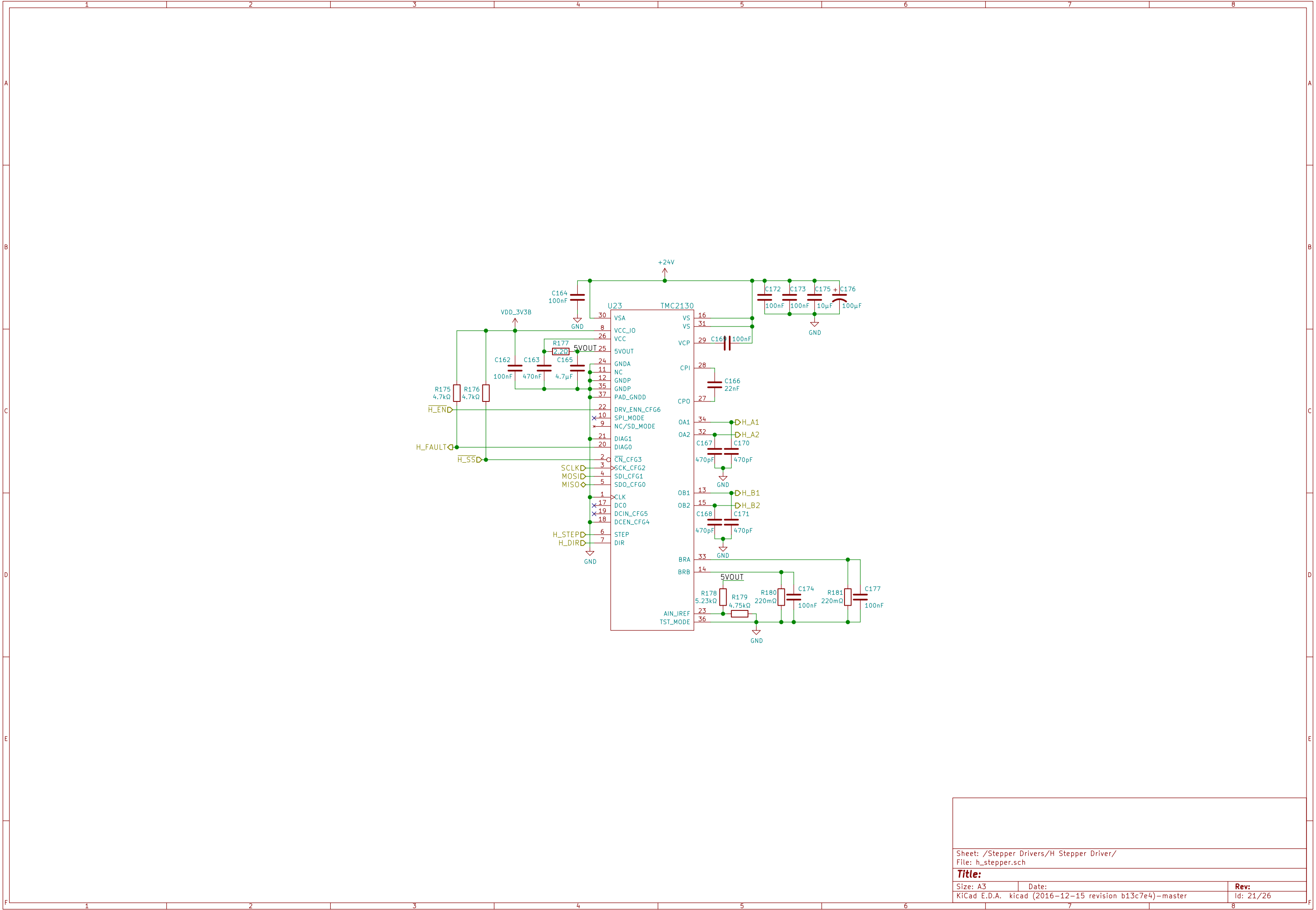
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File: z0\_stepper.sch

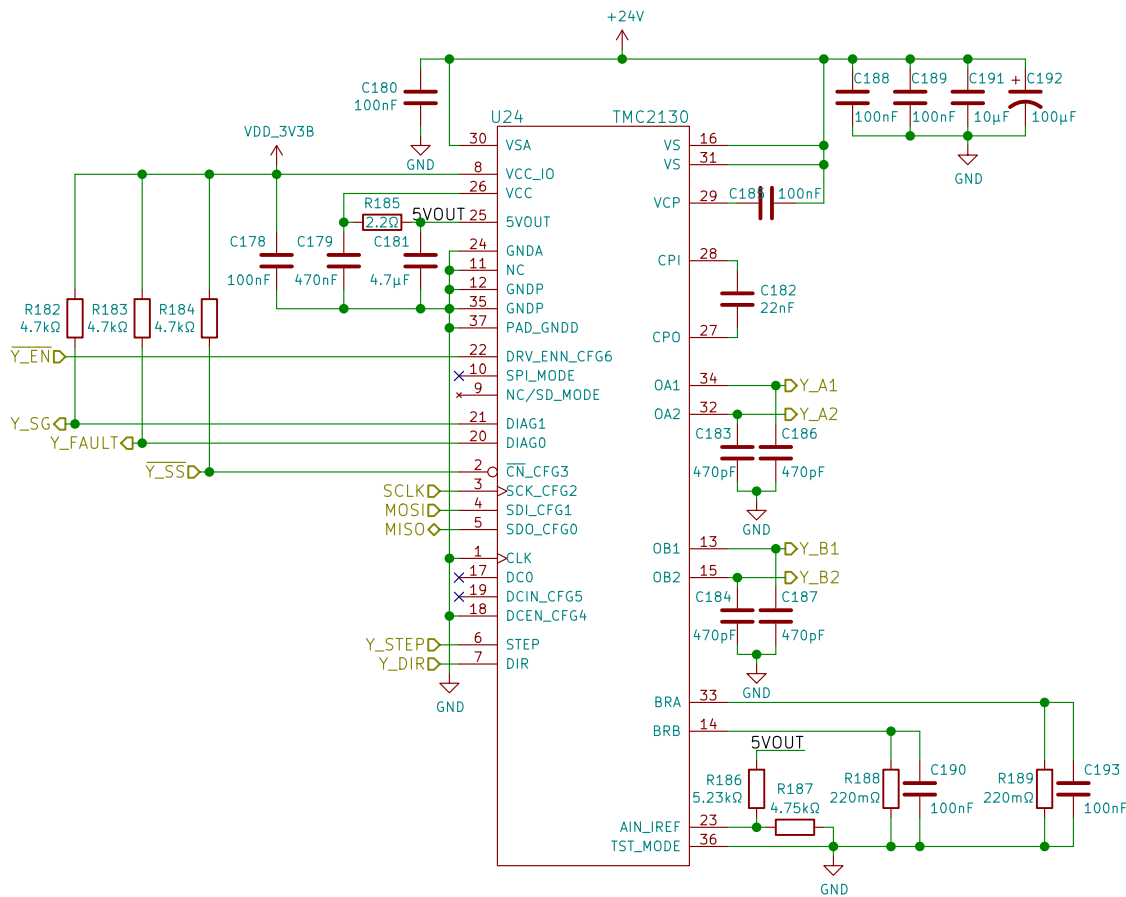
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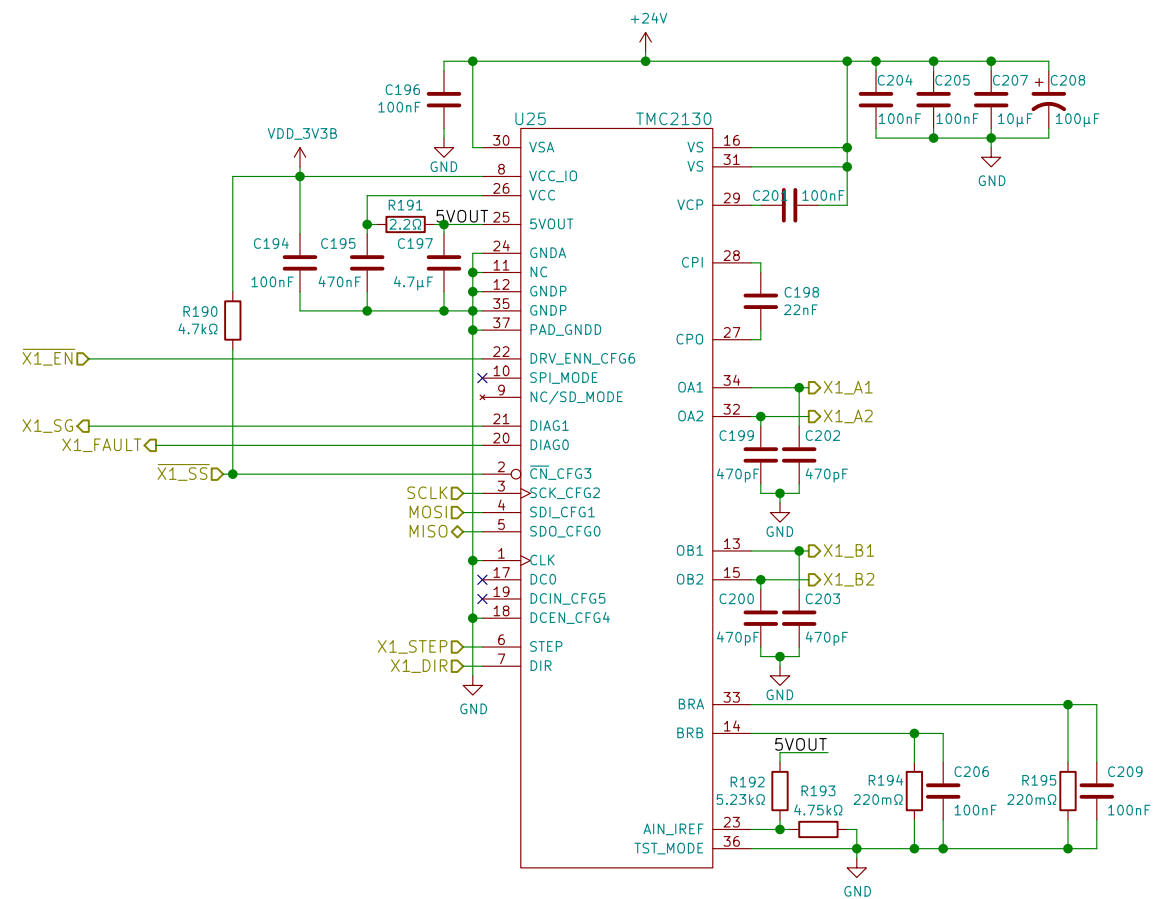




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**Title:**

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Sheet: /Stepper Drivers/X1 Stepper Driver/  
File: x1\_stepper.sch

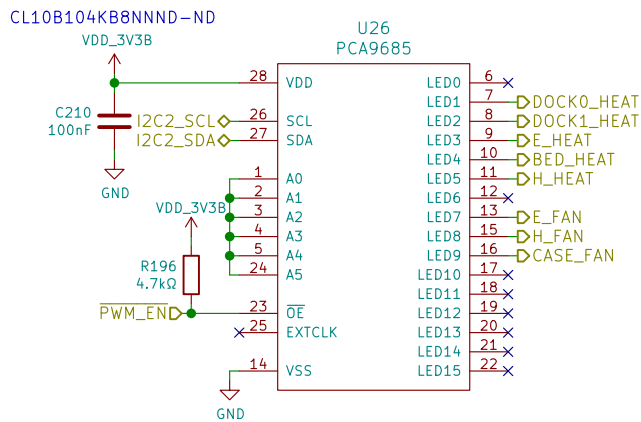
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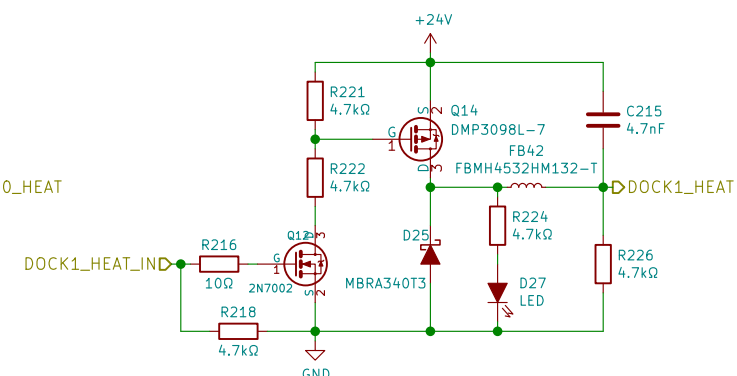
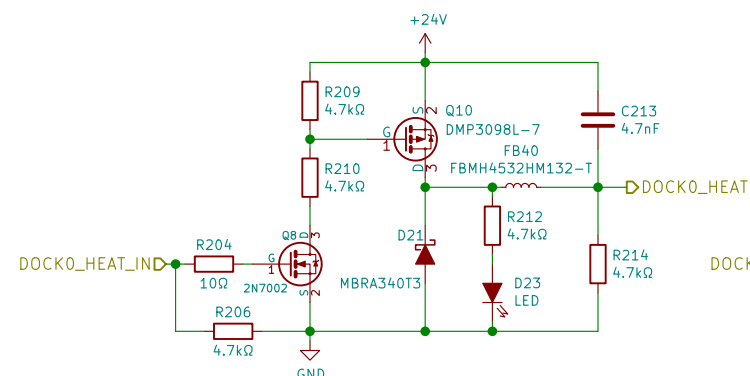
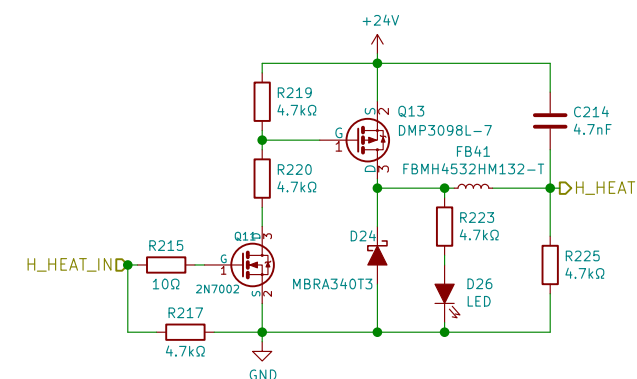
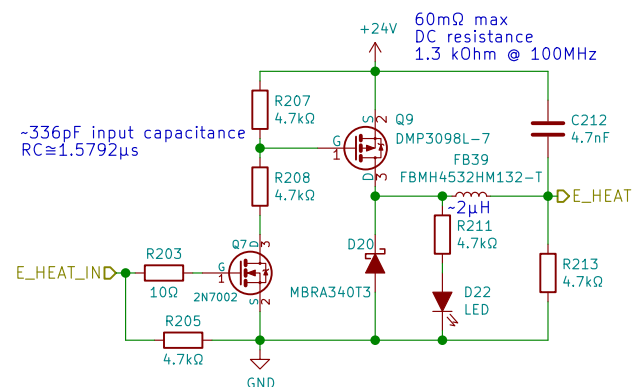
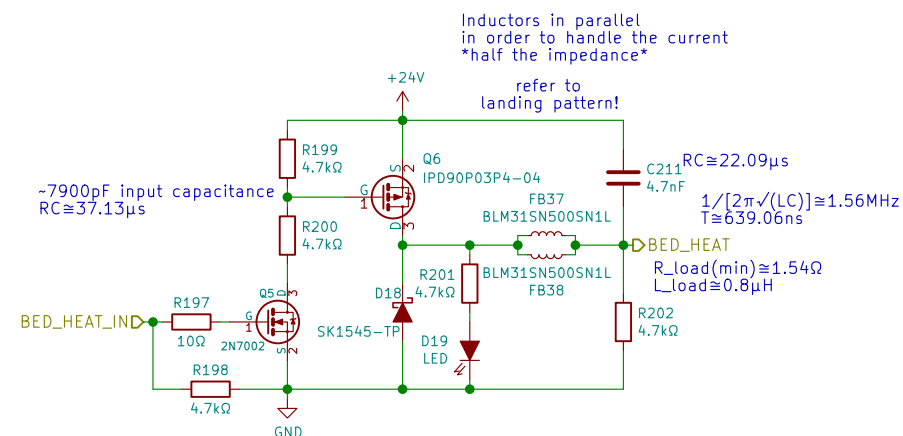
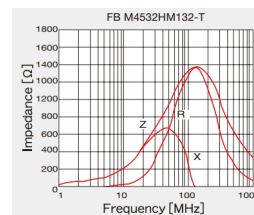
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Sheet: /PWM/ File: pwm.sch		
<b>Title:</b>		
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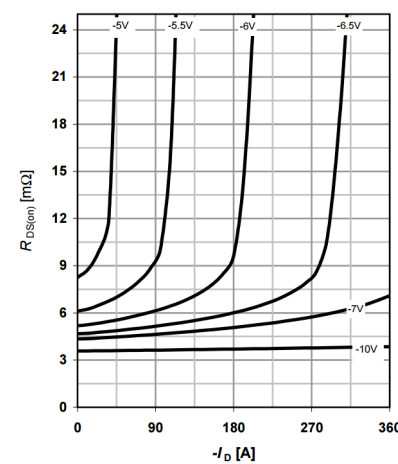




**6 Typ. drain-source on-state resistance**

$$R_{DS(on)} = (I_D); T_j = 25\text{ }^{\circ}\text{C}$$

parameter:  $V_{GS}$



**8 Typ. drain-source on-state resistance**

$$R_{DS(on)} = f(T_j); I_D = -90 \text{ A}; V_{GS} = -10 \text{ V}$$

