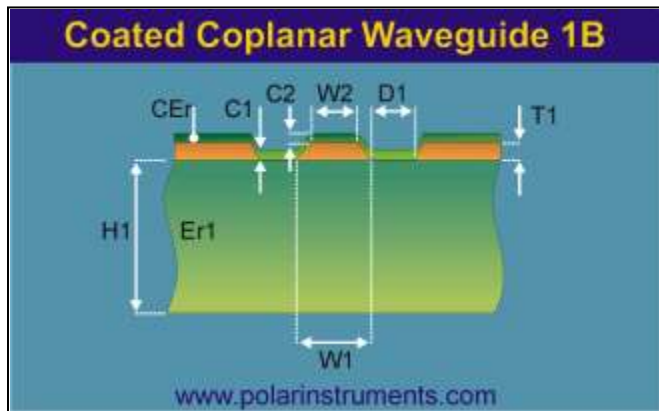


Polar Si9000 PCB Transmission Line Field Solver



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	29.8100	+/-	0.0000	29.8100	29.8100
Substrate 1 Dielectric	Er1	3.8800	+/-	0.0000	3.8800	3.8800
Lower Trace Width	W1	47.2440	+/-	0.0000	47.2440	47.2440
Upper Trace Width	W2	47.2440	+/-	0.0000	47.2440	47.2440
Ground Strip Separation	D1	7.0000	+/-	0.0000	7.0000	7.0000
Trace Thickness	T1	1.7000	+/-	0.0000	1.7000	1.7000
Coating Above Substrate	C1	1.6000	+/-	0.0000	1.6000	1.6000
Coating Above Trace	C2	0.4000	+/-	0.0000	0.4000	0.4000
Coating Dielectric	CEr	3.3000	+/-	0.0000	3.3000	3.3000

Impedance	Zo	49.52	-----	49.52	49.52
Delay (ps/in)	D	130.847	-----	130.847	130.847
Inductance (nH/in)	L	6.479	-----	6.479	6.479
Capacitance (pF/in)	C	2.642	-----	2.642	2.642

Notes

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