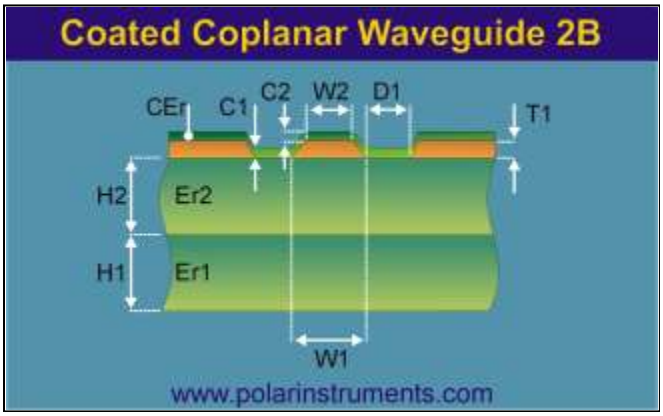
Polar Si9000 PCB Transmission Line Field Solver



				Tolerance	Minimum	Maximum
Substrate 1 Height	H1	3.9370	+/-	0.0000	3.9370	3.9370
Substrate 1 Dielectric	Er1	4.2500	+/-	0.0000	4.2500	4.2500
Substrate 2 Height	H2	3.1650	+/-	0.0000	3.1650	3.1650
Substrate 2 Dielectric	Er2	3.8000	+/-	0.0000	3.8000	3.8000
Lower Trace Width	W1	59.0550	+/-	0.0000	59.0550	59.0550
Upper Trace Width	W2	59.0550	+/-	0.0000	59.0550	59.0550
Ground Strip Separation	D1	6.0000	+/-	0.0000	6.0000	6.0000
Trace Thickness	T1	1.4000	+/-	0.0000	1.4000	1.4000
Coating Above Substrate	C1	1.0000	+/-	0.0000	1.0000	1.0000
Coating Above Trace	C2	0.4000	+/-	0.0000	0.4000	0.4000
Coating Dielectric	CEr	4.2000	+/-	0.0000	4.2000	4.2000

Impedance	Zo	50.84	------	50.84	50.84
Delay (ps/in)	D	118.246	------	118.246	118.246
Inductance (nH/in)	L	6.011	------	6.011	6.011
Capacitance (pF/in)	C	2.326	------	2.326	2.326

Notes

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