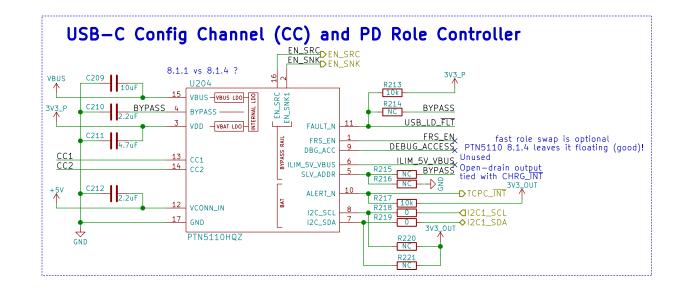
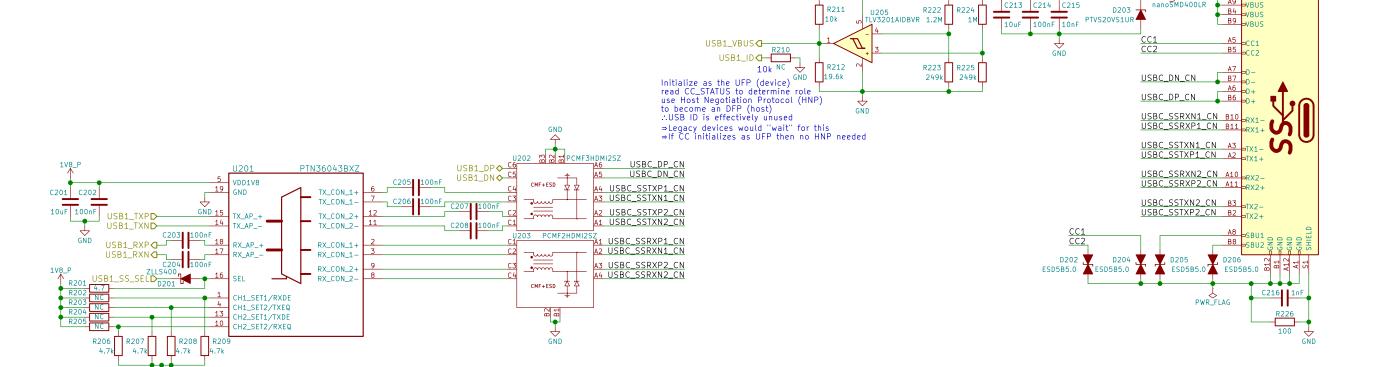


"Under dead battery operation, PTN5110 applies voltage clamps to both CC pins so that the system may receive power as a Sink. To support platforms with buck—boost configuration, PTN5110 asserts EN_SNK1 pin based on validity of VBUS voltage (facilitates 5 V VBUS sinking)."



 $USB1_VBUS=+5V$ when VBUS>4.31V

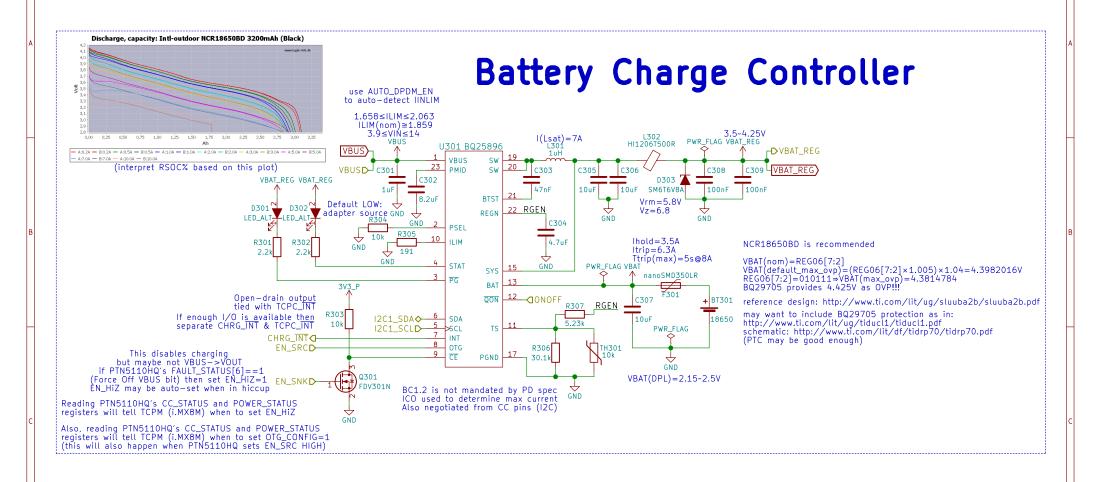


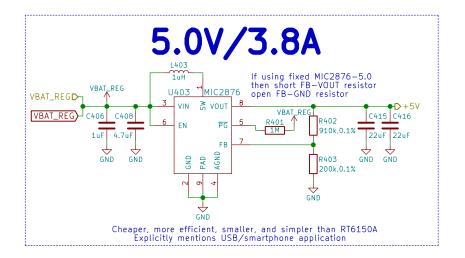
	GNU GPLv3			
	Copyright 2018			
	Purism SPC			
Sheet: /USB-C/				
	File: usb-c.sch			
	Title: USB Ty	rpe C		
	Size: A3	Date: 2018-05-02		Rev: v0.1.0
	KiCad E.D.A. kic	ad 4.0.7		ld: 2/17
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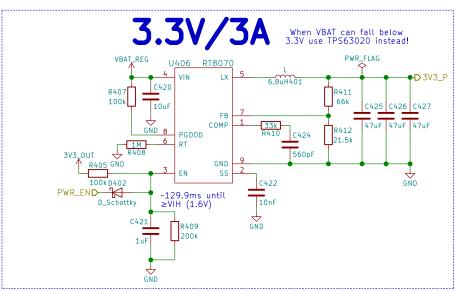
VBUS ◆ **D VBUS**

Ihold=4A Itrip=8A PWR_FLAG F201_ →

J201 USB_C_Receptacle







AMOLED POWER If using Li—ion then VBAT ok U402 TPS65632RTER VBAT_REG 12 SWP1 PVIN 16 AVIN OUTP1 C401 C404 C405 C407 FBS 100nF 10uF 10uF 10uF -4.0V L401 GND 15 GND SWP2 AVDD_END-C412 FN 9 SWIRED 13 OUTP2 C402 L404 4.7uH SELP2 AGND SWN 10uF PGND1 14 PGND2 C410 GND GND VCI_TP_VCC → AGND U401 MIC5501 VOUT C403 C409 10uF GND GND

