



				<u>Tolerance</u>	<u>Minimum</u>	<u>Maximum</u>
Substrate 1 Height	H1	3.9370	+/-	0.0000	3.9370	3.9370
Substrate 1 Dielectric	Er1	4.2500	+/-	0.0000	4.2500	4.2500
Substrate 2 Height	H2	3.1650	+/-	0.0000	3.1650	3.1650
Substrate 2 Dielectric	Er2	3.8000	+/-	0.0000	3.8000	3.8000
Lower Trace Width	W1	47.2440	+/-	0.0000	47.2440	47.2440
Upper Trace Width	W2	47.2440	+/-	0.0000	47.2440	47.2440
Ground Strip Separation	D1	5.3000	+/-	0.0000	5.3000	5.3000
Trace Thickness	T1	1.4000	+/-	0.0000	1.4000	1.4000
Coating Above Substrate	C1	1.0000	+/-	0.0000	1.0000	1.0000
Coating Above Trace	C2	0.4000	+/-	0.0000	0.4000	0.4000
Coating Dielectric	CEr	4.2000	+/-	0.0000	4.2000	4.2000

Impedance	Zo	50.20	-----	50.20	50.20
Delay (ps/in)	D	120.629	-----	120.629	120.629
Inductance (nH/in)	L	6.055	-----	6.055	6.055
Capacitance (pF/in)	C	2.403	-----	2.403	2.403

Notes

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