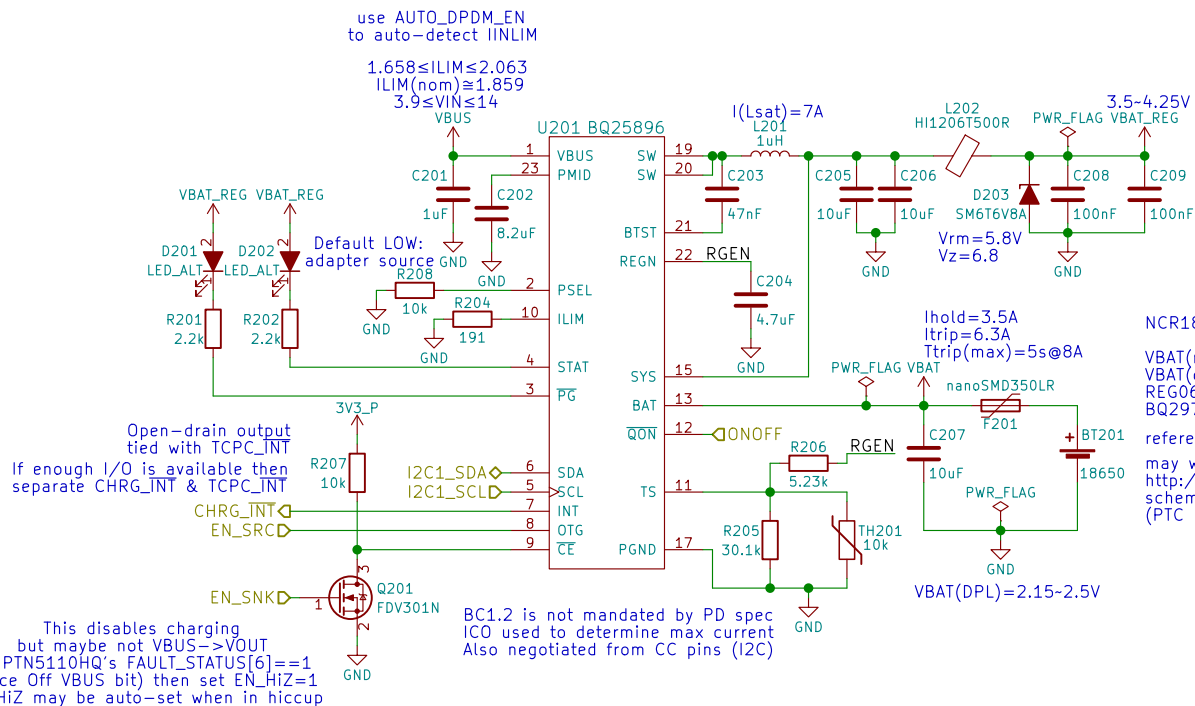


(interpret RSOC% based on this plot)



Reading PTN5110HQ's CC_STATUS and POWER_STATUS registers will tell TCPM (i.MX8M) when to set EN_HiZ

Also, reading PTN5110HQ's CC_STATUS and POWER_STATUS registers will tell TCPM (i.MX8M) when to set OTG_CONFIG=1 (this will also happen when PTN5110HQ sets EN_SRC HIGH)

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Purism SPC

Sheet: /Battery/
 File: battery.sch

Title: Battery

Size: A4 Date: 2018-05-02

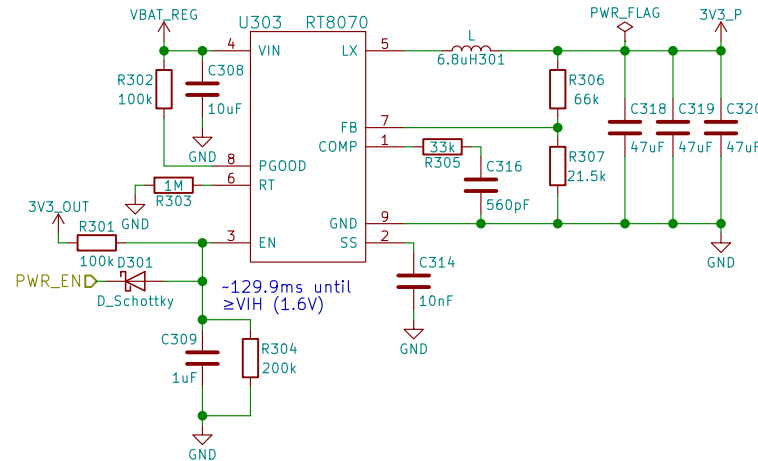
KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

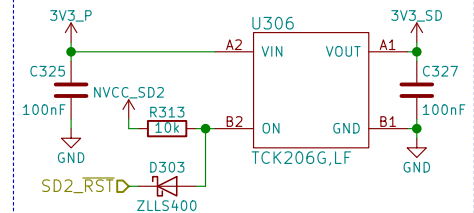
Id: 2/16

3.3V/3A

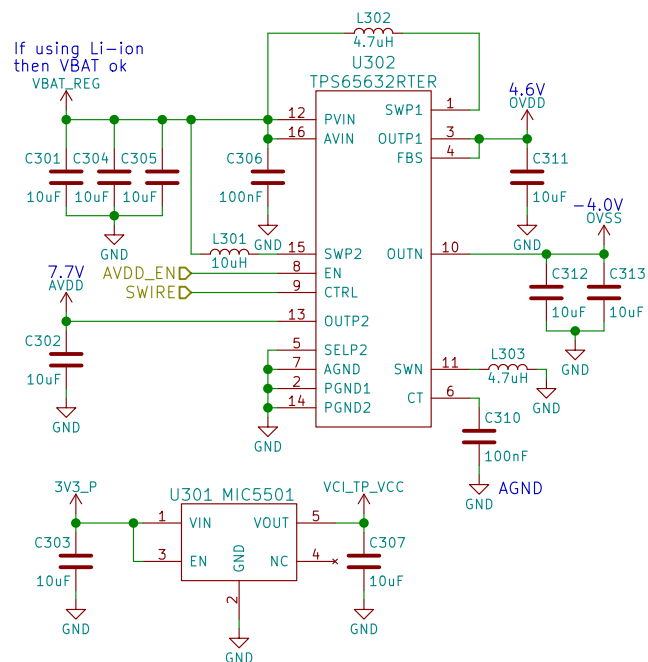
When VBAT can fall below 3.3V use TPS63020 instead!



SD POWER

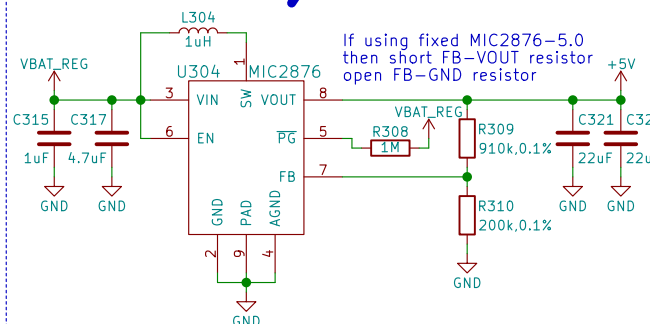


AMOLED POWER



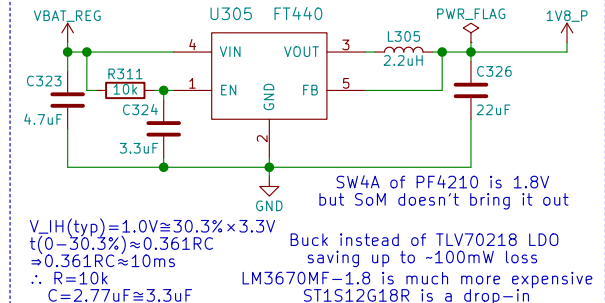
5.0V/3.8A

If using fixed MIC2876-5.0 then short FB-VOUT resistor open FB-GND resistor



Cheaper, more efficient, smaller, and simpler than RT6150A
Explicitly mentions USB/smartphone application

1.8V/600mA



SW4A of PF4210 is 1.8V but SoM doesn't bring it out
Buck instead of TLV70218 LDO saving up to ~100mW loss
LM3670MF-1.8 is much more expensive
ST1S12G18R is a drop-in

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Purism SPC

Sheet: /Power/
File: power.sch

Title: Power

Size: A4 Date: 2018-05-02

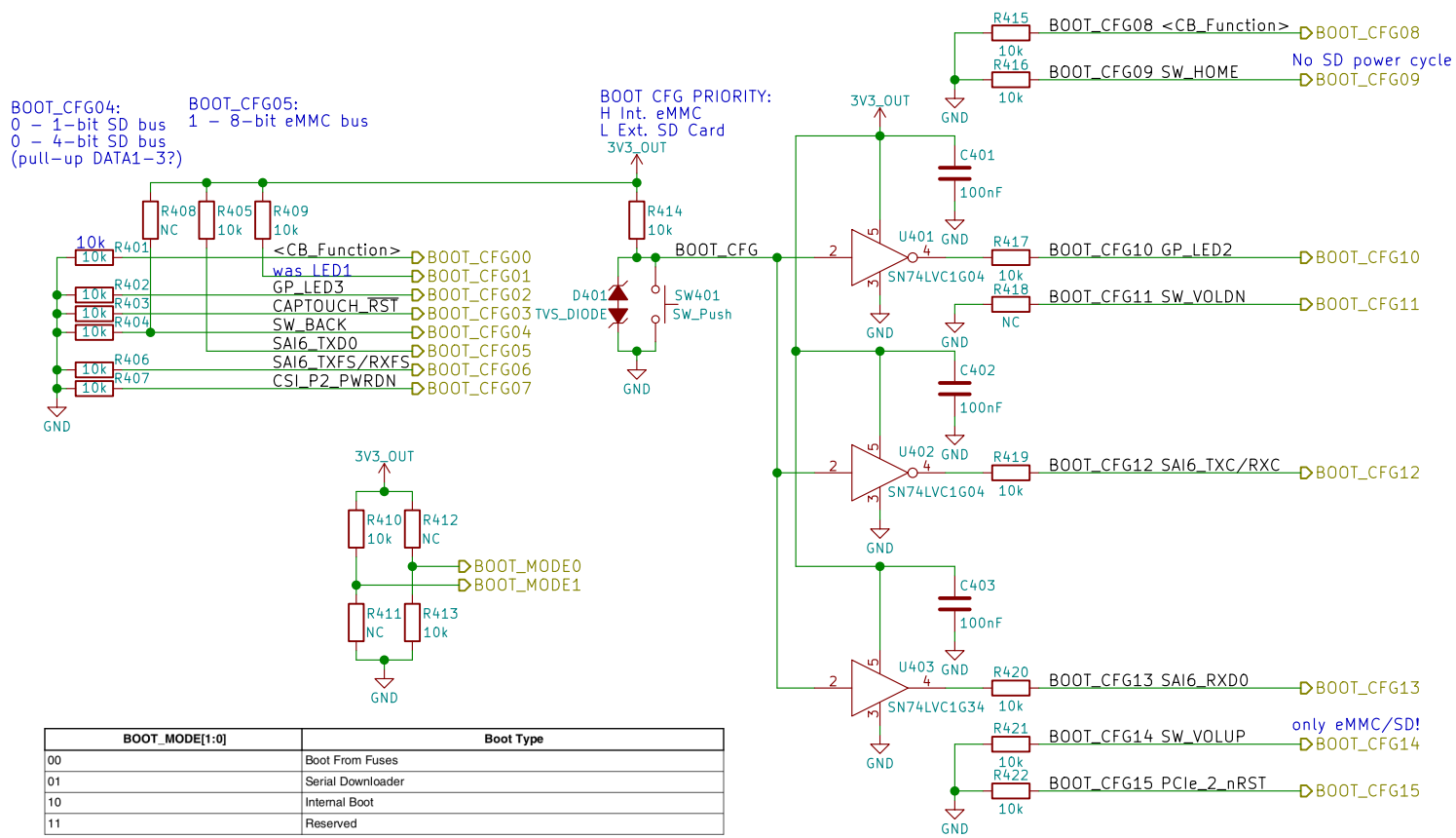
KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 3/16

BOOT_CFG04:
0 - 1-bit SD bus
0 - 4-bit SD bus
(pull-up DATA1-3?)

BOOT_CFG05:
1 - 8-bit eMMC bus



BOOT CFG PRIORITY:
H Int. eMMC
L Ext. SD Card

BOOT_MODE[1:0]	Boot Type
00	Boot From Fuses
01	Serial Downloader
10	Internal Boot
11	Reserved

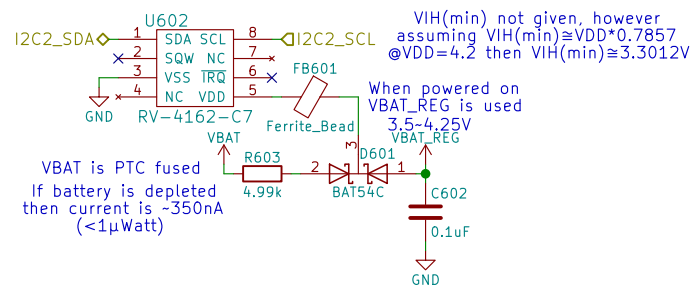
BOOT_CFG[14:12]		Boot device			
001		SD/eSD			
010		MMC/eMMC			
011		NAND			
Fuse	Config	Definition	GPIO ¹	Shipped value	Settings
BOOT_CFG[11:10]	OEM	USDHC port selection	Yes	00	00 - USDHC-1 01 - USDHC-2 10 - USDHC-3 else - reserved

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Purism SPC

Sheet: /Boot Config/
File: boot.sch

Title: Boot Configuration

Size: A4	Date: 2018-05-02	Rev: v0.1.0
KiCad E.D.A. kicad 4.0.7		Id: 4/16



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Sheet: /RTC Battery/
File: rtc.sch

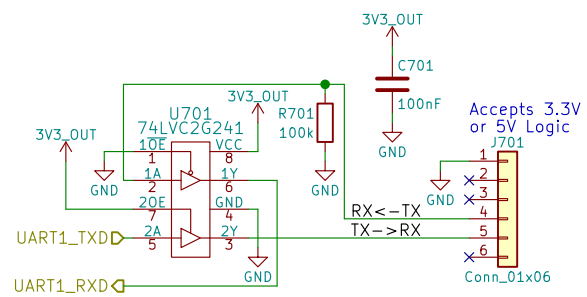
Title: RTC Battery

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 5/16



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Purism SPC

Sheet: /UART Debug/
File: uart.sch

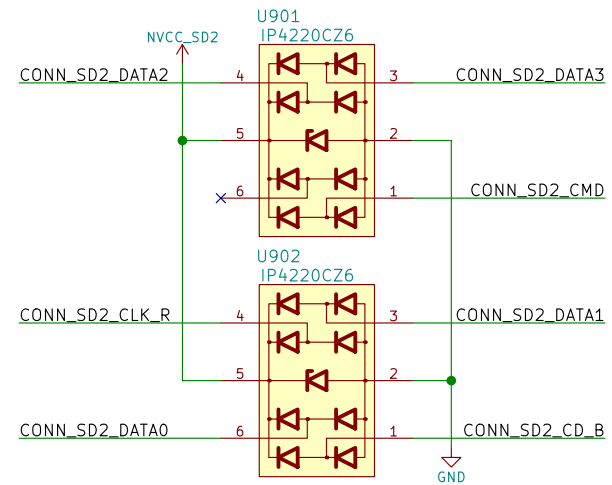
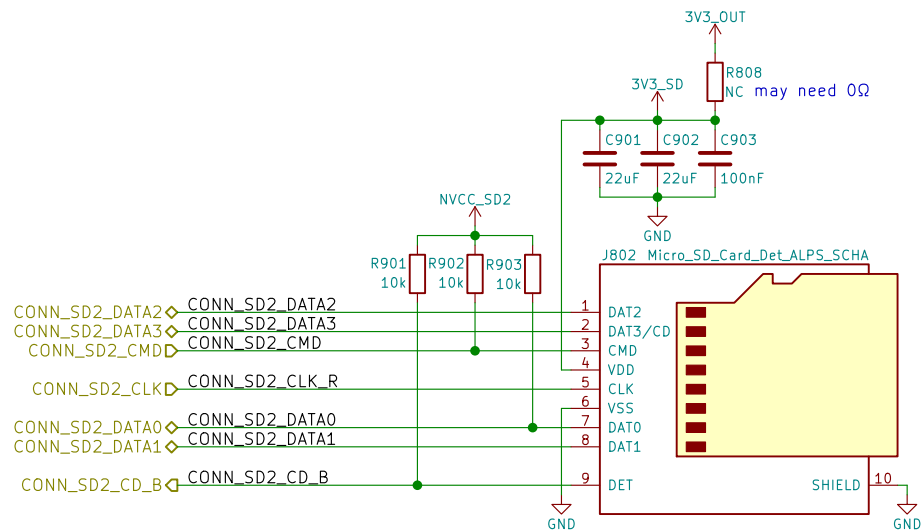
Title: UART Debug

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 6/16



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Purism SPC

Sheet: /uSD Card/

File: sd.sch

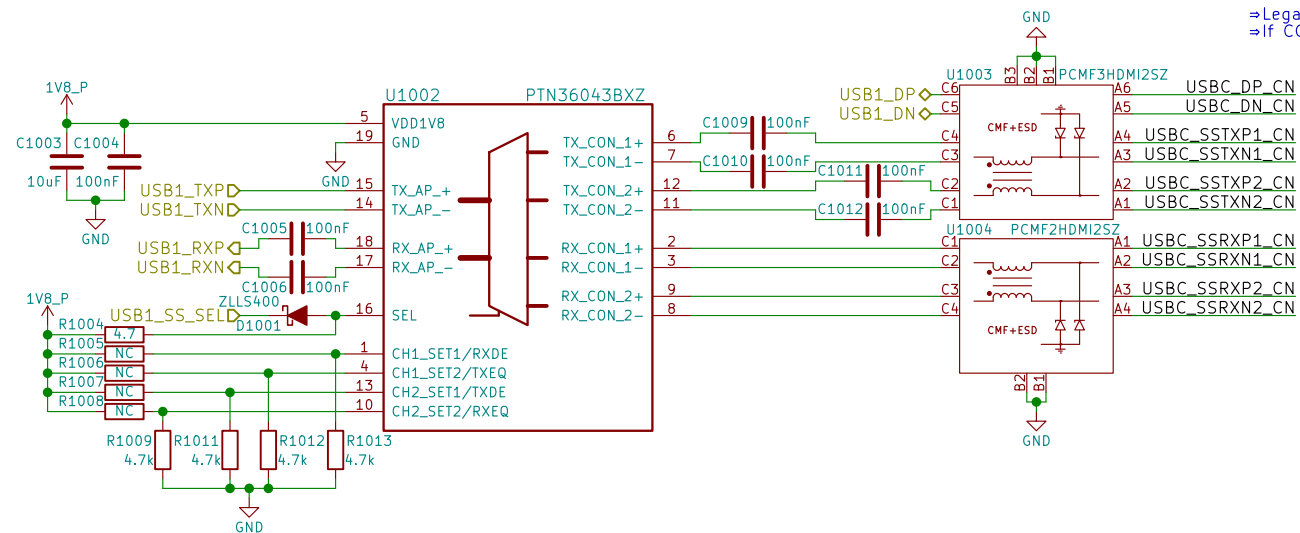
Title: uSD Card

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

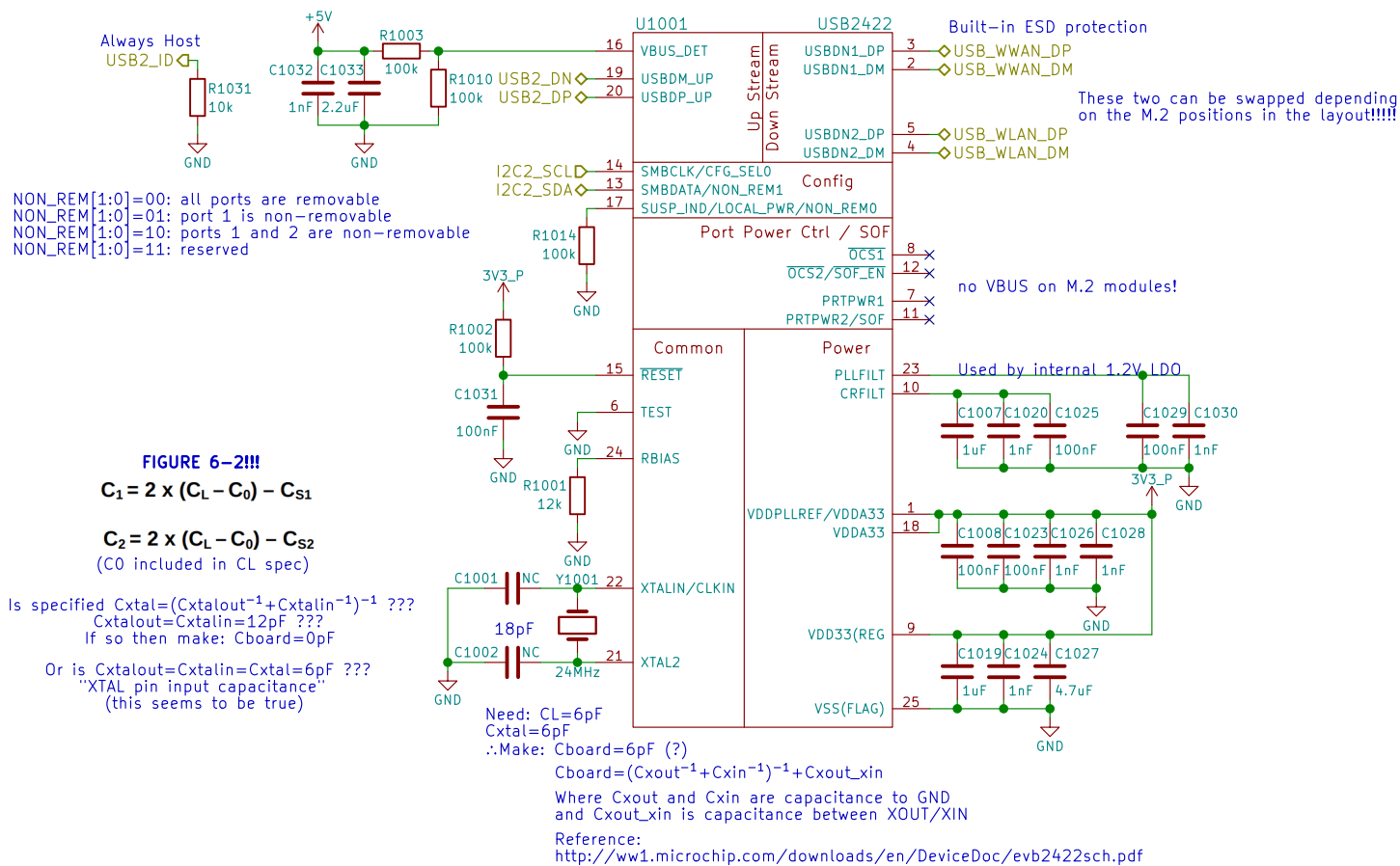
Rev: v0.1.0

Id: 8/16

[illegible]

Title: USB Type C

Rev: v0.1.0
d: 9/16



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Sheet: /USB Hub/

File: usb_hub.sch

Title:

Size: A4

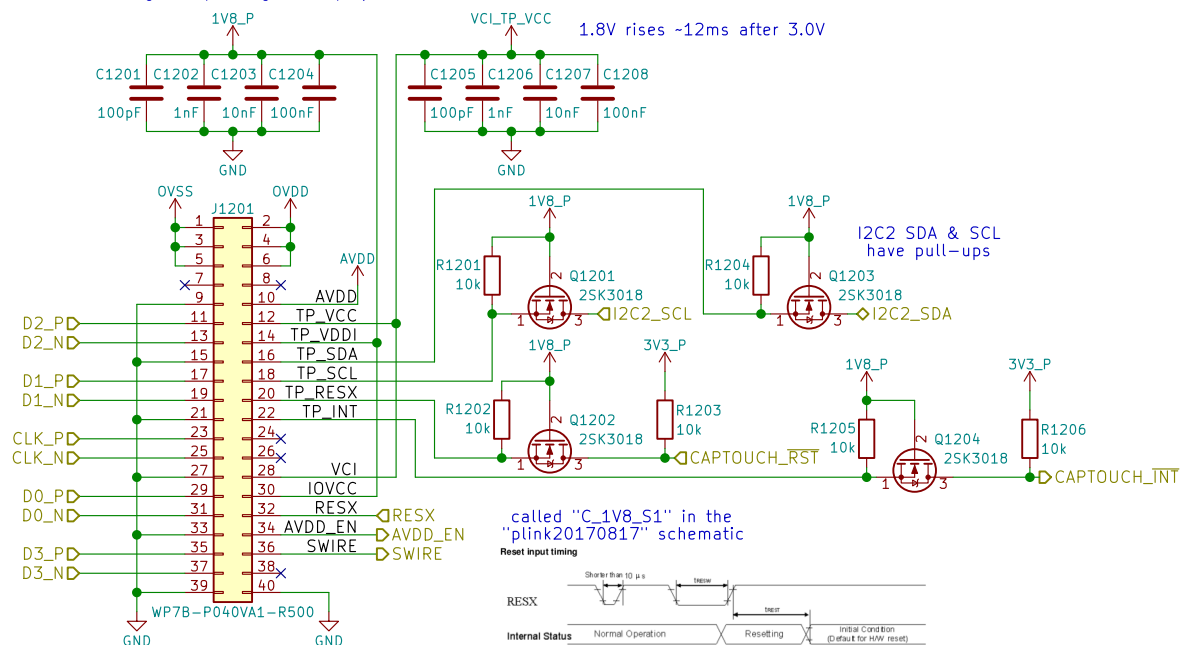
Date: 2018-05-02

Rev: v0.1.0

KiCad E.D.A. kicad 4.0.7

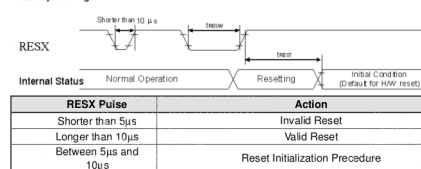
Id: 10/16

Using H546DLB01.1 pin assignment may need to be changed depending on display used



called "C_1V8_S1" in the "plink20170817" schematic

Reset input timing



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Purism SPC

Sheet: /MIPI DSI/
File: mipi_dsi.sch

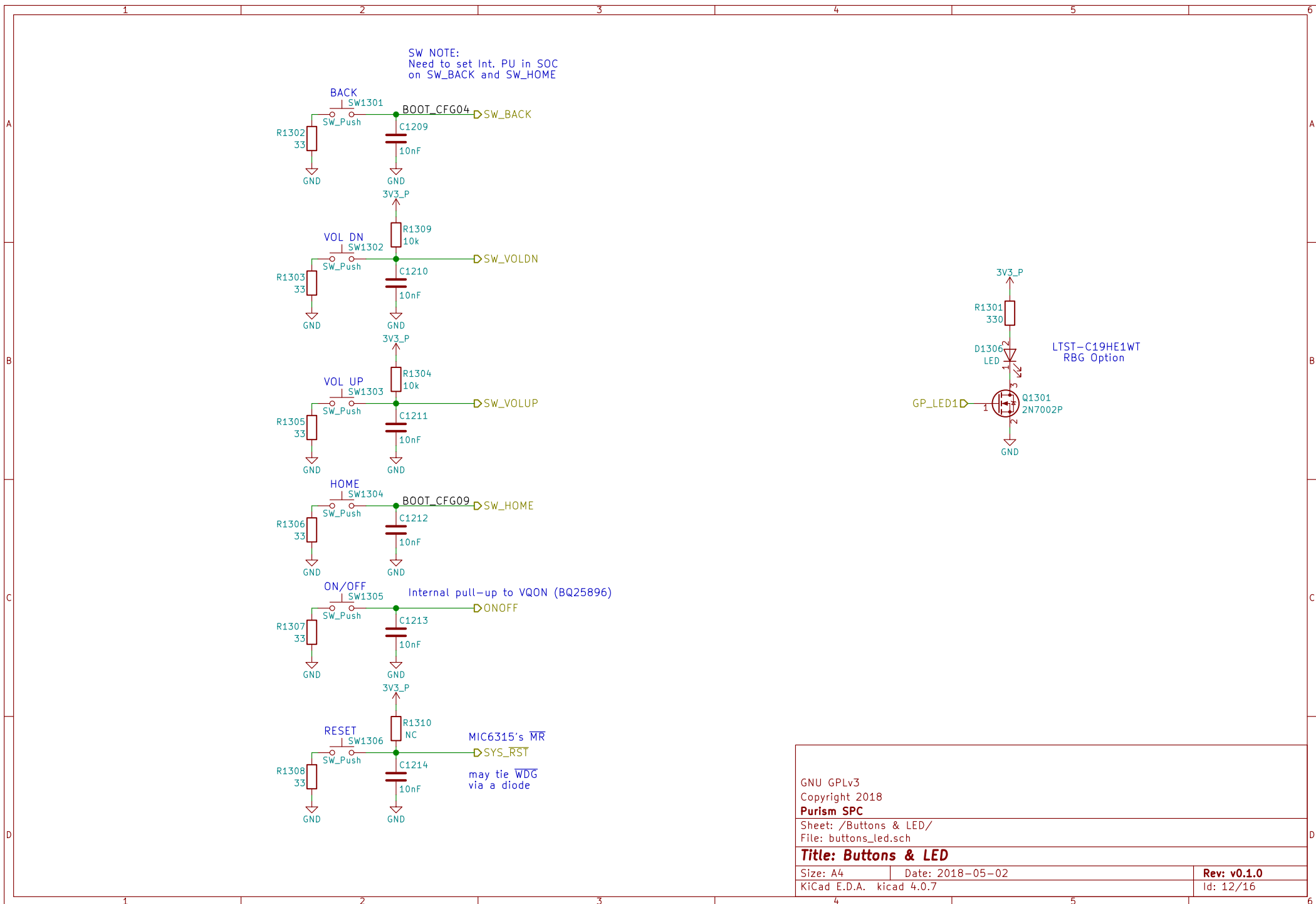
Title: MIPI DSI

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 11/16



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Sheet: /Buttons & LED/
File: buttons_led.sch

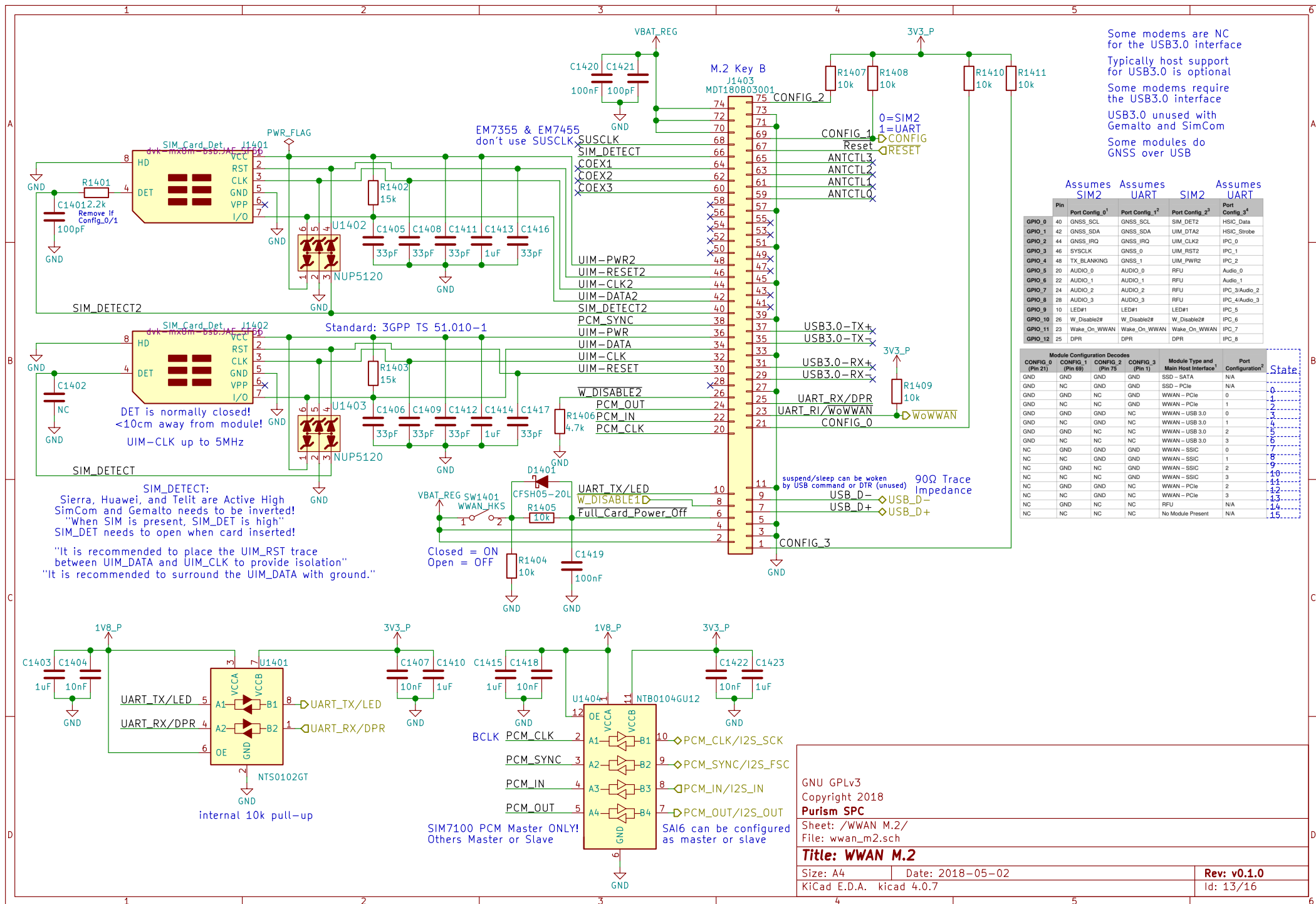
Title: Buttons & LED

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 12/16

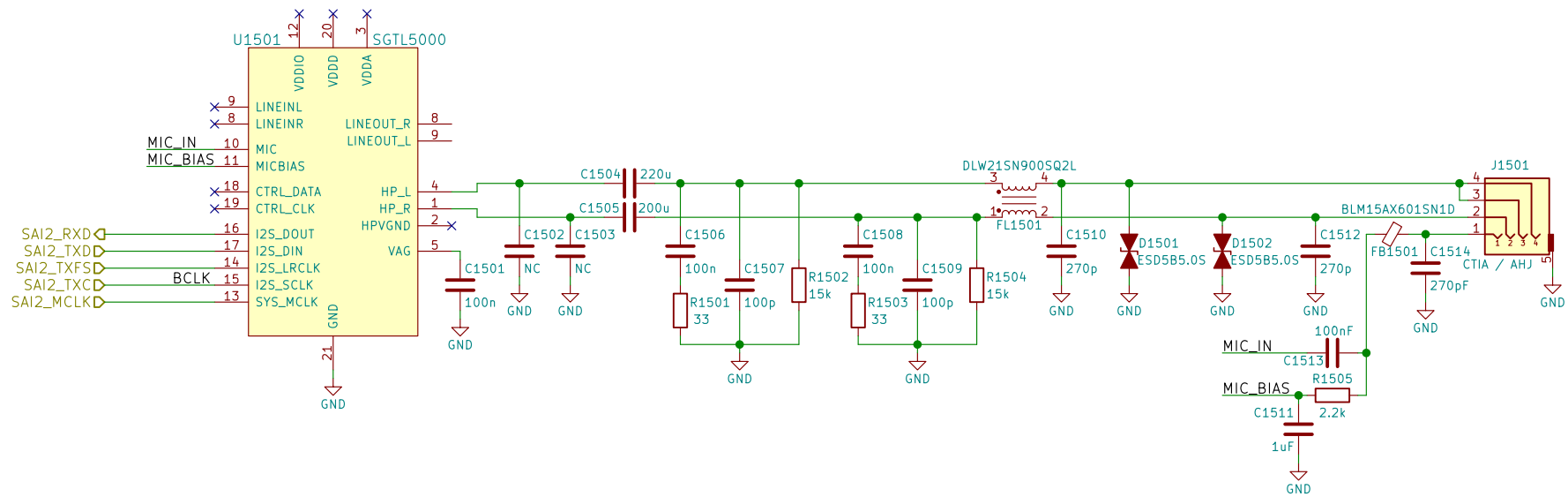


Some modems are NC for the USB3.0 interface
Typically host support for USB3.0 is optional
Some modems require the USB3.0 interface
USB3.0 unused with Gemalto and SimCom
Some modules do GNSS over USB

		Assumes SIM2	Assumes UART	Assumes SIM2	Assumes UART
Pin		Port Config. 0 ¹	Port Config. 1 ²	Port Config. 2 ³	Port Config. 3 ⁴
GPIO_0	40	GNSS_SCL	GNSS_SCL	SIM_DET2	HSIC_Data
GPIO_1	42	GNSS_SDA	GNSS_SDA	UIM_DTA2	HSIC_Strobe
GPIO_2	44	GNSS_IRQ	GNSS_IRQ	UIM_CLK2	IPC_0
GPIO_3	46	SYSClk	GNSS_0	UIM_RST2	IPC_1
GPIO_4	48	TX_BLANKING	GNSS_1	UIM_PWR2	IPC_2
GPIO_5	20	AUDIO_0	AUDIO_0	RFU	Audio_0
GPIO_6	22	AUDIO_1	AUDIO_1	RFU	Audio_1
GPIO_7	24	AUDIO_2	AUDIO_2	RFU	IPC_3/Audio_2
GPIO_8	28	AUDIO_3	AUDIO_3	RFU	IPC_4/Audio_3
GPIO_9	10	LED#1	LED#1	LED#1	IPC_5
GPIO_10	26	W_Disable2#	W_Disable2#	W_Disable2#	IPC_6
GPIO_11	23	Wake_On_WWAN	Wake_On_WWAN	Wake_On_WWAN	IPC_7
GPIO_12	25	DPR	DPR	DPR	IPC_8

		Module Configuration Decodes	Module Type and Main Host Interface ¹	Port Configuration ²	State
CONFIG_0 (Pin 21)	CONFIG_1 (Pin 69)	CONFIG_2 (Pin 75)	CONFIG_3 (Pin 1)		
GND	GND	GND	GND	SSD - SATA	N/A
GND	NC	GND	GND	SSD - PCIe	N/A
GND	GND	NC	GND	WWAN - PCIe	0
GND	NC	NC	GND	WWAN - PCIe	1
GND	GND	GND	NC	WWAN - USB 3.0	2
GND	NC	GND	NC	WWAN - USB 3.0	3
GND	GND	NC	NC	WWAN - USB 3.0	4
GND	NC	NC	NC	WWAN - USB 3.0	5
NC	GND	GND	NC	WWAN - SSIC	6
NC	NC	GND	GND	WWAN - SSIC	7
NC	GND	NC	GND	WWAN - SSIC	8
NC	NC	GND	GND	WWAN - SSIC	9
NC	GND	NC	GND	WWAN - SSIC	10
NC	NC	GND	GND	WWAN - SSIC	11
NC	GND	NC	GND	WWAN - PCIe	12
NC	NC	GND	NC	RFU	13
NC	GND	NC	NC	RFU	14
NC	NC	NC	NC	No Module Present	15

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Purism SPC
Sheet: /WWAN M.2/
File: wwan_m2.sch
Title: WWAN M.2
Size: A4 Date: 2018-05-02 Rev: v0.1.0
KiCad E.D.A. kicad 4.0.7 Id: 13/16



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Purism SPC

Sheet: /Audio/
File: audio.sch

Title: Audio

Size: A4 Date: 2018-05-02
KiCad E.D.A. kicad 4.0.7

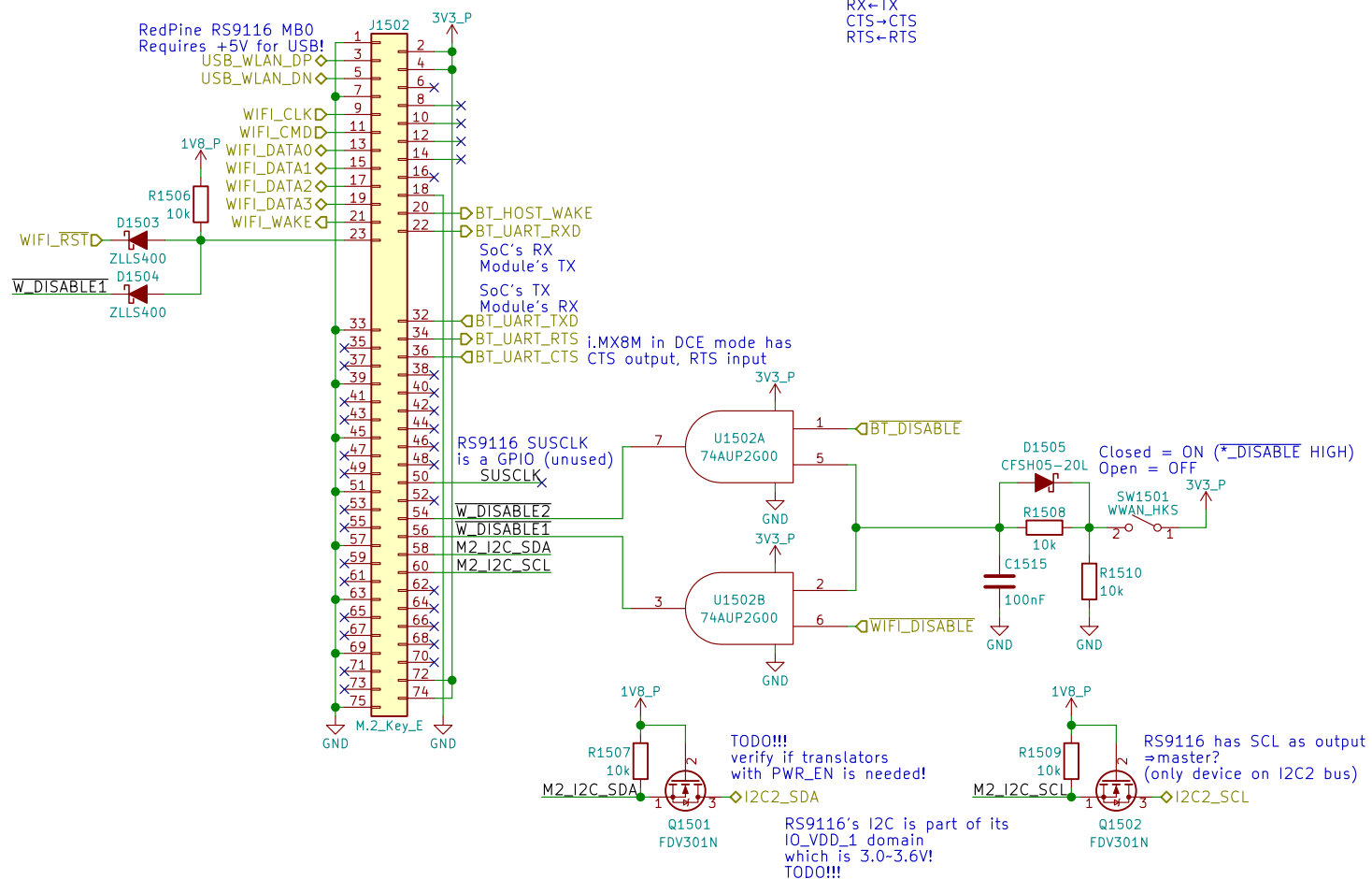
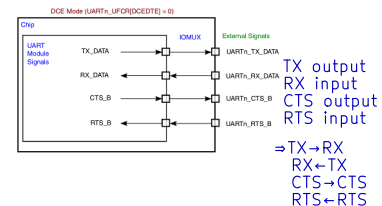
Rev: v0.1.0
Id: 14/16

RS9116 NC:
RTS, CTS, BT_HOST_WAKE, WIFI_WAKE

6.2 M.2 Signal Directions

Module: Table 23
Socket: Table 46

UARTn_UFCR[DCEDTE]=0 on POR



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Purism SPC

Sheet: /WLAN+BT M.2/

File: wifi_bt_m2.sch

Title: WLAN+BT M.2

Size: A4 Date: 2018-05-02

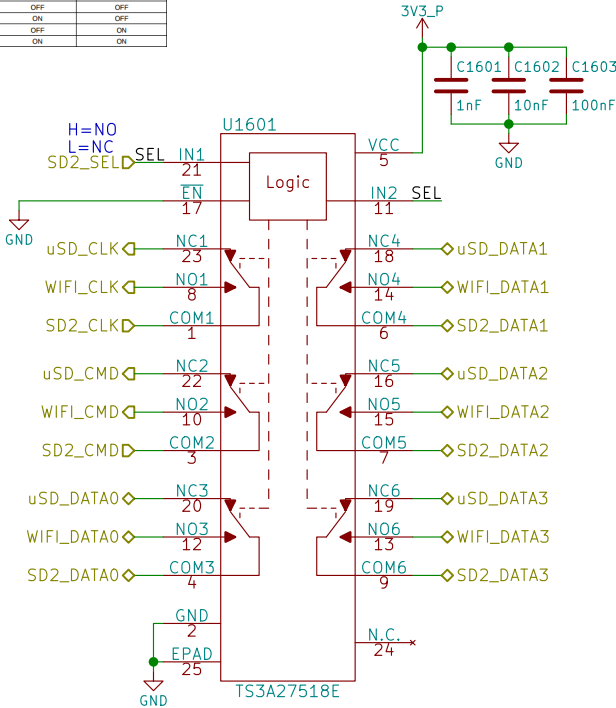
KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 15/16

Can swap around signals in the layout:

EN	IN1	IN2	NC1023 TO COM1023, COM1023 TO NC1023	NC4056 TO COM4056, COM4056 TO NC4056	NC1023 TO COM1023, COM1023 TO NC1023	NC4056 TO COM4056, COM4056 TO NC4056
H	X	X	OFF	OFF	OFF	OFF
L	L	L	ON	ON	OFF	OFF
L	H	L	OFF	ON	ON	OFF
L	L	H	ON	OFF	OFF	ON
L	H	H	OFF	OFF	ON	ON



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Purism SPC

Sheet: /SDIO DEMUX/
File: sdio_demux.sch

Title: SDIO Demultiplexer

Size: A4 Date: 2018-05-02

KiCad E.D.A. kicad 4.0.7

Rev: v0.1.0

Id: 16/16