

WP0: Management, Coordination and Dissemination
Lead: GRAME

WP1: FPGA Programming With Faust
Lead: CITI

WP2: Hardware Implementation
Lead: CITI

WP3:
3D Artificial
Reverb
Lead: LMFA

WP4:
DSP Processor
Module
Lead: GRAME

**WP5: Active Control Applications &
Artistic/Perceptual Validation**
Lead: LMFA/GRAME

Workpackages

Human Resources

Postdoc: LMFA

Engineer: GRAME/CITI

Intern: LMFA

Intern: GRAME

PhD: GRAME/CITI

T0

T0+42