## **WP0:** Management, Coordination and Dissemination *Lead: GRAME*

**WP1:** FPGA Programming With Faust *Lead: CITI* 

**WP2:** Hardware Implementation Lead: CITI WP3: 3D Artificial Reverb Lead: LMFA WP4:
DSP Processor
Module
Lead: GRAME

Demonstrator //
Use In an Artistic Context
Lead: GRAME

**WP5**:

Workpackages

**WP6:** Active Control Applications *Lead: LMFA* 

Postdoc: LMFA

**Human Resources** 

**Engineer:** GRAME/CITI

Intern: LMFA

**Intern:** GRAME

**Postdoc:** GRAME

PhD: GRAME/CITI