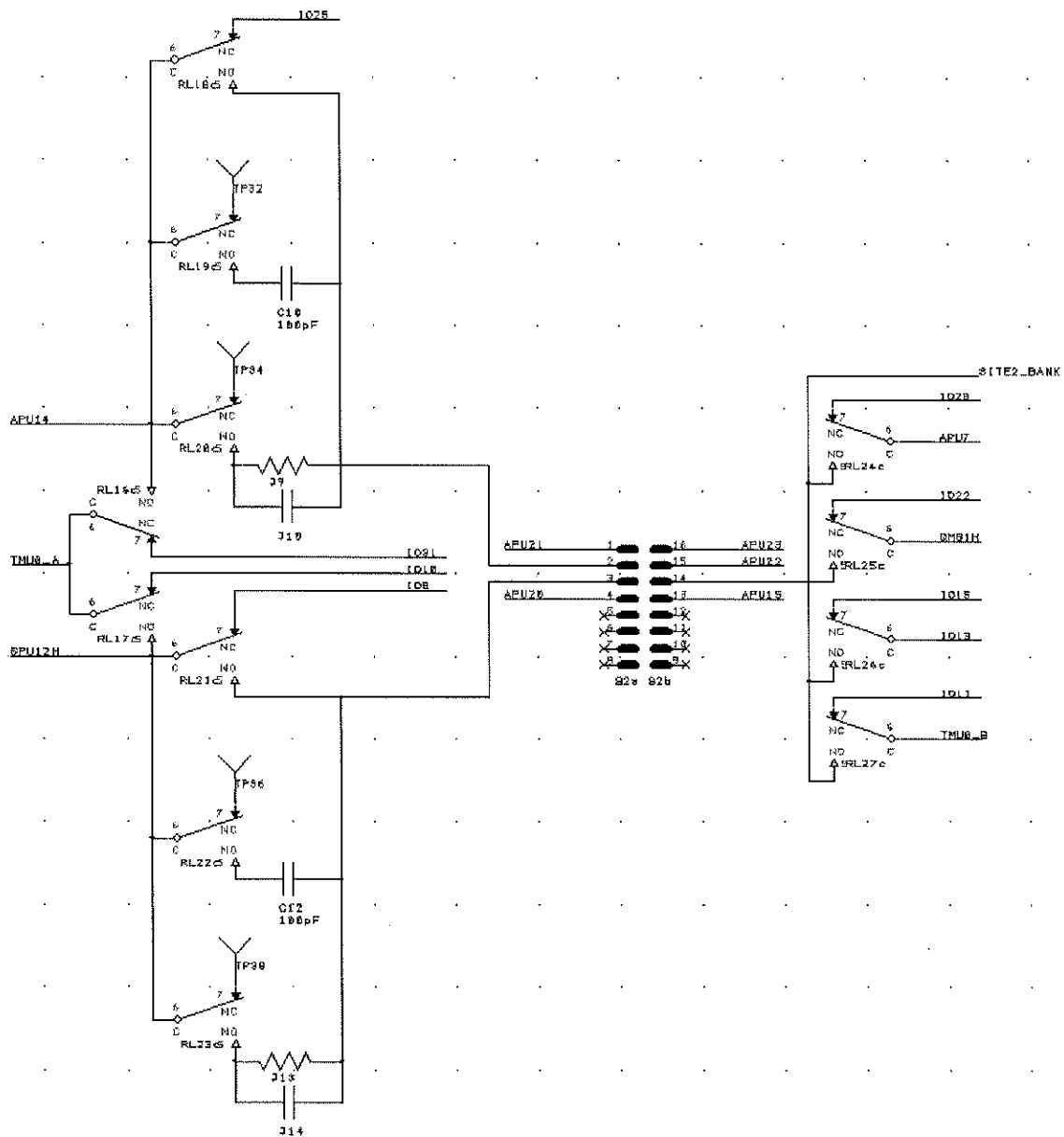
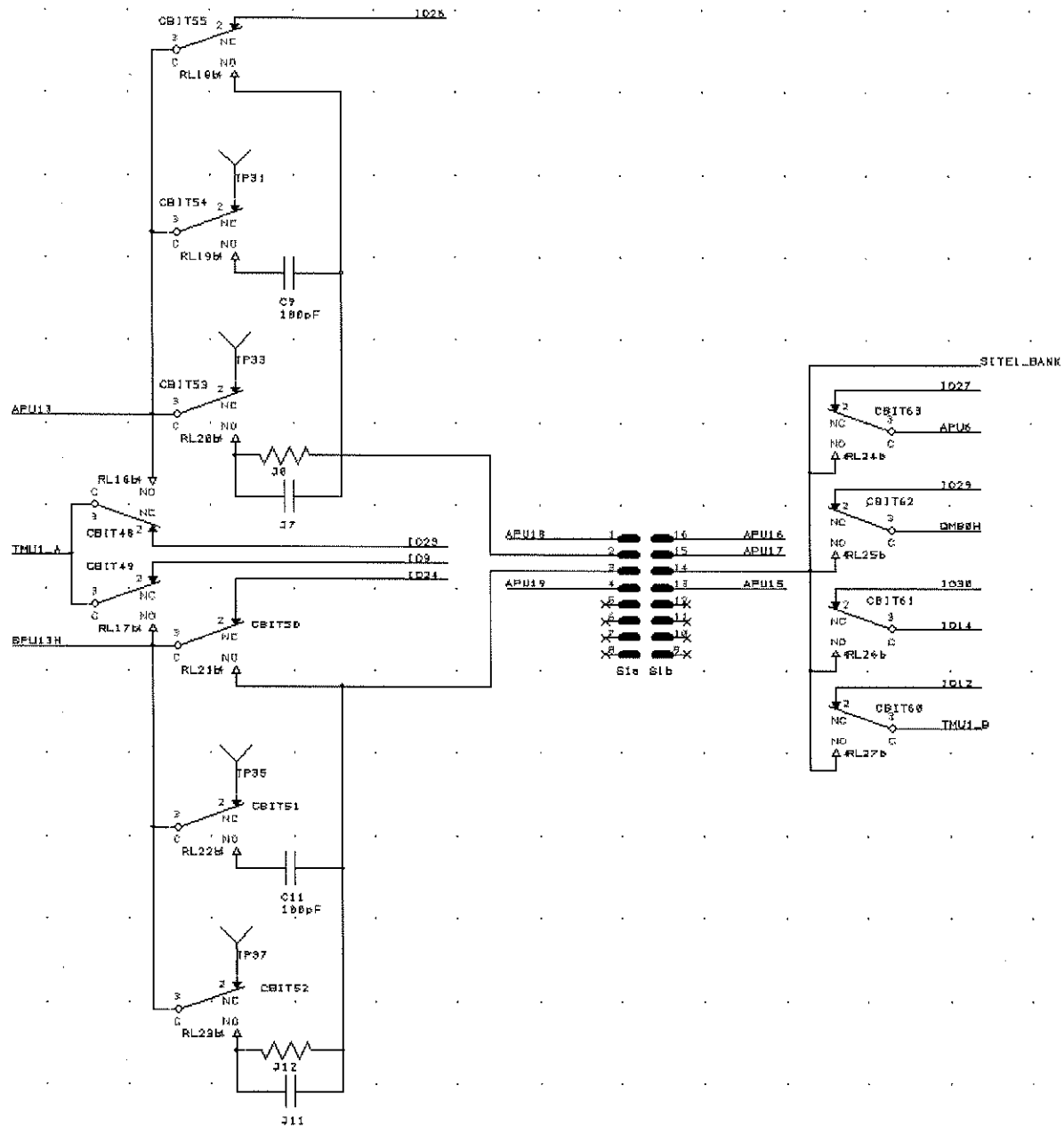


Appendix B: Daughter Board Schematics





APU7

