Environment Block Diagram

Class: axi4_2x2_fabric_environment

Type: uvmf_in_order_race_scoreboard
Inst: axi4_m0_rw_sb

Class: axi4_slave_rw_predictor
Inst: axi4_slave_rw_pred

rw_txn

rw_txn

AXI4_ID_WIDTH=4

AXI4_ID_WIDTH=5

Class: axi4_2x2_fabric_qvip
Inst: axi4_qvip_subenv

mgc_axi4_m0

mgc_axi4_s0

mgc_axi4_m1

mgc_axi4_s1

axi4_master_rw_transaction

Type: uvmf_in_order_race_scoreboard
Inst: axi4_m0_sb

mvc_sequence_item_base

Type: axi4_master_predictor
Inst: axi4_m1_pred

Class: axi4_slave_predictor
Inst: axi4_s1_pred
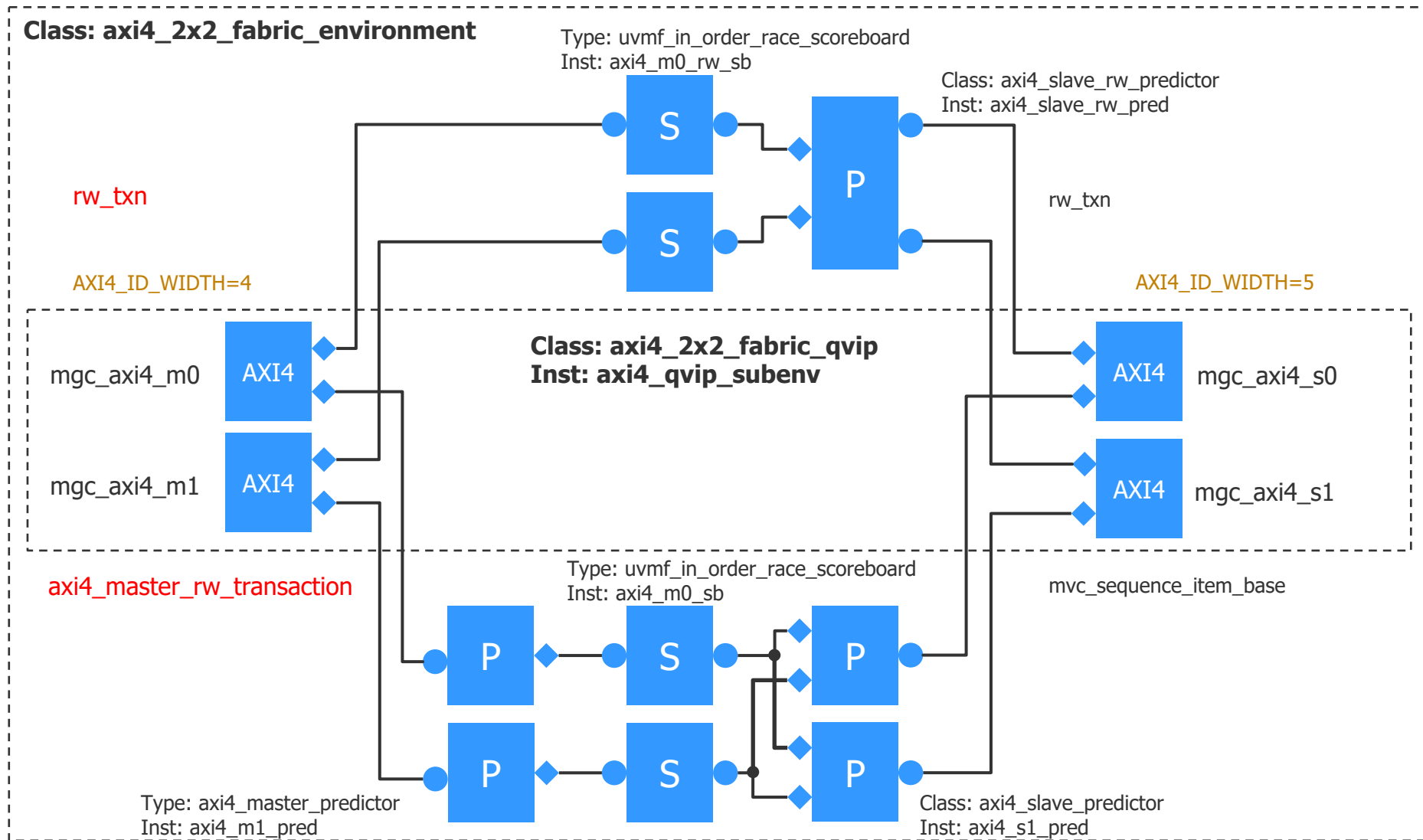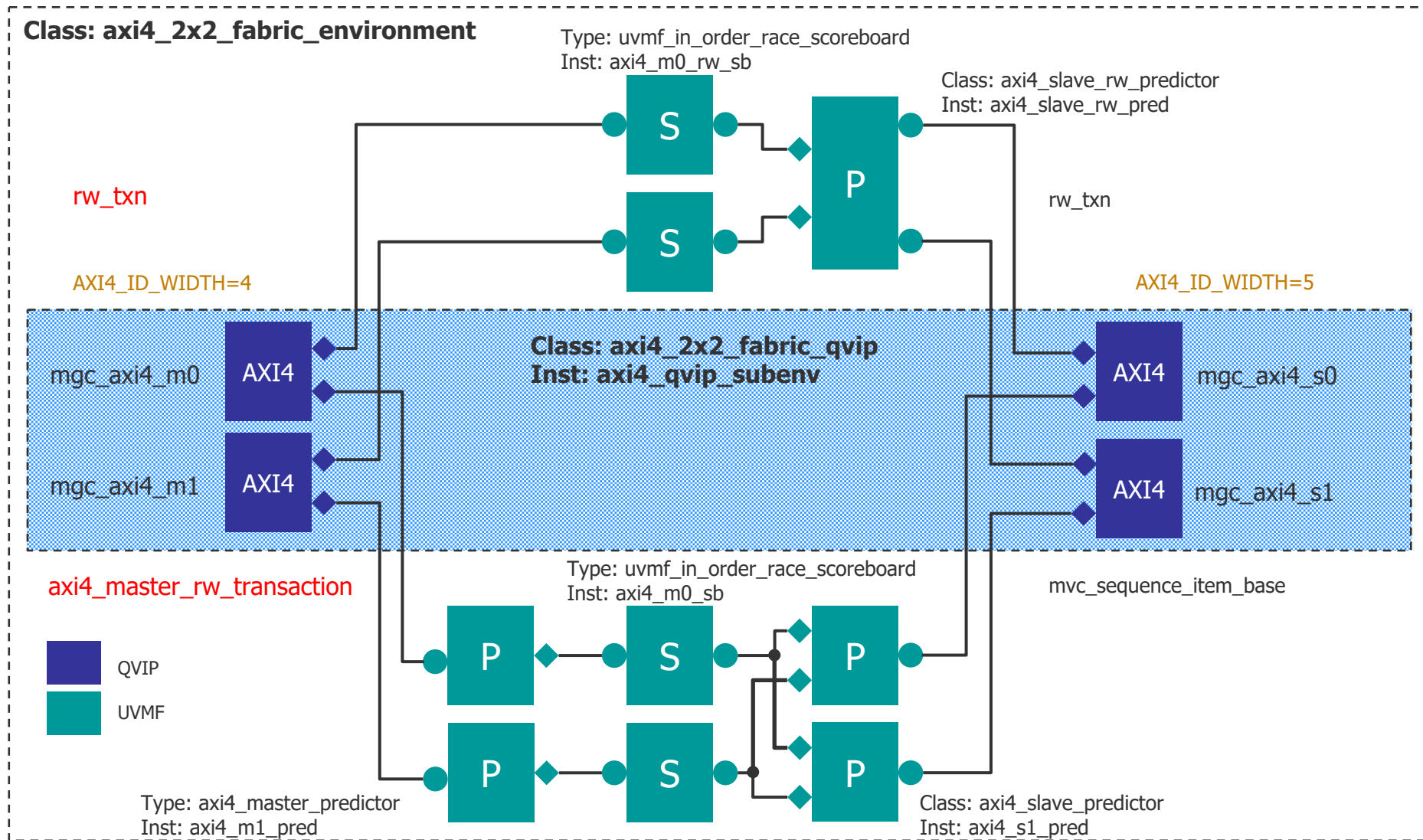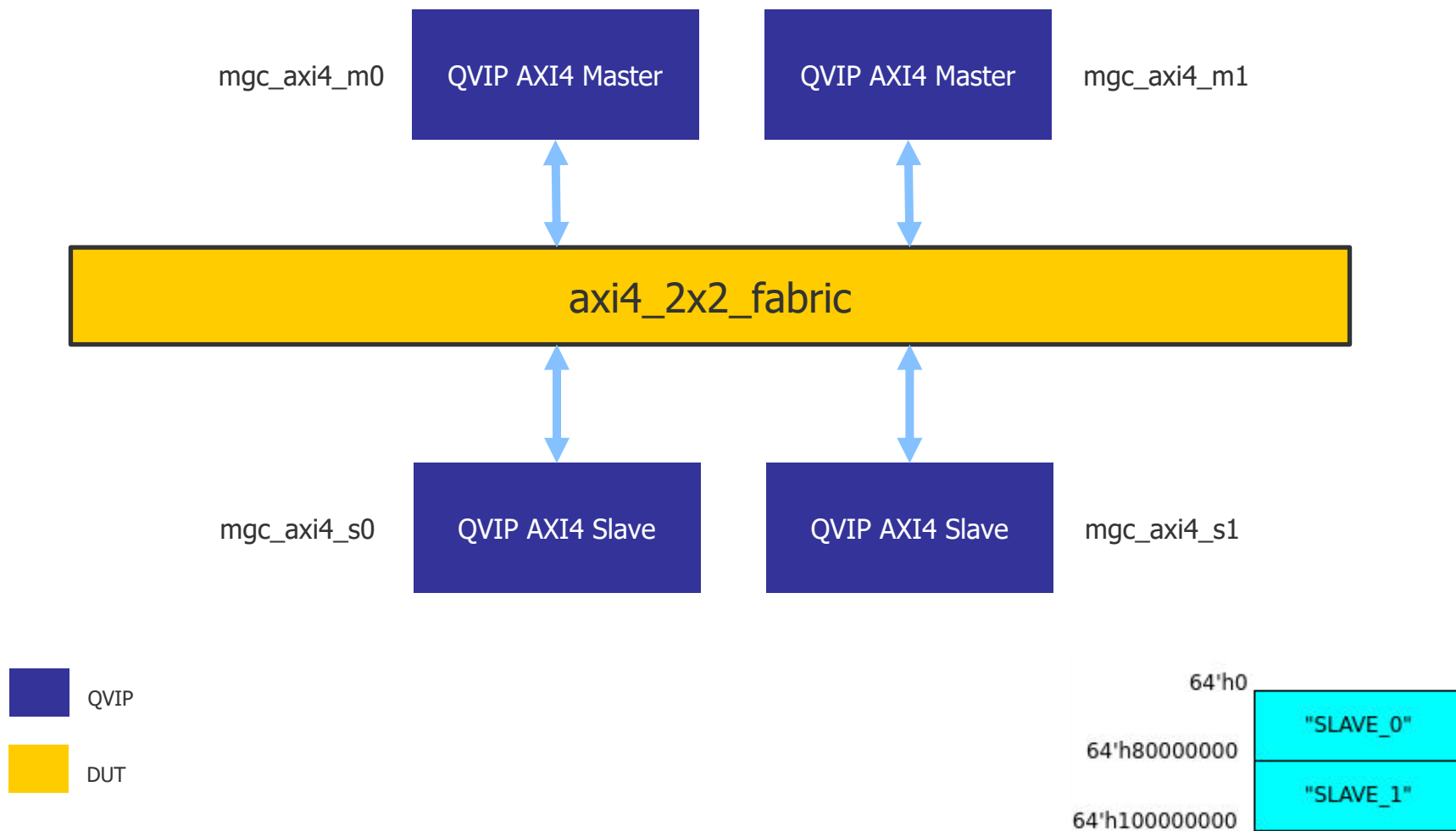
Environment Block Diagram

DUT Connections Block Diagram

Testbench Block Diagram

Module: hdl_top

Module: hdl_axi4_2x2_fabric_qvip
Inst: uvm_test_top_environment_axi4_qvip_subenv_qvip_hdl

mgc_axi4_m0 — AXI4 — AXI4 — mgc_axi4_s0

mgc_axi4_m1 — AXI4 — AXI4 — mgc_axi4_s1

Module Wrapper: axi4_master   Module Wrapper: axi4_slave

CLK Generator

RST Generator

QVIP Configurator

DUT
axi4_2x2_fabric