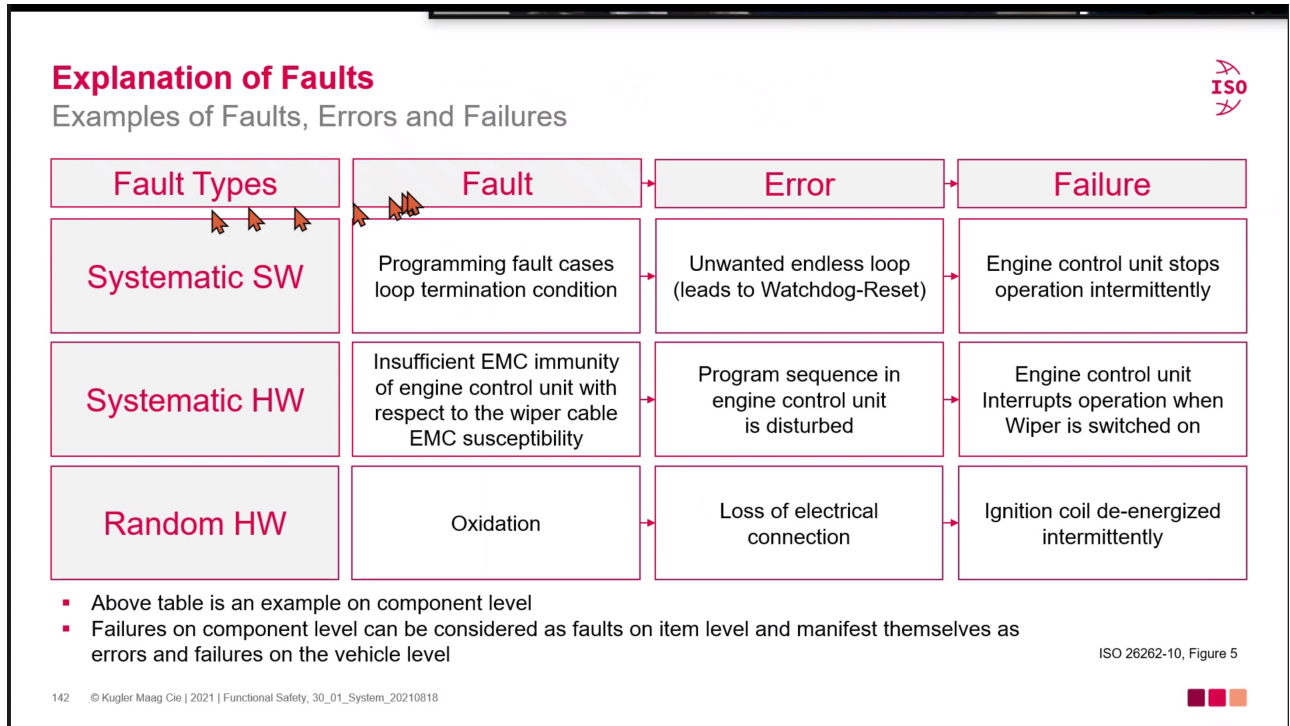


# Safety Analyses Day 1

## Methods for Safety Analyses and Safety Analyses on system Level

- Explanation of Faults



- systematic HW - Random HW
    - systematic은 특정 상황에서 발생
- System Design Safety Analyses
  - Perform safety of the system design according to the following table and ISO 26262-9, clause 8

Methods		ASIL			
		A	B	C	D
1	Deductive analysis	0	+	++	++
2	Inductive analysis	++	++	++	++
Deductive analysis methods include FTA, reliability block diagrams, Ishikawa diagram. Inductive analysis methods include FMEA, ETA, Markov modelling.					

## ISO26262-9

### ASIL Decomposition

- **Obecjective**

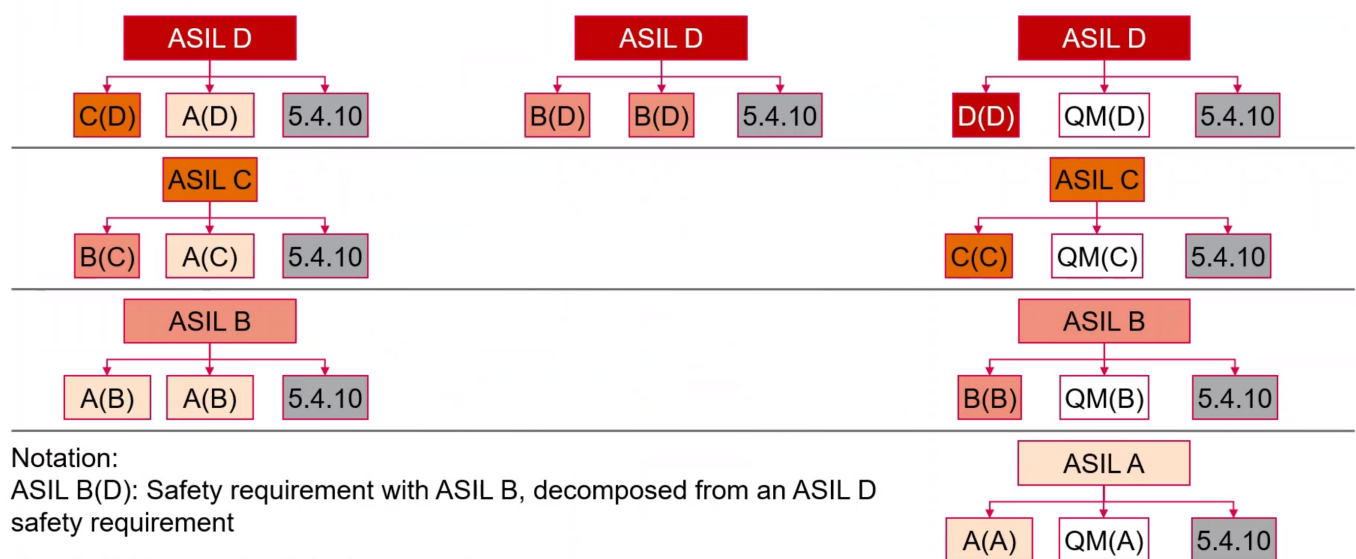
- To ensure that a safety req. is decomposed into redundant safety reqs at the next level of detail, and that these are allocated to sufficiently indepent design elemets
- To apply ASIL decomposition according ot permitted ASIL decomposition schemas

- **Benefit?**

- Avoid signle-point faults, achieve hw qunatiative tagets
- less developments effort because of lower ASILs or elements
- Avoid implementing intended functionality with ASILs

## ASIL Decompotion

### ASIL Decomposition (2)



Notation:

ASIL B(D): Safety requirement with ASIL B, decomposed from an ASIL D safety requirement

5.4.10: Evidence of sufficient independence

\* why? \* .....

### ASIL Decompotion(3)

- ASIL decompostion taolopring during the design process
  - ASIL decompostion applies to **safety reqs**, not to ach elemets
  - decomposition can related to any archi lv.
  - more than one such ASIL decomposition may be applied
- Possible reasons for decomposition:
  - Reduced developmennt effort for decomposed elements due to reduced ASIL.(eg. analysis effort, specificaition effort, review, test)
  - Redundancy allows to achieve hardware metires and reliability targets easier. (eg. less single point faults)

### ASIL Decompotion(4)

- similar safety reqs., sufficiently indepedndnt achitectual emelmets or subsystems.
- Use reduced ASIL for developments activities.
-

## Analysis of dependent Failures (1)

- Objectives
  - To confirm the required independence or freedom from interference is sufficiently achieved in the design by analysing their potential causes or initiators
  - To define safety measures to mitigate plausible dependent failures, if necessary
  - The following info. shall be available for the analysis:
    - Req for independence and freedom from interference at the applied lv.
    - Archi. info.
- Dependent Failures
  - failure that not statistically independent.

## Analysis of dependent Failures (2)

- Each identified potential for dependent failures shall be evaluated to determine if foreseeable causes exist that lead to the occurrence of dependent failures and consequently violates a required independence.

## Safety analyses (general)

- Objective
  - To ensure that the risk of a safety goal violation due to systematic faults or random hardware faults is sufficiently low
- Validation / verification of safety goal safety concepts and safety reqs.
- Identification of conditions, faults and failures that could lead to a violation of safety

## Methods for Safety Analyses

# Methods for Safety Analyses

## An Overview

- Failure modes and effects analysis (FMEA)
- Cause consequence diagrams
- Event tree analysis (ETA)
- Failure modes, effects and criticality analysis (FMECA)
- Fault tree analysis (FTA)
- Markov models
- Reliability block diagrams (RBD)
- Monte-Carlo simulation
- Fault tree models
- Generalized Stochastic Petri net models (GSPN)

### Safety Analyses in the safety lifecycle

- Purpose is to assist in the design

### Reliability Block Diagram (RBD)

#### Goal and Definition

- Goal
  - Set of events that must take place and conditions which must be fulfilled for a successful operation of a system or a task
  - Depict Success path consisting of blocks, line and logical junctions.
  - more a method of representation than a method of analysis.

???????

### FTA (Fault Tree Analysis)

#### FTA

- Analytical technique where the causes of an undesired state of a system are analyzed
- the system is analyzed using boolean logic in the context of its environments and operation to find all circumstances under which the top event can occur
- Graphic model of the parallel and sequential combinations of faults which can cause the top event
- Top-down, deductive approach