

LP Assignment 5

27-4-15

1. Give an overview of analysis and synthesis phase of assembler design along with relevant data structures.
2. State and explain the principles used when designing calling sequences and the layout of activation records.
3. Explain the different kinds of instructions and addressing modes supported by a simple three address target machine.
4. Give the algorithms for the following:
 - a. Partitioning three address instructions into basic blocks.
 - b. Determining the liveness and next-use information for each statement in a basic block.