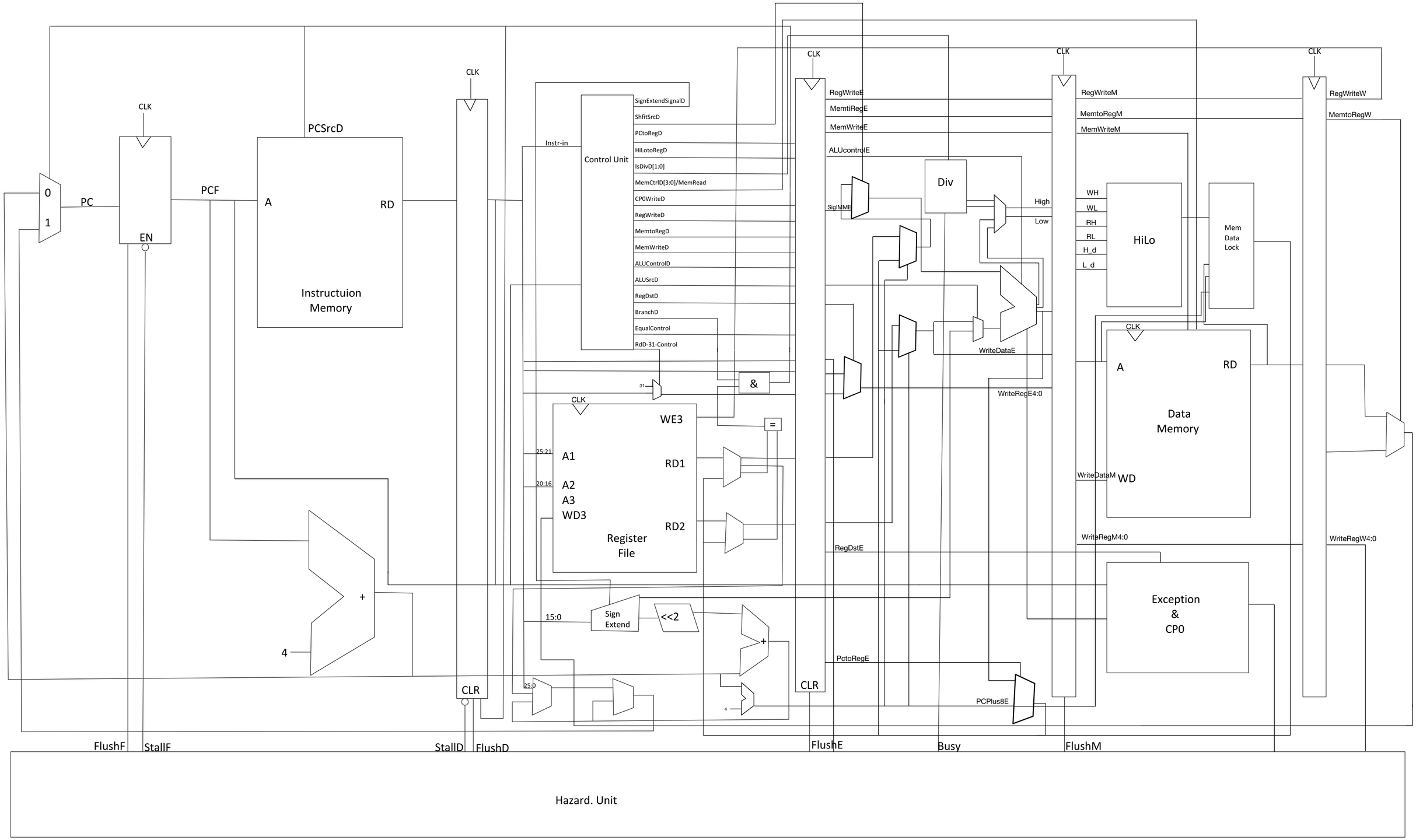
2019年4月5日


CLK
PCSrcD
0
1
PC
PCF
EN
A
RD
Instructuion
Memory
Instr-in
Control Unit
SignExtendSignalD
ShfitSrcD
PCtoRegD
HiLotoRegD
IsDivD[1:0]
MemCtrlD[3:0]/MemRead
CP0WriteD
RegWriteD
MemtoRegD
MemWriteD
ALUControlD
ALUSrcD
RegDstD
BranchD
EqualControl
RdD-31-Control
RegWriteE
MemtiRegE
MemWriteE
ALUcontrolE
SigIMME
Div
High
Low
WriteDataE
WriteRegE4:0
RegDstE
PctoRegE
PCPlus8E
RegWriteM
MemtoRegM
MemWriteM
WH
WL
RH
RL
H_d
L_d
HiLo
Mem
Data
Lock
Data
Memory
WD
WriteDataM
WriteRegM4:0
Exception
&
CP0
RegWriteW
MemtoRegW
WriteRegW4:0
&
=
31
WE3
A1
A2
A3
WD3
RD1
RD2
Register
File
25:21
20:16
15:0
Sign
Extend
<<2
+
4
25:0
CLR
FlushF
StallF
StallD
FlushD
FlushE
Busy
FlushM
Hazard. Unit