

* EM_BUTTON2 * EM_UART_RTS

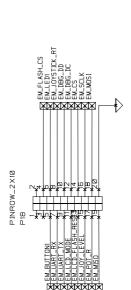
EM_JOYSTICK_UP * EM_JOYSTICK_LT * EM_RESET * EM_LCD_CS * EM_JOY_MOVE *

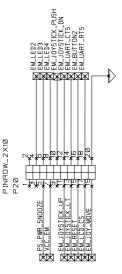
*EM_LED2 *EM_LED3 *EM_LED4

P6 SMD_HEADER_2×10

EM_JOYSTICK_PUSH * PS_PWR_SNOOZE;



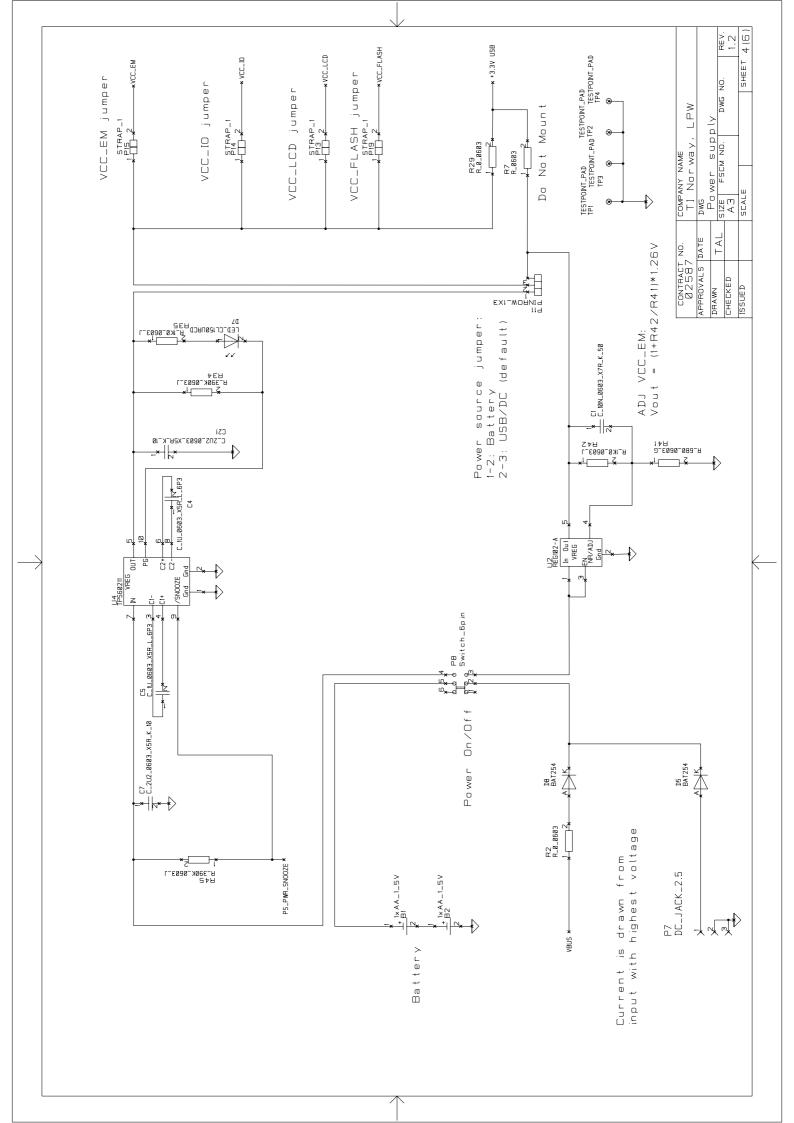


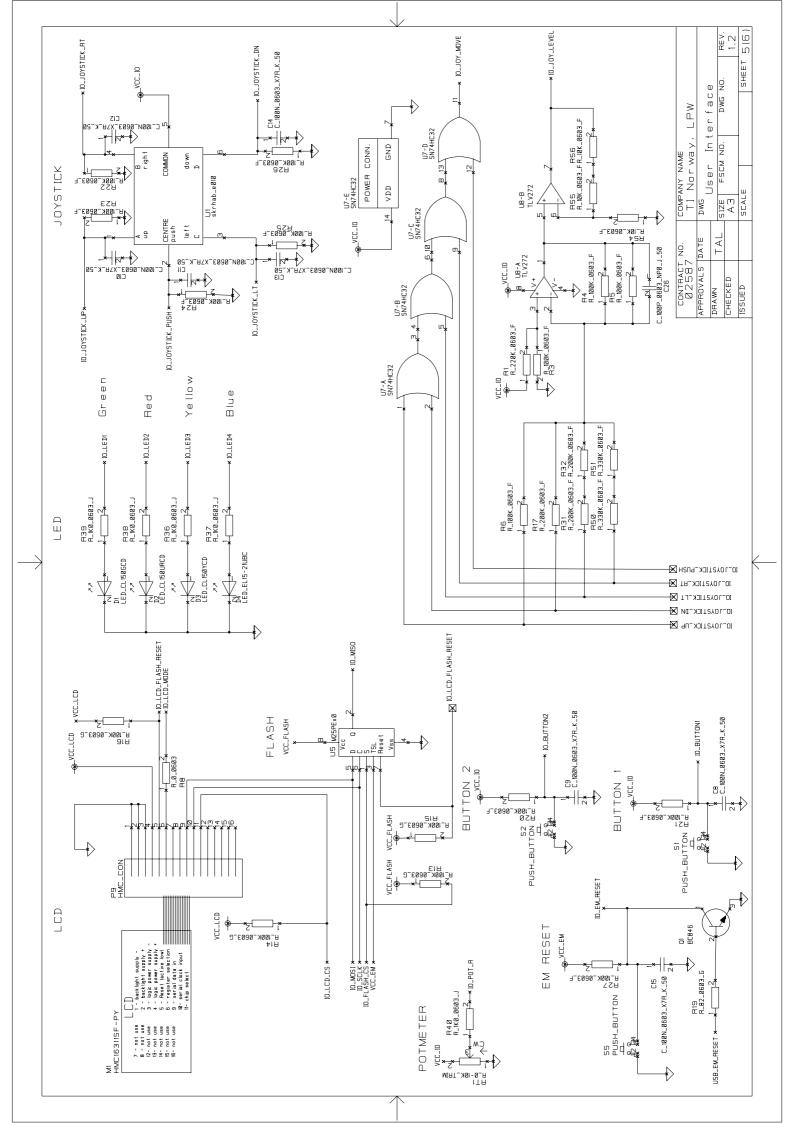


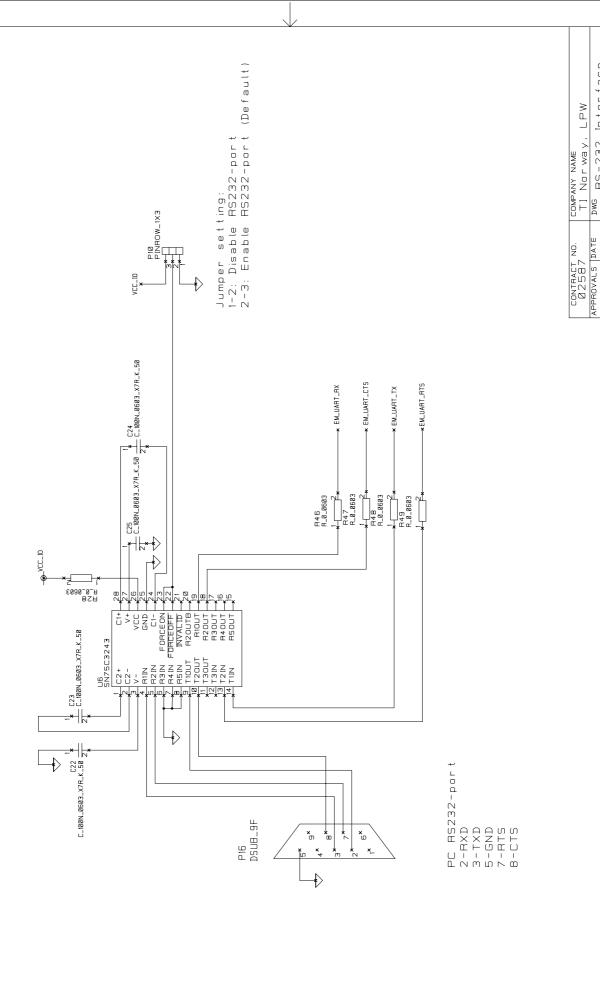
Debug Connectors

Debug connectors mirror EM connectors with a few exceptions. Exceptions: Exceptions: Firs 1, Z and 3 left unconnected, GND connected to pin 20. Reason for exception is to be able to connect an Agilent logic analyzer probedirectly to the debug connector.

			PE V	1.2	3 (6
W L L			DWG NO.		SHEET
		O)			
OMPANY NAME TI Norway, LPW		In ter tace	FSCM NO.		
COMPANY NAME	-				щ
COMP	DWG	∑ ∐	SIZE	ΑЭ	SCALE
CONTRACT NO. 02587	DATE	- -	1 A L		
	APPROVALS DATE	DRAWN		CHECKED	ISSUED







NO. REV. 1.2

DWG NO.

FSCM NO.

SIZE A3 SCALE

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CHECKED DRAWN

ISSUED

APPROVALS DATE

RS-232 Interface