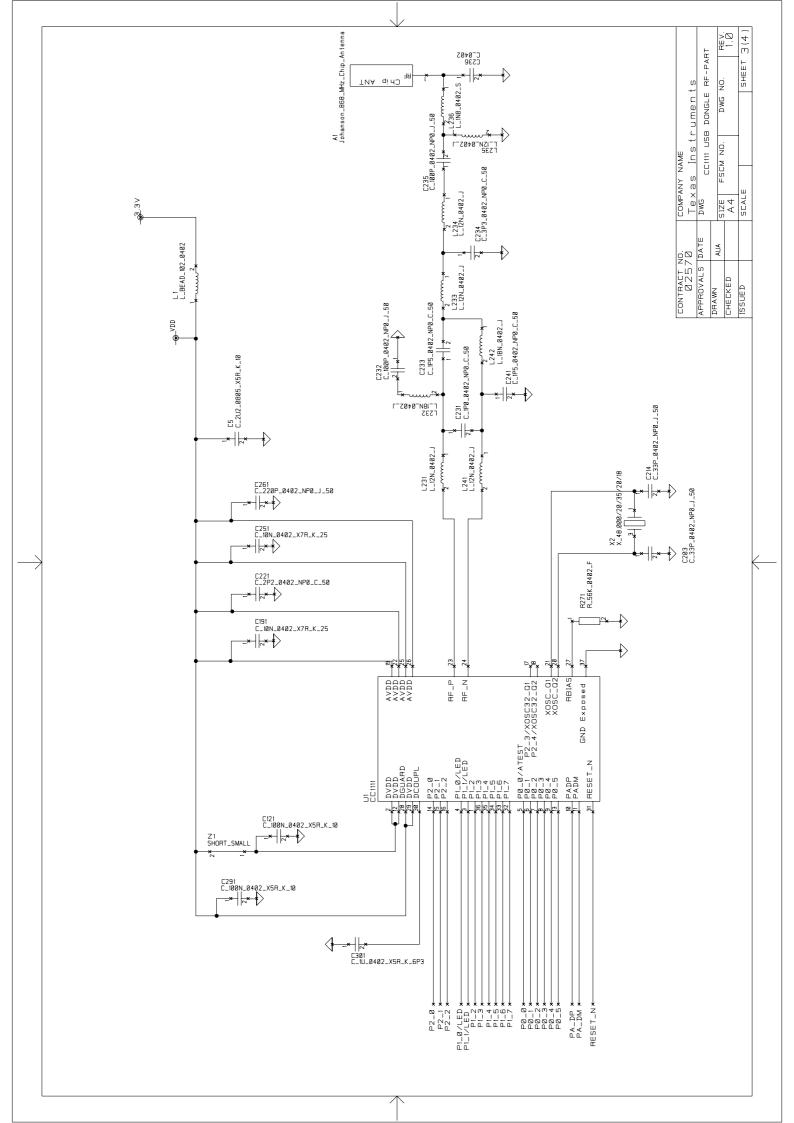
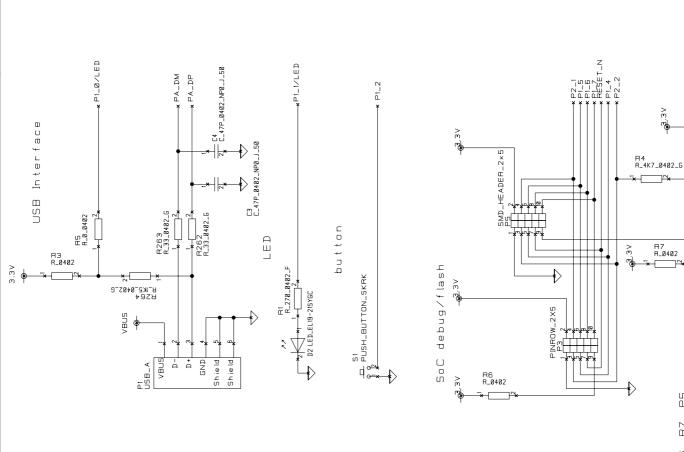
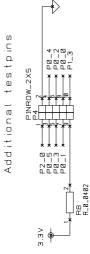
RF-SoC PART Including chip antenna RESET_N TTTTT 222222 111111 2-2640 000000 00000 111111 0-0040 RESET_N SoC periferials - USB Connector - Buttons - LEDs - SMD sockets VOLTAGE REGULATOR Generated voltage: 3.3 V for CCIIII

FIDUCIAL_MARK_EDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FIDUCIAL_MARK FING FMG (%) (%) (%) (%) (%)

CONTRACT NO.	.0	COMPANY NAME
レロクロ	_	
777	2	lexas Instruments
APPROVALS DATE	DATE	DWG.
DRAWN	A11A	
	¥0¥	SIZE FSCM NO. DWG NO. REV.
CHECKED		A4 1.8
ISSUED		SCALE SHEET 1(4)

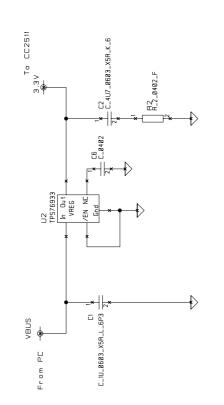






CONTRACT NO.		COMPANY NAME	
7920	2	Texas Instruments	ents
APPROVALS DATE	DATE	DWG	
		Vational along delight visits	2011
DHAWN	VIIV		
	AUA	SIZE FSCM NO. I	DWG NO. REV.
СНЕСКЕВ		A4	1.0
ISSUED		SCALE	SHEET 4(4)

Not mount: R3, R6, R7, P5



Not mount: C6

CONTRACT NO.		COMPANY NAME	
7520	2	Texas Instruments	t s
APPROVALS DATE	DATE	DWG	
		ALTA III DEA PONT I DIVOT BELL MILL	AUTA HEREI
DRAWN	*11.4		10001
	AUA	SIZE FSCM NO. DWG	DWG NO. REV.
CHECKED		A4	1. Ø
ISSUED		SCALE	SHEET 2 (4)