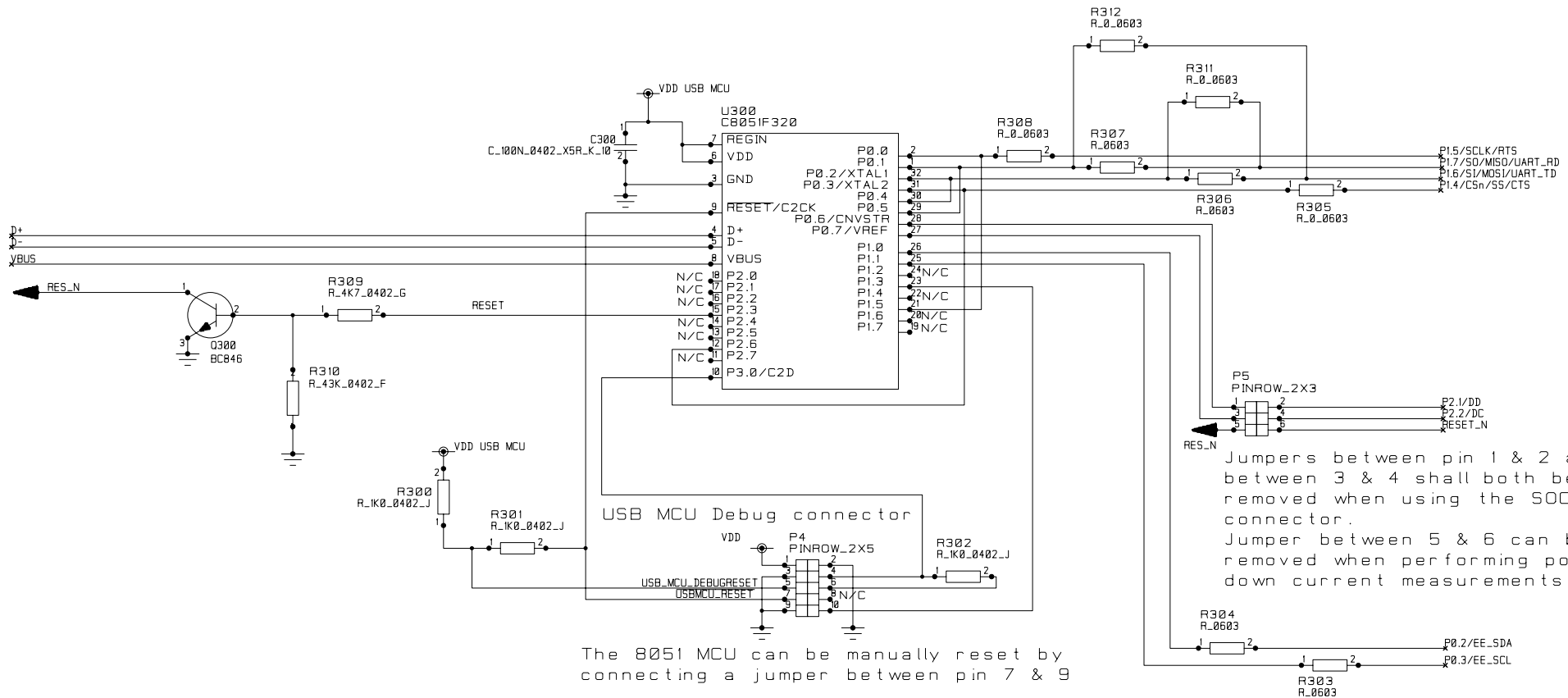CC2430 is nominally connected as a slave on the SPI bus with the USB MCU.
To connect it as master, resistors R311 and R312 must be removed and
0Ohm resistors put in positions R306 and R307.

R312
R_0_0603

R311
R_0_0603

R308
R_0_0603

R307
R_0603

VDD USB MCU

U300
C8051F320

C300
C_100N_0402_X5R_K_10

7 REGIN
6 VDD
3 GND

9 RESET/C2CK

4 D+
5 D-
8 VBUS

P0.0
P0.1
P0.2/XTAL1   32
P0.3/XTAL2   31
P0.4   30
P0.5   29
P0.6/CNVSTR   28
P0.7/VREF   27

P1.0   26
P1.1   25
P1.2   24 N/C
P1.3   23
P1.4   22 N/C
P1.5   21
P1.6   20 N/C
P1.7   19 N/C

P1.5/SCLK/RTS
P1.7/SO/MISO/UART_RD
P1.6/SI/MOSI/UART_TD
P1.4/CSn/SS/CTS

R306
R_0603

R305
R_0_0603

D+
D-

VBUS

RES_N

R309
R_4K7_0402_G

RESET

N/C 18 P2.0
N/C 17 P2.1
N/C 16 P2.2
     15 P2.3
N/C 14 P2.4
N/C 13 P2.5
     12 P2.6
N/C 11 P2.7
     10 P3.0/C2D

Q300
BC846

R310
R_43K_0402_F

P5
PINROW_2X3

P2.1/DD
P2.2/DC
RESET_N

RES_N

Jumpers between pin 1 & 2 and
between 3 & 4 shall both be
removed when using the SOC debug
connector.
Jumper between 5 & 6 can be
removed when performing power
down current measurements.

VDD USB MCU

R300
R_1K0_0402_J

R301
R_1K0_0402_J

USB MCU Debug connector

VDD

P4
PINROW_2X5

R302
R_1K0_0402_J

USB_MCU_DEBUGRESET
USBMCU_RESET

N/C

R304
R_0603

P0.2/EE_SDA
P0.3/EE_SCL

R303
R_0603

The 8051 MCU can be manually reset by
connecting a jumper between pin 7 & 9