Proiect VLSI

**Grecu Andrei-George, 342C1**

1. Acumulator A
   1. A screenshot of a computer

      Description automatically generated with medium confidenceRealizare schema DSCH
   2. A screenshot of a computer

      Description automatically generated with medium confidenceFunctionare
   3. Graphical user interface, application

      Description automatically generatedCod Verilog
   4. Microwind cu capacitor pe iesire
   5. Simulare Microwind
2. Acumulator B
   1. A screenshot of a computer

      Description automatically generated with medium confidenceRealizare schema DSCH
   2. A picture containing background pattern

      Description automatically generatedFunctionare (Pe input a fost 6, dupa am schimbat in 0)
   3. Graphical user interface, text

      Description automatically generatedCod Verilog
   4. Microwind cu capacitor pe iesire
   5. Simulare Microwind
3. Add/Substract Block
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

Graphical user interface, text, application

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Input Register
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

A screenshot of a computer

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Output Register
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

A screenshot of a computer

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Phase Generator
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

A screenshot of a computer

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Program Counter 0 to 15
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

A screenshot of a computer

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Instruction Register
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Microinstruction Controller
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Cod Verilog

Graphical user interface, application

Description automatically generated

Graphical user interface, application

Description automatically generated

Graphical user interface, application

Description automatically generated

Graphical user interface, application

Description automatically generated

* 1. Microwind cu capacitor pe iesire
  2. Simulare Microwind

1. Microprocesor
   1. Realizare schema DSCH

A screenshot of a computer

Description automatically generated with medium confidence

* 1. Functionare
  2. Cod Verilog
  3. Microwind cu capacitor pe iesire
  4. Simulare Microwind