



## PLC Stamp mini 2 Datasheet

I2SE GmbH

October 26, 2017

## Contents

<b>1</b>	<b>Revisions</b>	<b>3</b>
<b>2</b>	<b>Abstract</b>	<b>3</b>
<b>3</b>	<b>Applications</b>	<b>3</b>
<b>4</b>	<b>Interfaces</b>	<b>4</b>
<b>5</b>	<b>Handling</b>	<b>4</b>
<b>6</b>	<b>Module Overview</b>	<b>4</b>
<b>7</b>	<b>Technical Data</b>	<b>5</b>
7.1	Absolute Maximum Ratings . . . . .	5
7.2	Operating Conditions . . . . .	5
7.3	Safety . . . . .	5
<b>8</b>	<b>Firmware and MAC Addresses</b>	<b>5</b>
<b>9</b>	<b>Module Pinout</b>	<b>6</b>
9.1	GPIO . . . . .	6
9.1.1	Power-on Configuration . . . . .	6
9.1.2	General Purpose I/O Functions . . . . .	7
9.2	Serial Signals . . . . .	7
9.2.1	UART . . . . .	8
9.2.2	SPI . . . . .	8
<b>10</b>	<b>Recommended Footprint</b>	<b>8</b>
<b>11</b>	<b>Getting Started</b>	<b>10</b>
11.1	CE Class B / North American Variant . . . . .	10
11.2	Automotive EVSE / PEV . . . . .	10
<b>12</b>	<b>Processing</b>	<b>10</b>
<b>13</b>	<b>Module Marking</b>	<b>11</b>
<b>14</b>	<b>Order Information</b>	<b>12</b>
<b>15</b>	<b>Package Materials Information</b>	<b>13</b>
15.1	Tape and Reel . . . . .	13
15.1.1	Orientation of the Module . . . . .	14
15.2	Tape and Reel Cardboard Box Dimensions . . . . .	14
<b>16</b>	<b>Contact</b>	<b>15</b>

## 1 Revisions

Revision	Release Date	Changes
10	October 26, 2017	corrected QCA7005 order codes (some incorrectly had order code I2PLCAMN-... instead of I2PLCBMN-...)
9	September 05, 2017	added section "processing"
8	June 13, 2017	updated section "GPIO", corrected error in meaning of GPIO levels
7	February 1, 2017	added Package Materials Information and order options, adding info about difference in QCA7000 and QCA7005
6	August 08, 2016	added safety related info: test voltage, creepage, clearance
5	March 21, 2016	added GPIO output current limit, add all GPIO3 functions and timings
4	February 24, 2016	fixing GPIO function assignment
3	February 9, 2016	clarifying UART settings
2	January 25, 2016	adding default UART settings
1	November 16, 2015	initial issue

## 2 Abstract

The PLC (PowerLine Communication) module gives your application access to powerline communication based on the HomePlug® Green PHY™ Chip QCA7000 / QCA7005. You can realize point-to-point and multi-point connections depending on your application. The data will be transmitted as Ethernet packets over the power line. This gives you the opportunity to use TCP/IP or whatever network protocols you wish to use.

This module includes the needed galvanic isolation between the power line and the secondary signals. You can freely design the power supply external to the module so that it perfectly meets the requirements for your application. The QCA7000 / QCA7005 by Qualcomm Atheros ensures compatibility with many other commercial powerline devices.

The main difference between the QCA7000 and QCA7005 is the chip package. The QCA7000 has a traditional QFN package, whilst the QCA7005 has a modified QFN for better optical inspection that is intended for the use in automotive applications with highest requirements on quality. The default option for these modules is the QCA7000.

Parameter	Value
Power supply	3.3 V
Power consumption	0.5 W
Data rate	max. 10 MBit/s
Reach	maximal 300 m over the Powerline
Temperature range	-40 °C - 85 °C (industrial) / 0 °C - 70 °C (commercial)
Outline dimension	43.5 mm x 22 mm x 6.5 mm
Weight	5.6 g
RoHS	PLC Stamp mini 2 is manufactured in compliance with RoHS

## 3 Applications

- interconnection of household appliances to the Smart Grid
- connection of smart meters to Smart Meter Gateways and/or LAN/WAN/WiFi
- connection of sensors
- connection of photovoltaic equipment
- connection of heating and air conditioning system
- coupling of machines and measurement devices
- forwarding of digital signals (remote I/O)
- coupling of RF cells for home automation

## 4 Interfaces

Powerline: 230 V AC, 110 V AC, DC, dead-wire 2-wire-connections

Serial interfaces: UART or SPI (order option)

## 5 Handling



This electronic component is sensitive to electrostatic discharge (ESD).

The module contains components with **moisture sensitivity level (MSL) 3**. Please handle them accordingly.

## 6 Module Overview

The block diagram in Figure 1 shows module components in the gray box as well as the connections and external components that you need in addition.

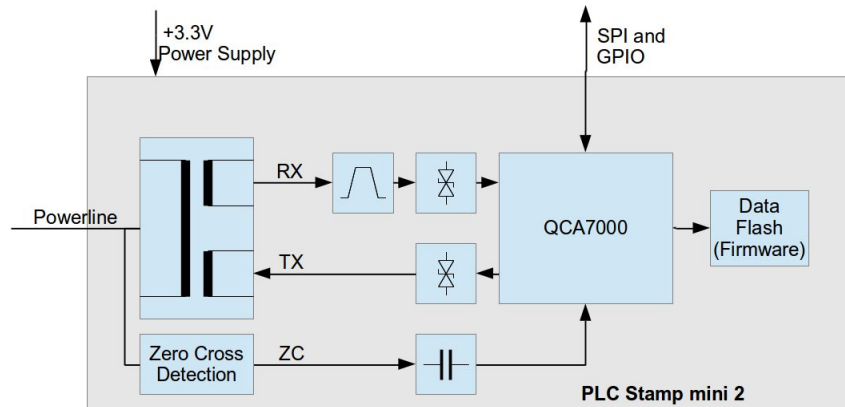


Figure 1: Block Diagram of PLC Stamp mini 2

Parts of the module that are not related to insulation are located below a metal shield. Information about the module are printed on a high-temperature label on top of this shield.

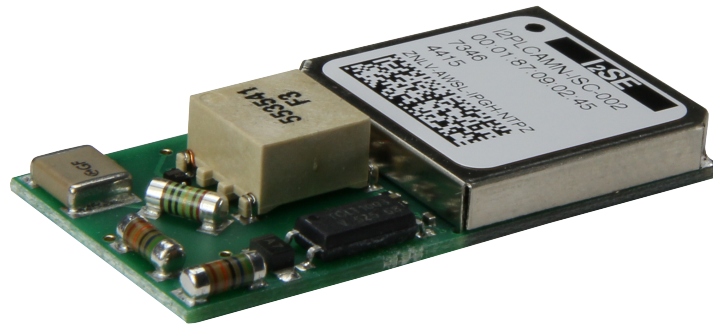


Figure 2: Image of the Module

## 7 Technical Data

### 7.1 Absolute Maximum Ratings

SYMBOL	PARAMETER	Min.	Max.	UNIT
VDD	Digital supply voltage	-0.3	3.46	V
VDIO	Digital input voltage	-0.3	3.63	V
TSTORE	Storage temperature	-40	150	°C
RAH	Relative air humidity (not condensing)	10	90	%

### 7.2 Operating Conditions

SYMBOL	PARAMETER	Min.	Typ.	Max.	UNIT
VDD	Digital supply voltage	3.13	3.3	3.46	V
IDD	Current for VDD	-	150 (average)	300	mA
VAC	Mains Voltage (AC)	85	110 / 230	250	V
VDC (1)	Mains Voltage (DC)	-	-	380	V
fAC (2)	Mains Frequency Tolerance (50Hz)	48.5	50	51.5	Hz
	Mains Frequency Tolerance (60Hz)	57.9	60	62.1	Hz
TCASE	Top of case temperature (industrial)	-40	-	85	°C
	Top of case temperature (iommmercial)	0	-	70	°C
I_GPIO	GPIO current	-	-	12	mA

Notes:

1. DC Voltage is specified as the sum of all AC and DC parts of the line.
2. If you couple PLC Stamp Mini 2 to a DC Line make sure to tie the ZC.IN pin to low via a 10 kOhm resistor.

### 7.3 Safety

Isolation between L/N terminals and all other connections: 8 mm creepage / 6.5 mm clearance

Isolation test voltage: 4000 Vac

## 8 Firmware and MAC Addresses

These modules are pre-programmed with firmware and parameter information block (PIB). The PIB contains the MAC addresses of the module, prescaler values defining the output power as well as settings for automotive use of the QCA7000 / QCA7005 (SLAC). The MAC address uses a prefix (organizationally unique identifier) that is assigned to I2SE. The prescaler values that are set in the production process were defined by Qualcomm for the intended application / market (see order option Parameter Optimization). Automotive variants include settings to enable SLAC for either the EVSE or the PEV side.

## 9 Module Pinout

PIN	Direction	Name	Description
1	IN/OUT	GPIO_0	QCA7000 / QCA7005 GPIO 0
2	IN/OUT	GPIO_1	QCA7000 / QCA7005 GPIO 1
3	IN/OUT	GPIO_2	QCA7000 / QCA7005 GPIO 2
4	IN/OUT	GPIO_3	QCA7000 / QCA7005 GPIO 3
5	IN	RESET_L	Reset (low active)
6	IN/OUT	SERIAL_4	Serial_4 of QCA7000 / QCA7005
7	IN/OUT	SERIAL_3	Serial_3 of QCA7000 / QCA7005
8	IN/OUT	SERIAL_2	Serial_2 of QCA7000 / QCA7005
9	IN/OUT	SERIAL_1	Serial_1 of QCA7000 / QCA7005
10	IN/OUT	SERIAL_0	Serial_0 of QCA7000 / QCA7005
11	SUPPLY	GND	Ground connection
12	SUPPLY	VDD	Supply Voltage for the module
13	IN	N.C. / ZC.IN	do not connect (home automation variant), zero cross detection input (automotive variant)
14	-	-	not available
15	IN/OUT	N	Powerline communication
16	IN/OUT	L	Powerline communication
17	IN/OUT	L	Powerline communication
18*	SUPPLY	GND	Ground connection
19*	SUPPLY	GND	Ground connection
20*	SUPPLY	GND	Ground connection
21*	SUPPLY	GND	Ground connection
22*	SUPPLY	GND	Ground connection
23*	SUPPLY	GND	Ground connection
24*	SUPPLY	GND	Ground connection
25*	SUPPLY	GND	Ground connection

\* not needed for electrical function, only for SMD assembly

### 9.1 GPIO

#### 9.1.1 Power-on Configuration

The QCA7000 / QCA7005 comprises four GPIO pins which are read at boot time to get the desired configuration. Table 7 shows the configuration the QCA7000 / QCA7005 at boot time.

GPIO #	Function	Pull Up	Pull Down	Preload on Module
0	Boot Source	Flash	Host	-
1	Host Interface	-	SPI Slave	10 kOhm Pull Down
2	SPI Slave Mode	Burst	Legacy	-
3	None	-	-	-

Table 7: QCA7000 / QCA7005 Boot Strap Options

### 9.1.2 General Purpose I/O Functions

The GPIOs of the QCA7000 / QCA7005 have different functions after booting. They can either be used as input or output to display various states or trigger some actions. It is not possible to use these pins from your own application - only the QCA7000 / QCA7005 firmware can control these GPIOs.

The GPIOs are set up as noted in Table 9.

GPIO #	Direction	Function
0	Output	PLC connection (1=connection established, 0=no connection)
1	Output	Pushbutton Simple Connect (toggling 1/0 with 1Hz: simple connect mode active, 0: not in simple connect mode)
2	Output	unused in default configuration
3	Input	Pushbutton Simple Connect (hold time: 0.5 to 3s), NMK randomize (hold time: 5 to 8s), Factory defaults (hold time: 10 to 15s)

Table 9: QCA7000 / QCA7005 GPIO Settings

Since the GPIO are also used as bootstrap pins special attention should be paid to the LED (Figure 3) and switch (Figure 4) connection according to the strap direction. Behaviour in Table 9 states a '1' for a pressed button or a lit LED, the electrical state (high/low) depends on the direction of the bootstrap resistor.

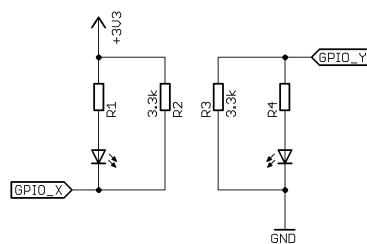


Figure 3: GPIO LED Bootstrap

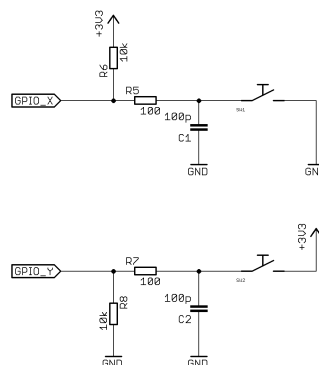


Figure 4: GPIO Switch Bootstrap

## 9.2 Serial Signals

The signals SERIAL\_0 to SERIAL\_4 represent the SPI or UART signals. Table 11 contains the UART and SPI function for each signal.

Please note that the selection between SPI and UART mode is effected using different QCA7000 / QCA7005 firmware and is therefore an order option.

Signal Name	SPI function	UART function
SERIAL_0	Interrupt	
SERIAL_1	CLK	RTS
SERIAL_2	CS	CTS
SERIAL_3	MISO	TXD
SERIAL_4	MOSI	RXD

Table 11: QCA7000 / QCA7005 UART/SPI Signals

### 9.2.1 UART

All module variants in UART mode use the settings in Table 13. Pins RTS and CTS are not used.

Setting	Value
Baud Rate	115200
Data Bits	8
Parity	None
Stop Bits	1
Flow Control	None

Table 13: UART Settings

### 9.2.2 SPI

The QCA7000 / QCA7005 uses SPI in mode 3: CPOL=1, CPHA=1.

SPI should be used in burst mode, meaning that the Chip Select signal is kept low during a complete SPI message. The SPI CLK period should not be less than 83.3 ns resulting in a maximum clock frequency of 12 MHz.

## 10 Recommended Footprint

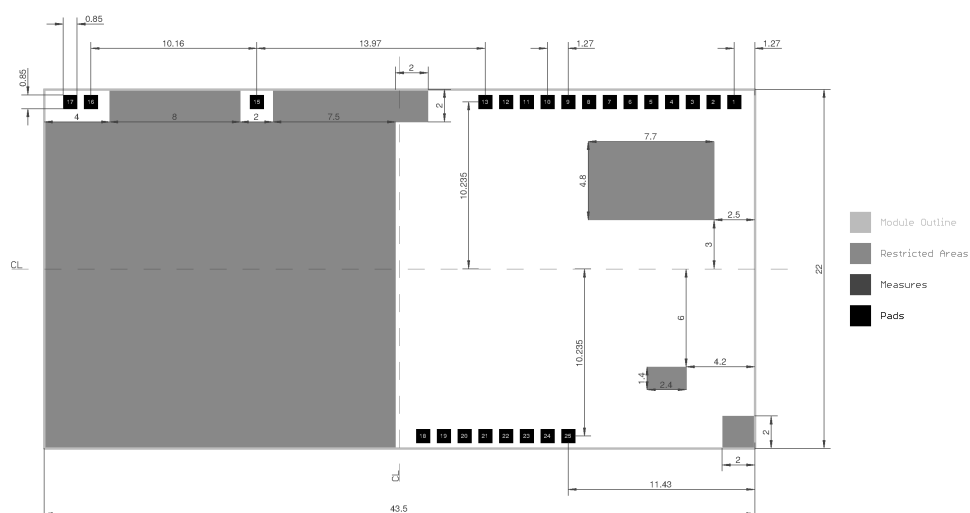


Figure 5: Recommended Footprint

Notes:

1. All dimensions are in mm.



2. Pads are all of the same size.
3. Distances between pads are equal if not otherwise specified in the drawing.
4. Pad 14 is not available for compatibility to the previous footprint of PLC Stamp mini
5. The drawing in Figure 5 shows the top view on the footprint (as if you look through the module).
6. Restricted areas should be kept free of copper on the base PCB.
7. The module outline shows the ideal measures - tolerance is not included.

## 11 Getting Started

The module does not need other parts for PLC connection. You need to connect it to:

1. 3.3 V supply
2. your processor (via SERIAL\_0 through SERIAL\_4)
3. to L and N of your mains circuit or to GND and CP for automotive uses (see order variants)

Refer to ?? to see how the module needs to be connected.

The following two sections detail the difference between mains connected and automotive applications.

### 11.1 CE Class B / North American Variant

The PLC Transformer used for these variants has a turns ratio of 1:4:5 and the Zero Cross Detection circuit is present, which is necessary for Powerline communication over the mains. Let the zero cross detection (Pin 13) floating.

The GPIO pins, the reset pin and the ZC pin do not need to be connected.

### 11.2 Automotive EVSE / PEV

The PLC Transformer has a turns ratio of 1:1:1.

These variants are not equipped with a Zero Cross Detection circuit. Some devices shall be capable to detect the zero cross of the AC line to support coexistence functionality. In this case refer to Figure 6. Connect signals L and N of Figure 6 to mains neutral and line and ZC.IN to pin 13 of the module. Usually the EVSE should have the zero cross detection while PEV naturally do not need it.

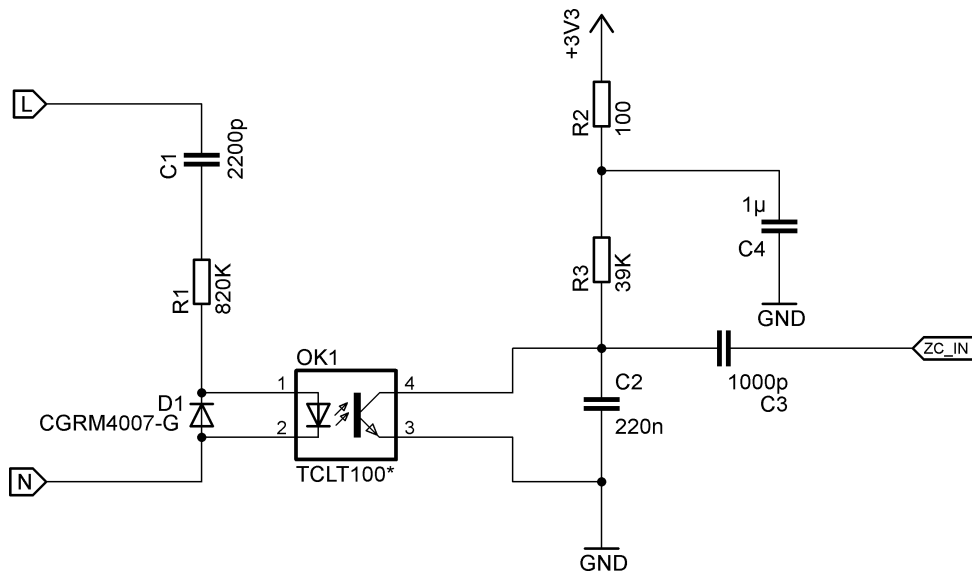


Figure 6: Schematic Zerocross Detection

Automotive variants of PLC Stamp mini 2 are not designed to work on mains. Connect pin 16 and 17 of the module to GND and use pin 15 for the powerline communication. Do not connect pin 15-17 to the AC line.

The GPIO pins, and the reset pin do not need to be connected.

## 12 Processing

- Process the modules according to IPC/JEDEC J-STD-020 and J-STD-033 guidelines.
- Limit repeated reflow processes to maximum 2.

## 13 Module Marking

Each Module is marked with a label containing the following data:

1. Order Code
2. QCA7000 / QCA7005 MAC Address (printed with a separating colon every two digits, the DataMatrix code contains the digits without separator)
3. Serial Number
4. Production Date Code: WWYY
5. Device Security Key
6. 2D DataMatrix code containing the above information as a list of space separated values

Additionally each label contains:

1. I2SE Banner
2. Pin 1 Marking

An example is shown in Figure 7.



Figure 7: Example Label for PLC Stamp Mini 2

## 14 Order Information

The following table provides an overview of the available PLC Stamp mini 2 variants.

Order code	Chip	Temperature Range / °C	Serial Interface	Application	PLC Transformer	Zero Cross Detection	Packaging	Availability
I2PLCAMN-ISC-002-T	QCA7000	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	standard
I2PLCAMN-ISC-002-R	QCA7000	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	standard
I2PLCAMN-ISN-002-T	QCA7000	-40 - 85	SPI	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-ISN-002-R	QCA7000	-40 - 85	SPI	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-ISE-002-T	QCA7000	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-ISE-002-R	QCA7000	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-ISP-002-T	QCA7000	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-ISP-002-R	QCA7000	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-IUC-002-T	QCA7000	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-IUC-002-R	QCA7000	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-IUN-002-T	QCA7000	-40 - 85	UART	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-IUN-002-R	QCA7000	-40 - 85	UART	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-IUE-002-T	QCA7000	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-IUE-002-R	QCA7000	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-IUP-002-T	QCA7000	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-IUP-002-R	QCA7000	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-IUC-002-T	QCA7000	0 - 70	SPI	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-CSC-002-R	QCA7000	0 - 70	SPI	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-CSN-002-T	QCA7000	0 - 70	SPI	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-CSN-002-R	QCA7000	0 - 70	SPI	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-CSE-002-T	QCA7000	0 - 70	SPI	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-CSE-002-R	QCA7000	0 - 70	SPI	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-CSP-002-T	QCA7000	0 - 70	SPI	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-CSP-002-R	QCA7000	0 - 70	SPI	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-CUC-002-T	QCA7000	0 - 70	UART	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-CUC-002-R	QCA7000	0 - 70	UART	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-CUN-002-T	QCA7000	0 - 70	UART	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCAMN-CUN-002-R	QCA7000	0 - 70	UART	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCAMN-CUE-002-T	QCA7000	0 - 70	UART	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-CUE-002-R	QCA7000	0 - 70	UART	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCAMN-CUP-002-T	QCA7000	0 - 70	UART	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCAMN-CUP-002-R	QCA7000	0 - 70	UART	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCBMN-ISC-002-T	QCA7005	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCBMN-ISC-002-R	QCA7005	-40 - 85	SPI	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCBMN-ISN-002-T	QCA7005	-40 - 85	SPI	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCBMN-ISN-002-R	QCA7005	-40 - 85	SPI	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCBMN-ISE-002-T	QCA7005	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCBMN-ISE-002-R	QCA7005	-40 - 85	SPI	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCBMN-ISP-002-T	QCA7005	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCBMN-ISP-002-R	QCA7005	-40 - 85	SPI	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCBMN-IUC-002-T	QCA7005	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCBMN-IUC-002-R	QCA7005	-40 - 85	UART	CE Class B	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCBMN-IUN-002-T	QCA7005	-40 - 85	UART	North America	I2PLCTR-1	on module	Tray, 20pcs	on request
I2PLCBMN-IUN-002-R	QCA7005	-40 - 85	UART	North America	I2PLCTR-1	on module	T/R, 240pcs	on request
I2PLCBMN-IUE-002-T	QCA7005	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCBMN-IUE-002-R	QCA7005	-40 - 85	UART	Automotive EVSE	I2PLCTR-2	external	T/R, 240pcs	on request
I2PLCBMN-IUP-002-T	QCA7005	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	Tray, 20pcs	on request
I2PLCBMN-IUP-002-R	QCA7005	-40 - 85	UART	Automotive PEV	I2PLCTR-2	external	T/R, 240pcs	on request

Table 15: PLC Stamp mini 2 Order Codes

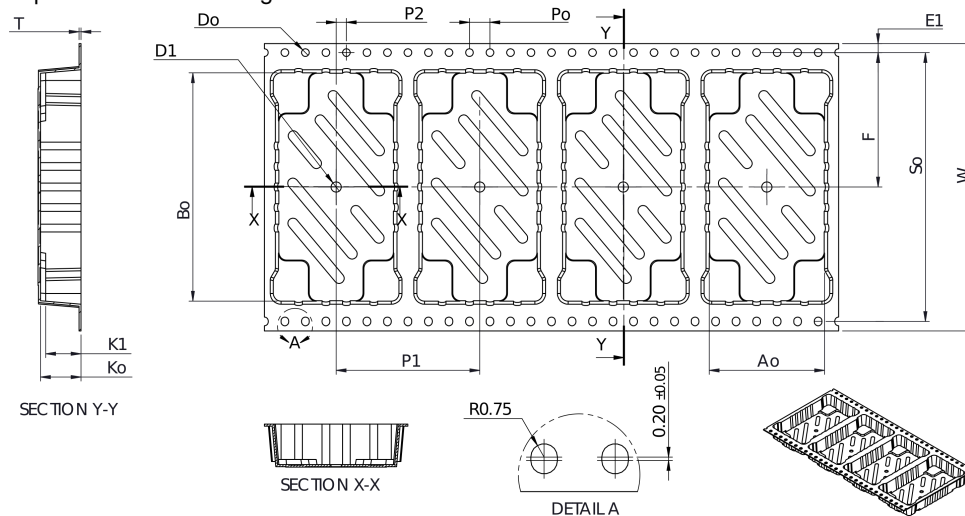
Product Family Code	Chip		Temperature Range	Serial Interface	Parameter Optimization	Version	Packaging
I2PLC	A: QCA7000	MN-	I: Industrial (-40 - 85 °C)	S: SPI	C: CE Class B	-002	-T: Tray, 20pcs
	B: QCA7005		C: Commercial (0 - 70 °C) - only for QCA7000	U: UART	N: North America		-R: Tape and Reel, 240pcs
					E: Automotive EVSE		
					P: Automotive PEV		

Table 17: PLC Stamp mini 2 Order Code Compilation

## 15 Package Materials Information

### 15.1 Tape and Reel

Tape and Reel according to EIA-481



Ao	22,5 ± 0,2
Bo	44,5 ± 0,2
D1	Ø2,0 MIN
Do	Ø1,5 + 0,1
E1	1,75 ± 0,10
F (III)	26,20 ± 0,15
Ko	7,9 ± 0,2
K1	6,9 ± 0,2
P1	28,00 ± 0,15
P2 (I)	2,00 ± 0,15
Po (II)	4,00 ± 0,15
So	52,40 ± 0,15
T	0,40 ± 0,04
W	56,0 ± 0,3

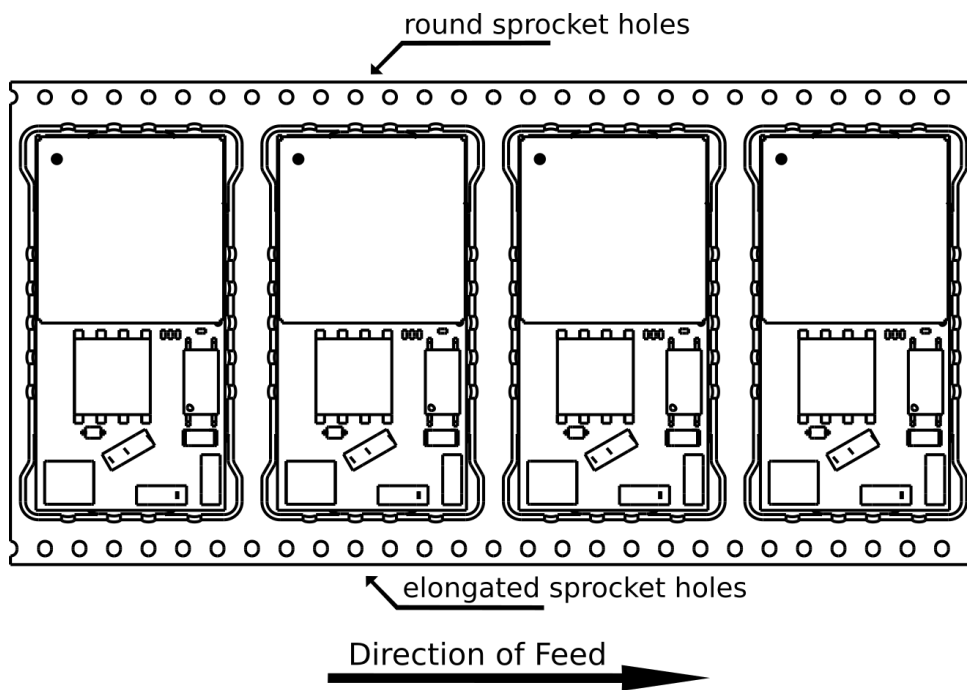
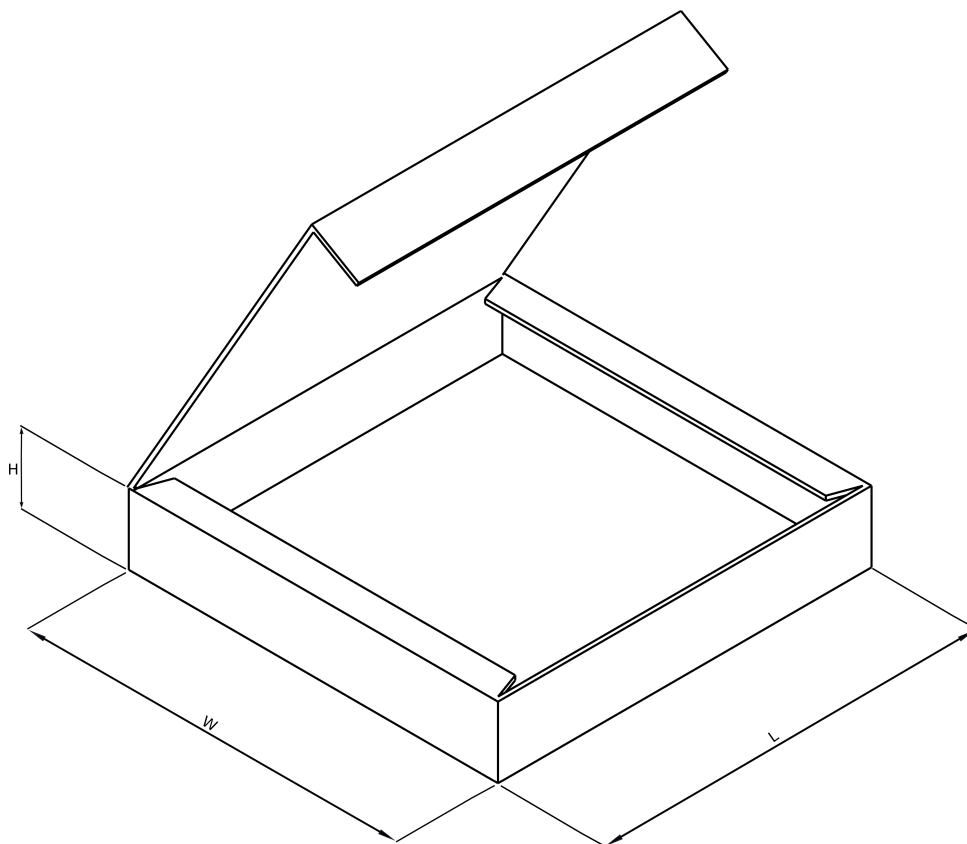
All dimensions in millimeters unless otherwise stated.

Material: Polystyrene

(I)	Measured from centreline of sprocket hole to centreline of pocket
(II)	Cumulative tolerance of 10 sprocket holes is ± 0,20
(III)	Measured from centreline of sprocket hole to centreline of sprocket

#### Reel Dimensions

Reel inner Diameter	4"
Reel outer Diameter	13"
Reel inner Width (Measured at hub)	56.4 + 2.0
Reel outer Width (Measured at hub)	62.4 MAX
Parts/Carrier Tape-Reel	240
Tape leader empty cavities	7..12
Tape trailer empty cavities	7..12

**15.1.1 Orientation of the Module****15.2 Tape and Reel Cardboard Box Dimensions**

H	65 mm
W	340 mm
L	340 mm

## 16 Contact

Website: <http://www.i2se.com>

I2SE GmbH

Friedrich-Ebert-Str. 61

04109 Leipzig

Germany