Statuts of Timing and Data Resilience and Management in WR Networs

	Protocol / Technology	Software	Hardware	WR Device	Soft. Develop.	HW Develop.
	VLAN	>	>	Switch/MMN	100 %	% 08
WR Data	GVRP	>	>	Switch/MMN	Open Implementation, TBT	%0
	FEC	>	>	Switch/MMN/Node	30 %	30 %
	STP	>	>	Switch/MMN	Kernel Support, TBT	% 0
	Link Aggregation	>	>	Switch/MMN	Linux Support, TBT	% 0
	PTP	<u> </u>	>	Switch/MMN/Node $ $	% 0	% 0
WR Timing	GVRP	-	<u> </u>	Switch/MMN	% 0	%0
	Traffic Monitor	<u> </u>	<u> </u>	Switch	SFlow, TBT	% 0
Resilience & Management	Flow Control	>	>	Switch/MMN/Node	0 %, TBT	%0
	Congestion Control	>	>	Switch/MMN	% 0	% 0
	LLDP	>	I	Switch/MMN	LLDPd, TBT	I
	SNMP	>	>	Switch/MMN/Node	SNMPd,TBT	I

 $\text{MMN} \to \!\! \text{Master Management Node TBT} \to \!\! \text{To Be Tested}$