

Test system

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- 2 SPEC cards with 2 DDS FMC mezzanines.
- Point-to-point link.
- Test frequency: 10 MHz cesium standard, loop bandwidth: 10 kHz, broadcast rate: 110 kHz.
- Encoder PLL performance: 6 ps rms jitter (10 Hz – 5 MHz), -110 dBc/Hz noise floor (@ 100 Hz)
- Recovered clock: 8.5 ps rms jitter.
- Significant part of the jitter likely comes from buggy PCB design (1st prototype)