References for ”Recovering Energy from Obsolete Data in Complex Reversible Systems” Poster

Byron Gregg, *Member, IEEE*

REFERENCES

[1] E. F. Fredkin and T. Toffoli, “Design Principles for Achieving High-Performance Submicron Digital Technologies,” in *Collision-based Computing*. Springer, Salmon Tower Building, New York, New York, USA, 2002, pp. 27–46.

[2] D. Clerk, “The theory of the gas engine.(including appendix).” in *Minutes of the Proceedings of the Institution of Civil Engineers*, vol. 69, no. 1882. Thomas Telford-ICE Virtual Library, 1882, pp. 220–250.

[3] R. Landauer, “Irreversibility and Heat Generation in the Computing Process,” *IBM Journal of Research and Development*, vol. 5, no. 3, pp. 183–191, 1961.

[4] C. H. Bennett, “Logical Reversibility of Computation,” *IBM Journal of Research and Development*, vol. 17, no. 6, pp. 525–532, 1973.

[5] J. S. Hall, “An Electroid Switching Model for Reversible Computer Architectures,” in *Physics of Computation Workshop*. Dallas Texas: Citeseer, Oct 1992.

[6] S. Younis and T. Knight, “Asymptotically Zero Energy Split-Level Charge Recovery Logic,” in *Int. Wkshp. on Low Power Design*, 1994, pp. 177–182.

[7] A. Barenco, C. H. Bennett, R. Cleve, D. P. DiVincenzo, N. Margolus, P. Shor, T. Sleator, J. A. Smolin, and H. Weinfurter, “Elementary gates for quantum computation,” *Physical review A*, vol. 52, no. 5, p. 3457, 1995.

[8] C. J. Vieri, “Reversible Computer Engineering and Architecture,” Ph.D. dissertation, Massachusetts Institute of Technology, 1999.

[9] M. P. Frank, R. W. Brocato, B. D. Tierney, N. A. Missert, and A. H. Hsia, “Reversible Computing with Fast, Fully Static, Fully Adiabatic CMOS,” in *2020 International Conference on Rebooting Computing (ICRC)*. IEEE, Piscataway, New Jersey, USA, 2020, pp. 1–8.

[10] T. Austin, D. Burger, M. Franklin, S. Breach, and K. Skadron. (1998) The simplescalar architectural research tool set, version 2.0. [Online]. Available: https://pages.cs.wisc.edu/ mscalar/simplescalar.html

[11] Y. Shen and M. Ferdman, “Temporal stream branch predictor,” in *Championship Branch Prediction Program*, June 2014.

B. Gregg is a Ph.D. student of the Department of Electrical and Computer Engineering, Portland State University, Portland, OR, 97201 USA e-mail: greggbyr@pdx.edu.