**Sinister Transistor**

**Detailed Design**

**COP 4331, Spring 2016**

Team Name: The Mega Bytes

Team Members:

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Modification history:

|  |  |  |  |
| --- | --- | --- | --- |
| Version | Date | Who | Comment |
| v0.0 | 03/02/16 | Greg | Template |
| v1.0 | <date here> | <who> | <put comment to summarize the changes made in this version> |
| ... |  |  |  |

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Design Issues

<In this section, provide more detail to the design issues discussed in the high-level design. Document the results of each design prototype. Document design decisions and associated risks. Provide sufficient detail so the maintainer won't have to say "why did they do it this way?">

Detailed Design Information

<In this section, you include diagrams, listings, etc. to provide the complete detail of the design of your product. A class diagram is required. Depending on the tools or development environments you are using, you may be able to automatically generate much or all of this section.>

<NOTE: The design documentation must be well organized, modular, and of sufficient detail so that, using only the concept of operations, SRS, and design documentation (and NOT the code):

* the technical reviewer or software maintainer can fully and easily understand the structure, function, and non-behavioral characteristics of the software
* the coder can develop the final product from the design with NO decisions to make other than syntax
* the maintainer can safely determine how to modify or enhance the code and know what other sections of code may be impacted by the change>

Trace of Requirements to Design:

<In this section, provide a trace of each requirement in your SRS to the design: for each requirement, where (in the design) is its implementation?>