

dynamixelRegisters

| Register name | Hexadecimal | Decimal(after binary bit-shift) | Binary bit-shift |
|-------------------------------|-------------|---------------------------------|------------------|
| REG_MODEL_NUMBER_L | 0x8000 | 0 | 0 |
| REG_MODEL_NUMBER_H | 0x8001 | 1 | 1 |
| REG_FIRMWARE_VERSION | 0x8002 | 2 | 10 |
| REG_ID | 0x8003 | 3 | 11 |
| REG_BAUDRATE | 0x8004 | 4 | 100 |
| REG_RETURN_DELAY_TIME | 0x8005 | 5 | 101 |
| REG_CW_ANGLE_LIMIT_L | 0x8006 | 6 | 110 |
| REG_CW_ANGLE_LIMIT_H | 0x8007 | 7 | 111 |
| REG_CCW_ANGLE_LIMIT_L | 0x8008 | 8 | 1000 |
| REG_CCW_ANGLE_LIMIT_H | 0x8009 | 9 | 1001 |
| REG_HIGHEST_LIMIT_TEMPERATURE | 0x800B | 11 | 1010 |
| REG_LOWEST_LIMIT_VOLTAGE | 0x800C | 12 | 1100 |
| REG_HIGHEST_LIMIT_VOLTAGE | 0x800D | 13 | 1101 |
| REG_MAX_TORQUE_L | 0x800E | 14 | 1110 |
| REG_MAX_TORQUE_H | 0x800F | 15 | 1111 |
| REG_SATATUS_RETURN_LEVEL | 0x8010 | 16 | 10000 |
| REG_ALARM_LED | 0x8011 | 17 | 10001 |
| REG_ALARM_SHUTDOWN | 0x8012 | 18 | 10010 |
| REG_TORQUE_ENABLE | 0x8019 | 24 | 10011 |
| REG_LED | 0x8020 | 25 | 10100 |
| REG_CW_COMPLIANCE_MARGIN | 0x881A | 26 | 10101 |
| REG_CCW_COMPLIANCE_MARGIN | 0x881B | 27 | 10110 |
| REG_CW_COMPLIANCE_SLOPE | 0x881C | 28 | 10111 |
| REG_CCW_COMPLIANCE_SLOPE | 0x801D | 29 | 11000 |
| REG_GOAL_POSITION_L | 0x801E | 30 | 11001 |
| REG_GOAL_POSITION_H | 0x801F | 31 | 11010 |
| REG_MOVING_SPEED_L | 0x8021 | 32 | 11011 |
| REG_MOVING_SPEED_H | 0x8022 | 33 | 11100 |
| REG_TORQUE_LIMIT_L | 0x8023 | 34 | 11101 |
| REG_TORQUE_LIMIT_H | 0x8024 | 35 | 11110 |
| REG_PRESENT_POSITION_L | 0x8025 | 36 | 11111 |
| REG_PRESENT_POSITION_H | 0x8026 | 37 | 100000 |
| REG_PRESENT_SPEED_L | 0x8027 | 38 | 100001 |
| REG_PRESENT_SPEED_H | 0x8028 | 39 | 100010 |
| REG_PRESENT_LOAD_L | 0x8029 | 40 | 100011 |
| REG_PRESENT_LOAD_H | 0x8030 | 41 | 100100 |
| REG_PRESENT_VOLTAGE | 0x802A | 42 | 100101 |
| REG_PRESENT_TEMPERATURE | 0x802B | 43 | 100110 |
| REG_REGISTERED | 0x802C | 44 | 100111 |
| REG_MOVING | 0x802E | 46 | 101000 |
| REG_LOCK | 0x802F | 47 | 101010 |
| REG_PUNCH_L | 0x8030 | 48 | 101011 |
| REG_PUNCH_H | 0x8031 | 49 | 101100 |