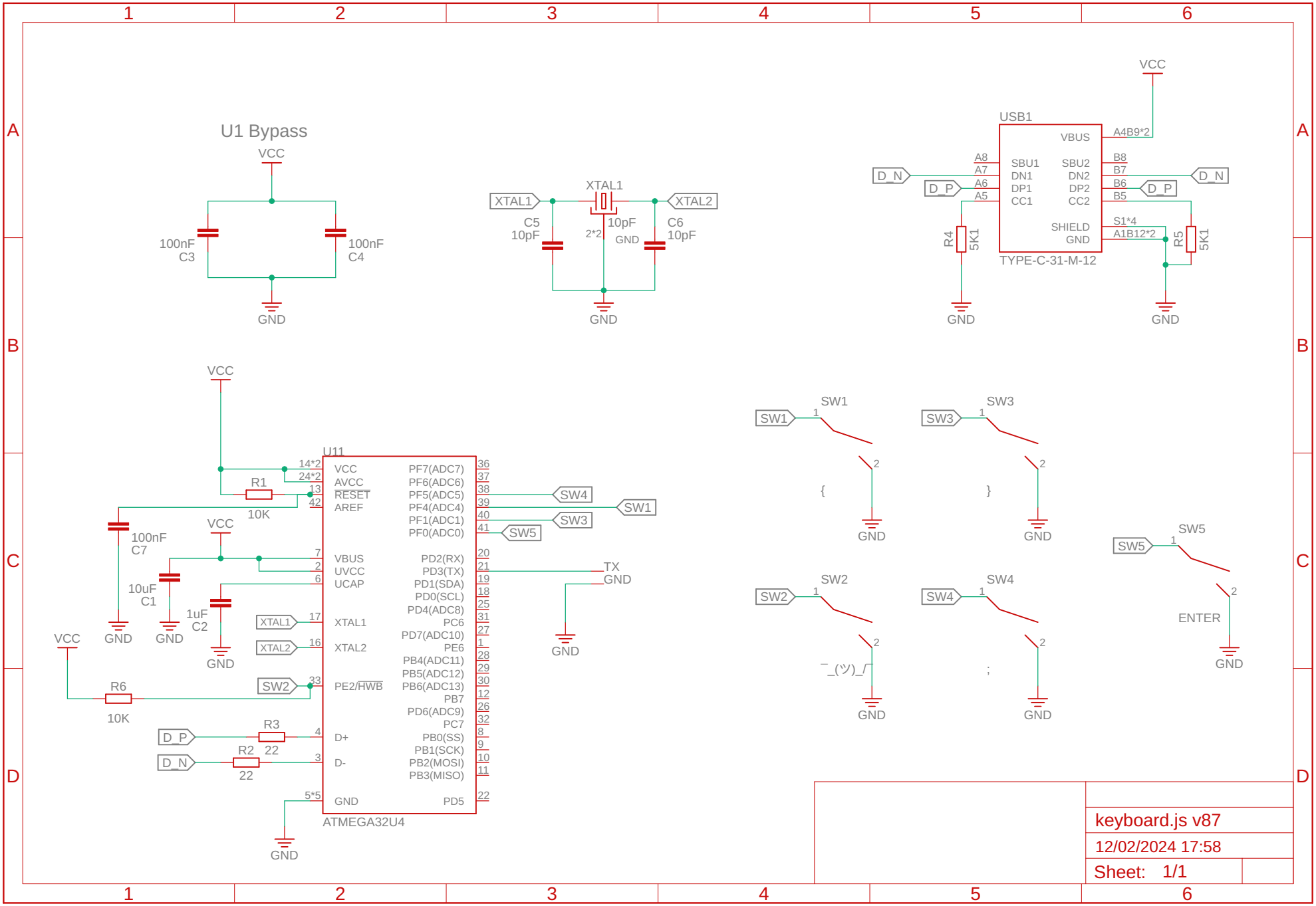
U1 Bypass
VCC
100nF
C3
100nF
C4
GND
XTAL1
XTAL2
C5
10pF
10pF
2*2
GND
C6
10pF
GND
USB1
VCC
VBUS
A4B9*2
A8
A7
A6
A5
SBU1
DN1
DP1
CC1
SBU2
DN2
DP2
CC2
B8
B7
B6
B5
D_N
D_P
SHIELD
GND
S1*4
A1B12*2
R4
5K1
R5
5K1
TYPE-C-31-M-12
GND
GND
VCC
U11
14*2
24*2
13
42
VCC
AVCC
RESET
AREF
R1
10K
100nF
C7
VCC
7
2
6
VBUS
UVCC
UCAP
10uF
C1
1uF
C2
GND
GND
GND
XTAL1
17
XTAL2
16
XTAL1
XTAL2
VCC
R6
10K
SW2
33
PE2/HWB
D_P
R3
4
D+
D_N
R2
22
3
D-
22
5*5
GND
GND
ATMEGA32U4
PF7(ADC7)
PF6(ADC6)
PF5(ADC5)
PF4(ADC4)
PF1(ADC1)
PF0(ADC0)
36
37
38
39
40
41
SW4
SW1
SW3
SW5
PD2(RX)
PD3(TX)
PD1(SDA)
PD0(SCL)
PD4(ADC8)
PC6
PD7(ADC10)
PE6
PB4(ADC11)
PB5(ADC12)
PB6(ADC13)
PB7
PD6(ADC9)
PC7
PB0(SS)
PB1(SCK)
PB2(MOSI)
PB3(MISO)
PD5
20
21
19
18
25
31
27
1
28
29
30
12
26
32
8
9
10
11
22
TX
GND
GND
SW1
SW1
1
2
{
GND
SW3
SW3
1
2
}
GND
SW2
SW2
1
2
¯_(ツ)_/¯
GND
SW4
SW4
1
2
;
GND
SW5
SW5
1
2
ENTER
GND
keyboard.js v87
12/02/2024 17:58
Sheet: 1/1