

Code A

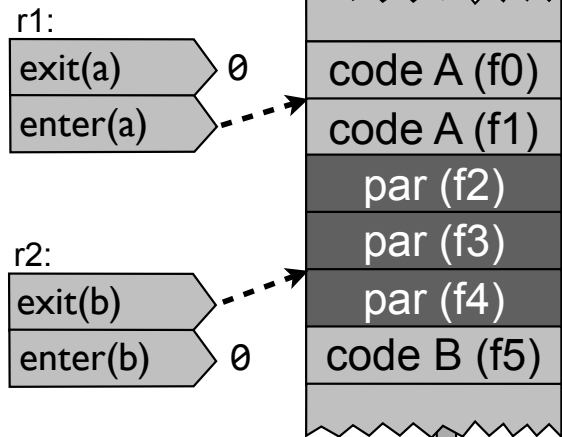
#pragma omp parallel "b"

Code B

#pragma omp parallel "c"

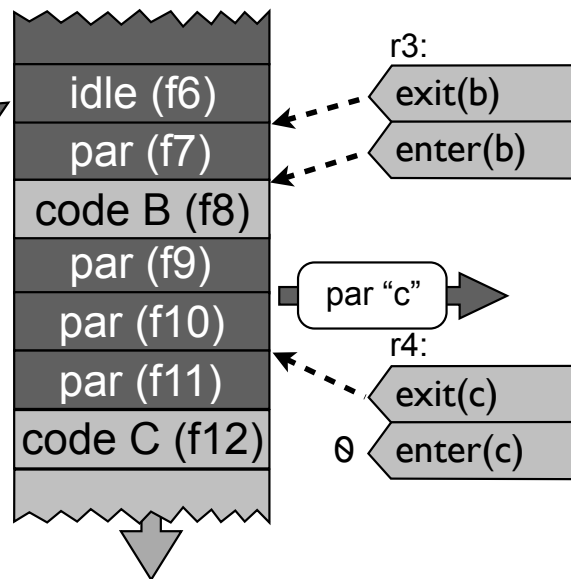
Code C

Thread 1



ompt_frame_t at ancestor level 0 → r2
ompt_frame_t at ancestor level 1 → r1

Thread 2



ompt_frame_t at ancestor level 0 → r4
ompt_frame_t at ancestor level 1 → r3
ompt_frame_t at ancestor level 2 → r1